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## Title word cross-reference

[LDP10]. **Boosting** [CCL$^+$18, KAH18b, ZLLX15, khR$^+$18]. **Booth** [JHQL16, LQW$^+$17, VALK19]. **Booting** [Cha14]. **Borrowing** [CCAM14]. **Both** [LXW$^+$19, WHC$^+$15b, ZHM$^+$19]. **Bound** [BMT14, Bin15, Fuj11, WLZ10]. **Boundary** [LKLT12]. **Boundary-Recognition-Free** [LKLT12]. **Bounded** [CCK10, HRK17, SC18, XXBL17, XLW14, ZY12]. **Bounding** [Fuj11]. **Bounds** [FJA$^+$17, FS10, HCCG10, ISC15, MRD19, RMB$^+$13, ST18a, Tse12, TKL$^+$14, YLH10]. **Box** [BTBB14, MKRM11, SWF$^+$19]. **Boxes** [MM17, ST18a]. **BPR** [YKK$^+$15]. **Branch** [BMT14, DEE17, Fuj11]. **Branch-and-Bound** [Fuj11]. **Branching** [STR15]. **Breakpoint** [MSPK12]. **Breakthrough** [CVGZ15]. **Bridging** [FD16, TLL12]. **Brief** [Liu11]. **Broadcast** [BS14, GYC$^+$16, KDEV19, KH10, LTP$^+$14, LWF13, PSM17, QSYS16, WXS12, WQZ$^+$16, XJW$^+$16, ZGY13, ZHW15]. **Broadcasting** [KS10a, YMTV14]. **Broadcasts** [LK15a]. **Broadside** [Pom12c, Pom12b, Pom13a, Pom13b, Pom14, Pom15a, Pom16a]. **Brokerage** [CWTT13, LKLT12]. **Bruijn** [MG16, YMAG17]. **BRW** [CMLRHS13]. **BSM** [YMTV14]. **BTI** [GBA18a]. **BTI-Induced** [GBA18a]. **Bubble** [MWLJ15, WSL$^+$18]. **Buddy** [PCLN15]. **Budget** [AF14]. **Budgeting** [PKC$^+$17, WZM$^+$16, WSL$^+$18, WTZ$^+$19, ZR15a]. **Buffer** [Ano13f, CLZ19, LSK13, LKLM15, LKBS16, MBD11, MD13, MBGS10, OGH$^+$14, PPND17, WW16, OGH$^+$14]. **Buffer-Aware** [LSK13]. **Bufferless** [KC14, ZGY14]. **Buffers** [CVMA10, DKLB15, HGCT13]. **Bugs** [FM19, ZYW$^+$16]. **Build** [PDXZ13, WSXZ13]. **Building** [AHNT16, AVS$^+$14, LFH$^+$16, LZZ$^+$17b, SWZG15, TMS$^+$14]. **Building-Block** [TMS$^+$14]. **Buildings** [WZY16]. **Built** [AK16, LLW$^+$17, Pom14]. **Built-In** [AK16, Pom14]. **Built-Off** [LLW$^+$17]. **Bulk** [SBI12, YZH$^+$15, ZWD$^+$16]. **Bullet** [Ano10g]. **Burst** [HXVQ15, NL15b, NL16a, RV13, ASE17]. **Burstiness** [CMS10]. **Bus** [CYA13, EE17, HHLK12, RVL$^+$14]. **Bus-Based** [CYA13]. **Buses** [BPC12, ST12]. **Buy** [SBI12]. **Buy-at-Bulk** [SBI12]. **BWLOCK** [YAGB17]. **By-Passing** [YKK$^+$15]. **Bypass** [KRP18, MMB14]. **Bypassing** [PSND16, SZL$^+$16]. **Byte** [CCC$^+$18, RV13, SBB18]. **Byte-addressable** [CCC$^+$18]. **Byzantine** [DCK16, KDEV19, LLKA19, VCB$^+$13]. **Byzantine-Resilient** [KDEV19]. **ByzCast** [KDEV19].

**C** [CZS$^+$19, KLJ$^+$14, MF14, RWC18]. **C-Lock** [KLJ$^+$14]. **C1G2** [SDZ15]. **CA** [SBB18]. **CA-Based** [SBB18]. **CABA** [MSKRJ17]. **CABE** [XHX$^+$17]. **CACC** [CWX$^+$14]. **Cache** [AK19, AYC16, ACW$^+$11, AGFM11, ADC11, BLN$^+$15, CVPS19, CWX$^+$14, CDQB15, CYCC11, CA12b, CKD$^+$17, CLCS19, DPS11, DW10, EF12, FSGAB$^+$16, GKD$^+$17, GGFPG15, HHM11, HK16, HK17, HK15b, HZX$^+$14, IPS17, JAKD18, JSC$^+$19, JZLD10, JKY10, KIJ14, KSEG15, KS14, KLK17, KCL19, KASZ13, KKC15b, KAQC14, KA19, LYH11, LKJ15, LK15b, LWKA15, LKLM15, LKBS16, LHL$^+$15a, LZZ16, LCLC19, MWLJ15, MHK15, MAD14, MMAC19, OOD$^+$17, PBV11, PCZB11, RM15a, RCFP$^+$12, RXC$^+$15, SV18, SZZ$^+$19, SYK14, SZL$^+$16, TAH$^+$16, VKS$^+$16, VYEB17, WMW12, WLT$^+$16, YMG15, ZGR13, ZDYZ13, ZDYZ14, KMC17]. **Cache-Aware** [IPS17]. **Cache-Based** [DW10, KASZ13, MMAC19]. **Cache-Coherent** [MWLJ15]. **Cache-Enabled** [LZZ16]. **Caches** [AVG$^+$15, CXLL16, CFMA19, CRG$^+$13, FKMK16, HWZ$^+$17, KIJ14, LLX$^+$17, MD16,

**Microprocessor-Based** [SDP+12]. **Microprocessors** [EGVFC+12, FRB+18, KKC15b, MTGM12, MMTM15, OPV+17, SNM16]. **Microprotocol** [VECD13]. **Midpoint** [Joh17]. **Midpoints** [JLMP11]. **Migrating** [YLH10]. **Migration** [GBO+16, HV14a, LR18, LLX+17, LCLC19, MRW+15, RSNK17, RSN+18, TKL+14, WJM15, XLL+14, ZRS+16]. **Migrations** [LWH+16]. **MIHST** [BCSR14]. **Miller** [LT14]. **Millimeter** [DCY+13]. **Millimeter-Wave** [DCY+13]. **MIMD** [NVB16]. **Mimicking** [YGS15]. **MIMO** [CWY13, LZ15]. **Min** [GLH+19, XLL15]. **Min-Entropy** [GLH+19]. **Min-Max** [XLL15]. **Mini** [FZL+14]. **Mini-Rank** [FZL+14]. **Miniaturized** [GJ14]. **MiniBench** [YEG+15]. **Minimal** [ARH+18, EDL+14, HHKW12, MKLW14, RBR13, SBM15, SJD+18, SG12, ZBW17]. **Minimal-Memory** [HHKW12]. **MINIME** [DSKH15]. **Minimization** [BCTV19, Cha14, LHL+15a, LGMP10, XSR15, ZLG+15]. **Minimizing** [DSG+19, HT12, RSNK17, YCZ10, ZC13]. **Minimum** [DVUS14, FEP+12, GPN11, KHPP16, KLT16, LYY16, LSX13, QPG10, SG13, WTY+14, WCLY16, YLY15b]. **Mining** [HBR11, RGK15, SKC+14]. **Minus** [BT16]. **Miss** [LK15b, SV18]. **Misses** [RXC+15]. **Missing** [LLM+15]. **Mission** [LXJD15]. **Mitchell** [KBO+19, LJ15]. **Mitchell-Based** [LJ15]. **Mitigate** [GBA18a, JSC+19, RBG14]. **Mitigating** [CJK19, GBGI18, HWK15, KCY18, KCKL19]. **Mitigation** [AS12, MLS19, SKH16, dOPSR16]. **Mixed** [ABA07, BBD+12, BM13b, BDBB18, CAbZM18, CGL+18, GGA+17, GIW18, LRP+18, LGS+18, RMB+12, ZCK19]. **Mixed-Critical** [BM13b]. **Mixed-Criticality** [BBD+12, BDBB18, CAbZM18, CGL+18, LRP+18, LGS+18].

**Mixed-Precision** [ZCK19]. **Mixed-Radix** [ABA07, GIW18]. **Mixed-Time-Criticality** [GGA+17]. **Mixing** [Ged14]. **Mixture** [HFG+17, RF14]. **Mixture-of-Experts** [RF14]. **MLC** [NL19, CK11, CCC+17, CKD+17, GWM+17, LK14, ZZJ+19]. **MLC-Based** [CK11]. **MLC/SLC** [CCC+17]. **MM*** [CH11]. **MMU** [EKJ+10]. **Mobile** [BWV15, Cao12, CS15, CKH15, FFCB14, GDY15, GCF+16, HZL+16, KCRG15, KkC15a, KKC17, LLCH13, LH16, LSX13, LHH14a, LCT11, LSL15, LZZ+17b, MCC12, RSN+18, SMP16, SKM14, SD13, TH11, TZL+14, WLYY16, XWY10, XWH14, YCCJ15, YCLH16, ZWL15, ZLW+17, ZMY11, ZY12, ZCC+14, ZLYS15]. **Mobile-Cloud** [LSL15]. **Mobility** [AEKT15, ASTU10, OPZ15, SKPC15]. **Möbius** [Kůr12]. **Modal** [LT15]. **Mode** [AHK10, HZ11, JRS+15, QLH+16, SWWC11, ZLBB19, PVKA14, DFP+13]. **MoDe-X** [PVKA14]. **Model** [Ano10f, BFR+15, CMB13, CMS10, CH11, CYHC14, CCH15b, CC16, Dar15, DAPS14, FLS16, GLH+19, HXVQ15, HSH+10, HK13b, HWL+14, JRJ+18, JAD+18, KGP15, LH12a, LWKA15, LK18, LLZ+17, LWZ18, LH12b, LKT13, MD16, MTFK19, MY10, NH10, NS13, SMA19, SAR+11, SIB13, SVAB14, TLP17, Tho15, Tsa13, WJL+12, WP16, WHYS16, XKT+15, XYHD17, YLL16, ZLBB19, ZYC16, ZYY10, ZZM+15]. **Model-Based** [XKT+15]. **Model-Driven** [CMS10, SAR+11]. **Model-Free** [WP16]. **Modeling** [ADP+15, BTBB14, BG12, CJSM17, CA12b, CSW+15, DMXY14, DDNT19, GBA+18b, HL10b, IPS17, KM11, KMLH11, LZZZ13, LMB+16, MBM11, MHH+17, MKM14, NS13, RKT19, SC11, VED+16, WZCG16, XYF+15, YZF+10, ZGB+15]. **Models** [AFH+10, BD15, BGM+13, CYA13, CCD12, FNS16, HFG+17, LK10, LCY+13, LHTG15, SD14, YMT13, YHT+16, ZYL15, ZRL15].

[MCM16, NL16b, PROM15]. **Symbolic** [BMZ17, NS13, PNKI13, ZJH$^+$14]. **Symmetric** [HC13b, MM17, ZWYY15]. **SymPLFIED** [PNKI13]. **Synchronization** [BBB$^+$17, HWZ$^+$12, KLJ$^+$14, LK18, TFCY16, WZLS16, ZJXL11]. **Synchronization-Aware** [HWZ$^+$12]. **Synchronization-Based** [BBB$^+$17]. **Synchronized** [PRGBSAC19]. **Synchronizer** [Zot10]. **Synchronizing** [HZ11]. **Synchronous** [LYL$^+$19]. **Syndrome** [Red14]. **Synergistic** [Pan16]. **Synergizing** [LHH14b]. **Synergy** [LHTG15]. **Synthesis** [AR12, BM13b, CBZ14, DYJ19, DSR15, FSR$^+$18, GAFN15, GM12, IB10, JWC12, JRC14, KK18, LH11, MY10, MIS$^+$14]. **Synthesizer** [DSKH15]. **Synthesizing** [RBIQ15]. **Synthetic** [GJ14]. **SyRA** [VSC$^+$19]. **System** [ADI11, ASM$^+$16, AHNT16, AE11, Ano11f, AC11, BKH$^+$13, BS15, BBP$^+$13, BJ12, BS10, BM13b, BSM$^+$14, CMS10, CXZ13, CCO$^+$14, CWZ11, CHLT14, CCC$^+$18, CLCS19, CH14, DKH$^+$13, DSB13, DFP$^+$13, DALD18, DCL$^+$11, FLP$^+$13, IBH$^+$13, JZLD10, KM11, KGC14, KP15, KCE$^+$18, KBP13, LK10, LYB15, LKK$^+$19, LSW15, LLD$^+$16, LLS$^+$16, LYJ$^+$18, LVF19, LSGZ16, MBM11, MMCS18, MLE14, MKM14, NLP$^+$14, PP11, PBT13, QWB$^+$13, ROGHNB$^+$18, RM15c, SOM$^+$13, SPC$^+$16, SCZ$^+$16, SWZG11, SLS$^+$12, SN16, SKM14, TKT16, VSC$^+$19, WZLX12, WLK15, WWY$^+$16, WP16, WSXZ13, XZL$^+$19, YY10, YSZ$^+$14, YXZZ14, ZGG$^+$16, ZZ10, ZMS13]. **System-Dependent** [JCY$^+$13]. **System-Level** [AE11, Ano11f, BS10, BM13b, LK10, WP16, ZMS13]. **System-on-Chip** [BKH$^+$13, LYJ$^+$18]. **System-Wide** [KCE$^+$18, SKM14]. **Systematic** [BGM$^+$13, DRC14, PWTS16, PC16, Red11, SZW$^+$16]. **SystemC** [PFGB14]. **Systems** [AJH15, AXS$^+$10, AEP18, AE11, AFH$^+$10, ARGT14, Ano13g, AISA16, BBPQ15, BKPMC13, BDM$^+$19, BIP$^+$17, BBB16, BMS11, BM13b, BKP16, BDL$^+$13, BGRH15, BDBB18, CAbZM18, CW10, CK11, CHH$^+$13, Cha14, CZ14, CHC$^+$15, CHTD19, CCH11, CS11b, CYCC11, CQW$^+$15, CCY$^+$16, CJA$^+$16, CvdBC18, CLM$^+$19, CQ14, CDK$^+$18, CK15, CH14, CWCS15, DZD$^+$16, DSB13, DYCG16, DLC$^+$13, DSY$^+$15, DW10, DKK16, EKJ$^+$10, FFCB14, FYSK14, FSR$^+$18, GCD$^+$11, GGSPM18, GV15, GGA$^+$17, GWM$^+$17, HRK17, HWZ$^+$12, HNV19, HWS$^+$17, HWSX17, HS18, Hie13, HCH15, HTC13, HHLK12, HWL$^+$14, HZW$^+$12, HT12, HWSN15, HLA$^+$17, HHW$^+$18, HLC$^+$19, JAJK15, JKY10, JKD$^+$19, KS10a, KIJ14, KMC17, KMLH11, KAH19, KCRG14, KLJ$^+$14, KH18, KAQC14, KA19, Kůr12, LBSK17, LSK13, LSSE15, LXJD15, LXDV17, LS10a, LSS13, LOX$^+$13, LLL15, LCH$^+$15, LTVL15, LSG$^+$18, LcZLG19, LH12b, LKT13, LZZZ13, LMB$^+$16, LLW$^+$11, LXZ$^+$15]. **Systems** [LZZV16, LZZ16, LHH17, LJVJ18, MW10, MJWT16, MS15, MBB$^+$17, MG11a, MUMB11, MBSSA19, MNK11, MCXZ18, MTBB10, MW13, NL16a, NL16b, NVB16, OGPK14, OKC13, PKC$^+$17, PDXZ13, Pan16, PCLN15, PBL16, PC10, PBE17, QZL$^+$16, QLH$^+$16, RVC$^+$15, RF14, RDEN10, RSU17, RBR13, SSKL16, SDP$^+$12, SVD18, SIVH16, SMN$^+$17, SSW12, SL13, SLC15a, SZG$^+$18, SKM14, SL14b, ST11a, SD13, SYH17, SWZG15, TLZV11, TB15, TSK16, TRA18, Tsa13, TFCY16, TS11, VSC$^+$19, VSF$^+$17, WS14, WLC$^+$15, WZM$^+$16, WSZ$^+$16, WSL$^+$18, WTZ$^+$19, WHL17, WhCCC12, WA10, WLZ10, WJF$^+$11, XDZ11, XLX$^+$14, XTW15, XSR15, YLY$^+$15a, YCLH16, YHC19, YPB$^+$16, YYW$^+$16, YTD$^+$17, YHV13, ZLBB19, ZWW$^+$16, ZFJ$^+$17, ZFHC19, ZQZ$^+$19, ZZ19, ZV14, ZZM$^+$15, ZL15, ZHM$^+$19, ZXX$^+$14, ZLLX15, ZCR16]. **Systems-on-Chip** [NVB16, RVC$^+$15].

[CS15]. **Thrashing** [LCLC19]. **Thread** [HGCT13, KK10, LKJ15, LR18, LLX$^{+}$17, LY18, RCM$^{+}$16, TLGM17, XLL$^{+}$18, ZJS14]. **Thread-Aware** [ZJS14]. **Thread-Level** [XLL$^{+}$18]. **Threaded** [TLGM17]. **Threading** [CvdBC18, MS15, RCC14]. **Threat** [YZF$^{+}$10]. **Three** [CNH13, CCE$^{+}$18, Hia16, Hia17, JWH$^{+}$15, MKLW14, PC16, SG13, Ste14]. **Three-Dimensional** [CCE$^{+}$18, JWH$^{+}$15, MKLW14]. **Three-Moduli** [Hia16, Hia17]. **Three-Vertex** [SG13]. **Three-Way** [CNH13]. **Threshold** [Cil11, FSGAB$^{+}$16, MHK15, RMKR12, STE17]. **Throttling** [LK16a]. **Throughput** [AFC10, CVGZ15, CA12b, FFISC13, HGCT13, KAH18a, KHZ17, LMC$^{+}$15, LCL17, OMFH14, PRM16, RL13, VC10, WSL$^{+}$18, YM11, ZZYZ14]. **Throughput-Efficient** [YM11]. **Throughput-Optimal** [RL13]. **Throwbox** [LYT$^{+}$16]. **Throwbox-Assisted** [LYT$^{+}$16]. **Throwboxes** [LYT$^{+}$16]. **Tianhe** [XYF$^{+}$15]. **Tianhe-2** [XYF$^{+}$15]. **Tier** [LZW$^{+}$15]. **Tiered** [SRHC12]. **Tight** [ST18a]. **Tighter** [YLH10]. **Tightly** [DMK$^{+}$15]. **Tightly-Coupled** [DMK$^{+}$15]. **Tiled** [KPS$^{+}$17]. **Tiling** [QLH$^{+}$16]. **Time** [ABH$^{+}$13, AF14, ADP$^{+}$15, ABEP16, AEP18, AE11, ASBdS16, BM13a, BBK10, BBD$^{+}$12, BBP$^{+}$13, BG12, Bin15, BBB16, BDB18, BPC12, BGRH15, CWF14, CBB19, CW10, Cha14, CWZ11, CYCC11, CCC15, CGL$^{+}$18, CQ14, CLR13, CCAM14, CYL$^{+}$14, DYW15, DCCK17, DZD$^{+}$16, DA12, DGC$^{+}$15, DZ10, DKK16, EE17, FM16, GKB$^{+}$10, GCLC11, GPRS17, GGA$^{+}$17, GCAG16, GSF$^{+}$10, HB11, HXVQ15, HWZ$^{+}$12, HV12, HCH15, HK16, HHLK12, HWL$^{+}$14, HXL11, HCC$^{+}$12, HV14b, HC17, HGW$^{+}$17, HZW$^{+}$12, HCZW13, IBH$^{+}$13, JAJK15, JSH$^{+}$17, JWWZ16, KS10a, KSS12, KM11, KRR$^{+}$18, KGP15, KTA$^{+}$14, KMC17, KLLK11, KCE$^{+}$18, KAQC14, KDEV19, KT12, LYH11, LR13, LSC11, LHC$^{+}$14, LAAM11, LMC$^{+}$15, LKLK13, LSSE15, LXJD15, LXDV17, LC16a, LWF$^{+}$17, LLW$^{+}$11, LLM$^{+}$15, LXZ$^{+}$15, LGS$^{+}$18, LMB13, MBD$^{+}$17, MUMB11, MAHD18, MBSSA19, MFG14, MKM14, MW13, NRG15, NZLK14, NZC11, OHCK17, OCK17]. **Time** [OPZ15, PTD$^{+}$12, PC10, PMH$^{+}$14, RHC$^{+}$14, RF14, RM15a, RLX15, SRCbL$^{+}$15, SCJ$^{+}$16b, TB15, TH11, TFCY16, TCHL18, THGT13, TKT16, VK15, WXS12, WBZ$^{+}$15, WLY$^{+}$14, WA10, WLZ10, WSXZ13, WZ14, XWLX17, Yam10, YPB$^{+}$16, YYW$^{+}$16, YHV13, YTM16, YAGB17, ZICL12, ZLBB19, ZD13, ZLY15, ZZ19, ZYY18, ZHM$^{+}$19, ZQQ11, ZCYX15]. **Time-Aware** [WSXZ13]. **Time-Borrowing** [CCAM14]. **Time-Dependent** [LR13]. **Time-Domain** [HXVQ15]. **Time-Driven** [BM13a, RM15a]. **Time-Efficient** [LLM$^{+}$15, LXZ$^{+}$15]. **Time-Evolving** [HCZW13, JWWZ16]. **Time-Memory** [ASBdS16]. **Time-Multiplexed** [GCLC11]. **Time-Predictable** [WA10]. **Time-Series** [ZLY15]. **Time-Slot** [RLX15]. **Time-Triggered** [MAHD18]. **Time/Just** [JAJK15]. **Time/Utility** [KM11]. **Timed** [LH12b]. **Timely** [STR15]. **Times** [YLH10, YLA$^{+}$15]. **Timetables** [DDNP11]. **Timing** [CYCC11, CCAM14, GV14, JRJ$^{+}$18, KTA$^{+}$14, LR16, MMAC19, MMP13, SBP16, SCJ$^{+}$16b, VTA16, YyHL11, ZM17]. **Timing-Aware** [CYCC11, SBP16]. **TLB** [KLK17, SMAR$^{+}$19]. **TLM** [BFP11, PFGB14]. **TMDTO** [MSS$^{+}$18]. **Toeplitz** [PCHS16, RM15b, CNH13, CLL$^{+}$14, HF15, HMNN12, HN13, PCHS18]. **Toffoli** [GM12]. **Toggle** [KCY18]. **Toggle-BasedX-Masking** [KCY18]. **Tolerance** [BWCW15, BKP16, DCK16, EYBK15, GV15, HHCH11, JWH$^{+}$15, JKJ$^{+}$10, KTA$^{+}$14, MSC12, MBSSA19, NLRB17, PPP13, RKR15, RRS$^{+}$16, RZPX19,

# References

**Agrawal:2019:SSN**

[AAR19] A. Agrawal, A. Ankit, and K. Roy. SPARE: Spiking neural network acceleration using ROM-embedded RAMs as in-memory-computation primitives. *IEEE Transactions on Computers*, 68(8):1190–1200, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Albader:2016:EDH**

[AB16] B. Albader and B. Bose. Edge disjoint Hamiltonian cycles in Gaussian networks. *IEEE Transactions on Computers*, 65(1):315–321, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anantha:2007:MRG**

[ABA07] M. Anantha, B. Bose, and B. F. AlBdaiwi. Mixed-radix gray codes in Lee metric. *IEEE Transactions on Computers*, 56(10):1297–1307, October 2007. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4302703. See comments [Jha13].

**Abdelfattah:2017:DAE**

[ABB17] Mohamed S. Abdelfattah, Andrew Bitar, and Vaughn Betz. Design and applications for embedded networks-on-chip on FPGAs. *IEEE Transactions on Computers*, 66(6):1008–1021, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7676325/.

**Aminifar:2016:ADR**

[ABEP16] A. Aminifar, E. Bini, P. Eles, and Z. Peng. Analysis and design of real-time servers for control applications. *IEEE Transactions on Computers*, 65(3):834–846, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Adikari:2013:IAT**

[ABH+13] J. Adikari, A. Barsoum, M. A. Hasan, A. H. Namin, and C. Negre. Improved area-time tradeoffs for field multiplication using optimal normal bases. *IEEE Transactions on Computers*, 62 (1):193–199, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Alizadeh:2015:SFD**

[ABSK15] B. Alizadeh, P. Behnam, and S. Sadeghi-Kohan. A scalable formal debugging approach with auto-correction capability based on static slicing and dynamic ranking for RTL datapath designs. *IEEE Transactions on Computers*, 64(6):1564–1578, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Arnold:2011:RCL**

[AC11] Mark G. Arnold and Sylvain Collange. A real/complex logarithmic number system ALU. *IEEE Transactions on Computers*, 60 (2):202–213, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Atalla:2013:BPA**

[ACGP13] Shadi Atalla, Davide Cuda, Paolo Giaccone, and Marco Pretti. Belief-propagation-assisted scheduling in input-queued switches. *IEEE Transactions on Computers*, 62(10):2101–2107, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Amat:2016:FED**

[ACM+16] E. Amat, A. Calomarde, F. Moll, R. Canal, and A. Rubio. Feasibility of embedded DRAM cells on FinFET technology. *IEEE Transactions on Computers*, 65(4):1068–1074, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Akleylek:2012:PMC**

[ACO12] Sedat Akleylek, Murat Cenk, and Ferruh Ozbudak. On the polynomial multiplication in Chebyshev form. *IEEE Transactions on Computers*, 61(4):584–587, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See comments [PCHS14].

**Alameldeen:2011:ACD**

[ACW+11] Alaa R. Alameldeen, Zeshan Chishti, Chris Wilkerson, Wei Wu, and Shih-Lien Lu. Adaptive cache design to enable reliable low-voltage operation. *IEEE Transactions on Computers*, 60(1):50–63, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ammari:2010:SCM**

[AD10] H. M. Ammari and S. K. Das. A study of $k$-coverage and measures of connectivity in 3D wireless sensor networks. *IEEE Transactions on Computers*, 59 (2):243–257, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5313797`.

**Ammari:2012:CCC**

[AD12] Habib M. Ammari and Sajal K. Das. Centralized and clustered $k$-coverage protocols for wireless sensor networks. *IEEE Transactions on Computers*, 61 (1):118–133, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ashraf:2013:SFR**

[AD13] Rizwan A. Ashraf and Ronald F. DeMara. Scalable FPGA refurbishment using netlist-driven evolutionary algorithms. *IEEE Transactions on Computers*, 62 (8):1526–1541, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Abawajy:2014:DRA**

[AD14] J. H. Abawajy and M. M. Deris. Data replication approach with consistency guarantee for data grid. *IEEE Transactions on Computers*, 63(12):2975–2987, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Alzahrani:2016:FOD**

[AD16] Ahmad Alzahrani and Ronald F. DeMara. Fast online diagnosis and recovery of reconfigurable logic fabrics using design disjunction. *IEEE Transactions on Computers*, 65(10):3055–3069, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ashby:2011:SBC**

[ADC11] Thomas J. Ashby, Pedro Diaz, and Marcelo Cintra. Software-based cache coherence with hardware-assisted selective self-invalidations using bloom filters. *IEEE Transactions on Computers*, 60(4):472–483, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Adikari:2011:HBT**

[ADI11] Jithra Adikari, Vassil S. Dimitrov, and Laurent Imbert. Hybrid binary-ternary number system for elliptic curve cryptosystems. *IEEE Transactions on Computers*, 60(2):254–265, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Adikari:2012:FHA**

[ADJ12] Jithra Adikari, Vassil S. Dimitrov, and Kimmo U. Jarvinen. A fast hardware architecture for integer to $\tau$NAF conversion for Koblitz curves. *IEEE Transactions on Computers*, 61(5):732–737, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Allyson:2019:SOI**

[ADJG19] F. B. Allyson, M. L. Danilo, S. M. José, and B. C. Giovanni. Sherlock $N$-overlap: Invasive normalization and overlap coefficient for the similarity analysis between source code. *IEEE Transactions on Computers*, 68(5):740–751, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Al-Dubai:2010:TOB**

[ADOKM10] A. Y. Al-Dubai, M. Ould-Khaoua, and L. M. Mackenzie. Trade-offs between latency, complexity, and load balancing with multicast algorithms. *IEEE Transactions on Computers*, 59(2):159–173, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184806`.

**Alawneh:2015:HRT**

[ADP+15] S. Alawneh, R. Dragt, D. Peters, C. Daley, and S. Bruneau. Hyper-real-time ice simulation and modeling using GPGPU. *IEEE Transactions on Computers*, 64(12):3475–3487, December 2015. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Aminzadeh:2011:CSS**

[AE11] Soheil Aminzadeh and Alireza Ejlali. A comparative study of system-level energy management methods for fault-tolerant hard real-time systems. *IEEE Transactions on Computers*, 60(9): 1288–1299, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5710892.

**Amrouch:2019:EVO**

[AEGH19] H. Amrouch, S. B. Ehsani, A. Gerstlauer, and J. Henkel. On the efficiency of voltage overscaling under temperature and aging effects. *IEEE Transactions on Computers*, 68(11):1647–1662, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Akbas:2015:LWS**

[AEKT15] M. I. Akbas, M. Erol-Kantarci, and D. Turgut. Localization for wireless sensor and actor networks with meandering mobility. *IEEE Transactions on Computers*, 64(4):1015–1028, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Aminifar:2018:OME**

[AEP18] Amir Aminifar, Petru Eles, and Zebo Peng. Optimization of message encryption for real-time applications in embedded systems. *IEEE Transactions on Computers*, 67(5):748–754, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8125122/.

**Ahmed:2014:PBA**

[AF14] Safayet Ahmed and Bonnie H. Ferri. Prediction-based asynchronous CPU-budget allocation for soft-real-time applications. *IEEE Transactions on Computers*, 63(9):2343–2355, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Alimohammad:2010:UAA**

[AFC10] A. Alimohammad, S. F. Fard, and B. F. Cockburn. A unified architecture for the accurate and high-throughput implementation of six key elementary functions. *IEEE Transactions on Computers*, 59 (4):449–456, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5313801.

**Anand:2010:GRC**

[AFH+10] M. Anand, S. Fischmeister, Y. Hur, Jesung Kim, and Insup Lee. Generating reliable code from hybrid-systems models. *IEEE Transactions on Computers*, 59(9): 1281–1294, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453343`.

**Amaru:2016:SCA**

[AGCD16] Luca Amarú, Pierre-Emmanuel Gaillardon, Anupam Chattopadhyay, and Giovanni De Micheli. A sound and complete axiomatization of majority-logic. *IEEE Transactions on Computers*, 65(9): 2889–2895, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ansari:2011:MSU**

[AGFM11] Amin Ansari, Shantanu Gupta, Shuguang Feng, and Scott Mahlke. Maximizing spare utilization by virtually reorganizing faulty cache lines. *IEEE Transactions on Computers*, 60 (1):35–49, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ahmadi:2010:LCC**

[AH10] O. Ahmadi and F. R. Henríquez. Low complexity cubing and cube root computation over $F_3^m$ in polynomial basis. *IEEE Transactions on Computers*, 59(10): 1297–1308, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374372`.

**Attiya:2013:CPS**

[AH13] Hagit Attiya and Eshcar Hillel. The cost of privatization in software transactional memory. *IEEE Transactions on Computers*, 62(12):2531–2543, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Antelo:2012:GEI**

[AHI12] Elisardo Antelo, David Hough, and Paolo Ienne. Guest Editors' introduction: Special section on computer arithmetic. *IEEE Transactions on Computers*, 61 (8):1057–1058, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Attiya:2010:PME**

[AHK10] H. Attiya, D. Hay, and I. Keslassy. Packet-mode emulation of output-queued switches. *IEEE Transactions on Computers*, 59(10): 1378–1391, October 2010.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374375.

**Ambusaidi:2016:BID**

[AHNT16] Mohammed A. Ambusaidi, Xiangjian He, Priyadarsi Nanda, and Zhiyuan Tan. Building an intrusion detection system using a filter-based feature selection algorithm. *IEEE Transactions on Computers*, 65(10): 2986–2998, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Asadi:2016:GEI**

[AISA16] H. Asadi, P. Ienne, and H. Sarbazi-Azad. Guest Editors' introduction: Special section on emerging memory technologies in very large scale computing and storage systems. *IEEE Transactions on Computers*, 65 (4):1006–1009, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ahn:2015:FTL**

[AJH15] J. Ahn, S. Jin, and J. Huh. Fast two-level address translation for virtualized systems. *IEEE Transactions on Computers*, 64(12): 3461–3474, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Azarderakhsh:2014:NDP**

[AK14] R. Azarderakhsh and K. Karabina. A new double point multiplication algorithm and its application to binary elliptic curves with endomorphisms. *IEEE Transactions on Computers*, 63(10): 2614–2619, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**AlRidhawi:2015:QBC**

[AK15] Y. Al Ridhawi and A. Karmouch. QoS-based composition of service specific overlay networks. *IEEE Transactions on Computers*, 64(3):832–846, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Acevedo:2016:CLC**

[AK16] O. Acevedo and D. Kagaris. On the computation of LFSR characteristic polynomials for built-in deterministic test pattern generation. *IEEE Transactions on Computers*, 65 (2):664–669, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Agarwal:2019:ILN**

[AK19] S. Agarwal and H. K. Kapoor. Improving the lifetime of non-volatile cache by write restriction. *IEEE Transactions on Computers*, 68(9):1297–1312, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ahn:2013:MTO**

[AKJ+13] Jung-Sang Ahn, Dongwon Kang, Dawoon Jung, Jin-Soo Kim, and Seungryoul Maeng. m*-tree: An ordered index structure for NAND flash memory with adaptive page layout scheme. *IEEE Transactions on Computers*, 62(4):784–797, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ahn:2012:SSE**

[AKKH12] Jeongseob Ahn, Daehoon Kim, Jaehong Kim, and Jaehyuk Huh. Subspace snooping: Exploiting temporal sharing stability for snoop reduction. *IEEE Transactions on Computers*, 61(11):1624–1637, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Almurib:2014:SAD**

[AKL14] Haider A. F. Almurib, T. Nandha Kumar, and Fabrizio Lombardi. Scalable application-dependent diagnosis of interconnects of SRAM-based FPGAs. *IEEE Transactions on Computers*, 63(6):1540–1550, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Almurib:2018:ADI**

[AKL18] Haider A. F. Almurib, Thulasiraman Nandha Kumar, and Fabrizio Lombardi. Approximate DCT image compression using inexact computing. *IEEE Transactions on Computers*, 67(2):149–159, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7990539/`.

**Ashraf:2018:MCP**

[AKTB18] Imran Ashraf, Nader Khammassi, Mottaqiallah Taouil, and Koen Bertels. Memory and communication profiling for accelerator-based platforms. *IEEE Transactions on Computers*, 67(7):934–948, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8234629/`.

**Abbas:2014:EMC**

[ALBP14] Syed Mohsin Abbas, Soonyoung Lee, Sanghyeon Baeg, and Sungju Park. An efficient multiple cell upsets tolerant content-addressable memory. *IEEE Transac-*

*tions on Computers*, 63(8): 2094–2098, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Abid:2011:ICS**

[ALW11] Z. Abid, Ming Liu, and Wei Wang. 3D integration of CMOL structures for FPGA applications. *IEEE Transactions on Computers*, 60(4):463–471, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Adam:2016:CPP**

[ALZ16] Omer Y. Adam, Young Choon Lee, and Albert Y. Zomaya. Constructing performance-predictable clusters with performance-varying resources of clouds. *IEEE Transactions on Computers*, 65(9): 2709–2724, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anderson:2017:EMF**

[AMG17] Andrew Anderson, Servesh Muralidharan, and David Gregg. Efficient multibyte floating point data formats using vectorization. *IEEE Transactions on Computers*, 66(12):2081–2096, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7950938/`.

**Ammari:2014:IES**

[Amm14] H. M. Ammari. Investigating the energy sink-hole problem in connected $k$-covered wireless sensor networks. *IEEE Transactions on Computers*, 63(11): 2729–2742, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Aguilar-Melchor:2018:CBG**

[AMR18] Carlos Aguilar-Melchor and Thomas Ricosset. CDT-based Gaussian sampling: From multi to double precision. *IEEE Transactions on Computers*, 67(11): 1610–1621, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8295226/`.

**Andujar-Munoz:2015:DTT**

[AMVOS+15] F. J. Andujar-Munoz, J. A. Villar-Ortiz, J. L. Sanchez, F. J. Alfaro, and J. Duato. $N$-dimensional twin torus topology. *IEEE Transactions on Computers*, 64(10): 2847–2861, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2010:RL**

[Ano10a] Anonymous. 2009 reviewers list. *IEEE Transactions on Computers*, 59

(1):139–143, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5341480.

**Anonymous:2010:RJI**

[Ano10b] Anonymous. 7 reasons for joining the IEEE Computer Society. *IEEE Transactions on Computers*, 59(3): 431, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5398798.

**Anonymous:2010:CPS**

[Ano10c] Anonymous. Call for papers: Special section on dependable computer architecture. *IEEE Transactions on Computers*, 59(1):144, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2010:ICSa**

[Ano10d] Anonymous. IEEE Computer Society CSDA certification. *IEEE Transactions on Computers*, 59 (4):576, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5420343.

**Anonymous:2010:ICSb**

[Ano10e] Anonymous. IEEE Computer Society CSDP certification. *IEEE Transactions on Computers*, 59 (11):1584, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5586717.

**Anonymous:2010:NOP**

[Ano10f] Anonymous. New Online-Plus publication model beginning in 2010. *IEEE Transactions on Computers*, 59(8):1152, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5494720.

**Anonymous:2010:SBS**

[Ano10g] Anonymous. Silver Bullet Security Podcast series. *IEEE Transactions on Computers*, 59(3):432, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5398799.

**Anonymous:2011:AI**

[Ano11a] Anonymous. 2010 annual index. *IEEE Transactions on Computers*, 60(1):INDEX:1–INDEX:18, January 2011.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2011:ARL**

[Ano11b] Anonymous. 2010 annual reviewers list. *IEEE Transactions on Computers*, 60 (1):139–143, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2011:CPE**

[Ano11c] Anonymous. Call for papers: Energy efficient computing. *IEEE Transactions on Computers*, 60(10):1518, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2011:CPN**

[Ano11d] Anonymous. Call for papers: Networks-on-chip. *IEEE Transactions on Computers*, 60(10):1517, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2011:CPSa**

[Ano11e] Anonymous. Call for papers: Special section on computer arithmetic. *IEEE Transactions on Computers*, 60(6):910, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2011:CPSb**

[Ano11f] Anonymous. Call for papers: System-level design and validation of heterogeneous chip multiprocessors. *IEEE Transactions on Computers*, 60(10):1519, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2011:NTCa**

[Ano11g] Anonymous. New Transactions on Computers Essential Sets available. *IEEE Transactions on Computers*, 60(10):1520, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=6008542`.

**Anonymous:2011:NTCb**

[Ano11h] Anonymous. New Transactions on Computers Essential Sets available. *IEEE Transactions on Computers*, 60(11):1664, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=6029372`.

**Anonymous:2011:SC**

[Ano11i] Anonymous. Stay connected. *IEEE Transactions on Computers*, 60(9):1376, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5962405.

**Anonymous:2012:AI**

[Ano12a] Anonymous. 2011 annual index. *IEEE Transactions on Computers*, 61(1):WebOnly, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2012:RL**

[Ano12b] Anonymous. 2011 reviewers list. *IEEE Transactions on Computers*, 61 (1):139–144, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2012:Ca**

[Ano12c] Anonymous. Cover1. *IEEE Transactions on Computers*, 61(9):c1, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2012:Cb**

[Ano12d] Anonymous. Cover2. *IEEE Transactions on Computers*, 61(9):c2, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2012:Cc**

[Ano12e] Anonymous. Cover3. *IEEE Transactions on Computers*, 61(9):c3, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2012:Cd**

[Ano12f] Anonymous. Cover4. *IEEE Transactions on Computers*, 61(9):c4, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:TAI**

[Ano13a] Anonymous. 2012 TC annual index. *IEEE Transactions on Computers*, 62(1): not in print, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:TRL**

[Ano13b] Anonymous. 2012 TC reviewers list. *IEEE Transactions on Computers*, 62 (1):201–208, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:ITE**

[Ano13c] Anonymous. *IEEE Transactions on Emerging Topics in Computing. IEEE Transactions on Computers*, 62(3):628, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:CPN**

[Ano13d] Anonymous. Call for papers for new *Transactions* on Emerging Topics in Computing. *IEEE Transactions on Computers*, 62(2):416, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:IOA**

[Ano13e] Anonymous. IEEE open access publishing. *IEEE Transactions on Computers*, 62(3):627, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:RBA**

[Ano13f] Anonymous. Receiving buffer adaptation for high-speed data transfer. *IEEE Transactions on Computers*, 62(11):2278–2291, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2013:SDA**

[Ano13g] Anonymous. Survivable data aggregation in multiagent network systems with hybrid faults. *IEEE Transactions on Computers*, 62(10): 2054–2068, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2014:AI**

[Ano14a] Anonymous. 2013 annual index. *IEEE Transactions on Computers*, 63(1): not in print, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2014:RL**

[Ano14b] Anonymous. 2013 reviewers list. *IEEE Transactions on Computers*, 63 (1):256–262, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2015:IIT**

[Ano15a] Anonymous. 2014 index IEEE Transactions on Computers vol. 63. *IEEE Transactions on Computers*, 64(1): 1–30, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2015:RL**

[Ano15b] Anonymous. 2014 reviewers list. *IEEE Transactions on Computers*, 64 (1):295–302, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2016:IIT**

[Ano16a] Anonymous. 2015 index IEEE Transactions on Computers vol. 64. *IEEE Transactions on Computers*, 65(1): 1–34, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2016:RL**

[Ano16b] Anonymous. 2015 reviewers list. *IEEE Transactions on Computers*, 65 (1):334–342, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2017:IIT**

[Ano17a] Anonymous. 2016 index IEEE Transactions on Computers vol. 65. *IEEE Transactions on Computers*, 66(1): 1–37, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2017:RLa**

[Ano17b] Anonymous. 2016 reviewers list. *IEEE Transactions on Computers*, 66 (1):178–182, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2018:IIT**

[Ano18a] Anonymous. 2017 index *IEEE Transactions on Computers* vol. 66. *IEEE Transactions on Computers*, 67(1): 1–23, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2018:RL**

[Ano18b] Anonymous. 2017 reviewers list. *IEEE Transactions on Computers*, 67 (1):144–148, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2019:IIT**

[Ano19a] Anonymous. 2018 index *IEEE Transactions on Computers* vol. 67. *IEEE Transactions on Computers*, 68 (1):157–176, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Anonymous:2019:RL**

[Ano19b] Anonymous. 2018 reviewers list. *IEEE Transactions on Computers*, 68 (1):152–156, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**AbdelSalam:2011:TET**

[AO11] Hady S. AbdelSalam and Stephan Olariu. Toward efficient task management in wireless sensor networks. *IEEE Transactions on Computers*, 60(11): 1638–1651, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669286`.

**AbdelSalam:2012:TAS**

[AO12a] Hady S. AbdelSalam and Stephan Olariu. Toward adaptive sleep schedules for balancing energy consumption in wireless sensor networks. *IEEE Transactions on Computers*, 61(10): 1443–1458, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Akleylek:2012:MRR**

[AO12b] Sedat Akleylek and Ferruh Ozbudak. Modified redundant representation for designing arithmetic circuits with small complexity. *IEEE Transactions on Computers*, 61(3):427–432, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Alomair:2014:MTM**

[AP14] Basel Alomair and Radha Poovendran. E-MACs: Toward more secure and more efficient constructions of secure channels. *IEEE Transactions on Computers*, 63 (1):204–217, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Aswal:2012:BFD**

[APP12] Abhilasha Aswal, M. Ganesh Perumal, and G. N. Srinivasa Prasanna. On basic financial decimal operations on binary machines. *IEEE Transactions on Computers*, 61 (8):1084–1096, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Al-Qawasmeh:2015:PTA**

[AQPMS15] A. M. Al-Qawasmeh, S. Pasricha, A. A. Maciejewski, and H. J. Siegel. Power and thermal-aware workload allocation in heterogeneous data centers. *IEEE Transactions on Computers*, 64 (2):477–491, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Altun:2012:LSS**

[AR12] Mustafa Altun and Marc D. Riedel. Logic synthesis for switching lattices. *IEEE Transactions on Computers*, 61(11):1588–1600, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Atighehchi:2017:OTM**

[AR17] Kevin Atighehchi and Robert Rolland. Optimization of tree modes for parallel hash functions: A case study. *IEEE Transactions on Computers*, 66(9): 1585–1598, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7896557/.

**Andalam:2014:PFS**

[ARGT14] Sidharta Andalam, Partha S. Roop, Alain Girault, and Claus Traulsen. A predictable framework for safety-critical embedded systems. *IEEE Transactions on Computers*, 63(7):1600–1612, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Adj:2014:SRC**

[ARH14] G. Adj and F. Rodriguez-Henriquez. Square root computation over even extension fields. *IEEE Transactions on Computers*, 63(11): 2829–2841, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**AlTawy:2018:TCM**

[ARH+18] Riham AlTawy, Raghvendra Rohit, Morgan He, Kalikinkar Mandal, Gangqiang Yang, and Guang Gong. Towards a cryptographic minimal design: The sLiSCP family of permutations. *IEEE Transactions on Computers*, 67(9):1341–1358, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8305605/`.

**Azarderakhsh:2013:LCM**

[ARM13] R. Azarderakhsh and A. Reyhani-Masoleh. Low-complexity multiplier architectures for single and hybrid-double multiplications in Gaussian normal bases. *IEEE Transactions on Computers*, 62 (4):744–757, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Abdulrahman:2015:NRR**

[ARM15] E. A. H. Abdulrahman and A. Reyhani-Masoleh. New regular radix-8 scheme for elliptic curve scalar multiplication without precomputation. *IEEE Transactions on Computers*, 64 (2):438–451, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Abdulrahman:2016:HSH**

[ARM16] Ebrahim A. Hasan Abdulrahman and Arash Reyhani-Masoleh. High-speed hybrid-double multiplication architectures using new serial-out bit-level Mastrovito multipliers. *IEEE Transactions on Computers*, 65(6):1734–1747, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Avirneni:2016:UAP**

[ARS16] N. D. P. Avirneni, P. K. Ramesh, and A. K. Somani. Utilization aware power management in reliable and aggressive chip multi processors. *IEEE Transactions on Computers*, 65(3):979–991, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Amin:2010:HRM**

[AS10] Alaaeldin Amin and Waleed Shinwari. High-radix multiplier-dividers: Theory, design, and hardware. *IEEE Transactions on Computers*, 59 (8):1009–1022, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453337.

**Avirneni:2012:LOS**

[AS12] Naga Durga Prasad Avirneni and Arun K. Somani. Low overhead soft error mitigation techniques for high-performance and aggressive designs. *IEEE Transactions on Computers*, 61(4):488–501, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Avirneni:2014:CPA**

[AS14] Naga Durga Prasad Avirneni and Arun K. Somani. Countering power analysis attacks using reliable and aggressive designs. *IEEE Transactions on Computers*, 63(6):1408–1420, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Aysu:2016:PMH**

[AS16] Aydin Aysu and Patrick Schaumont. Precomputation methods for hash-based signatures on energy-harvesting platforms. *IEEE Transactions on Computers*, 65(9): 2925–2931, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Andrade:2016:LEP**

[ASBdS16] Ewerton R. Andrade, Marcos A. Simplicio, Paulo S. L. M. Barreto, and Paulo C. F. dos Santos. Lyra2: Efficient password hashing with high security against time-memory trade-offs. *IEEE Transactions on Computers*, 65(10):3096–3108, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Akram:2017:DOD**

[ASE17] Shoaib Akram, Jennifer B. Sartor, and Lieven Eeckhout. DEP+BURST: Online DVFS performance prediction for energy-efficient managed language execution. *IEEE Transactions on Computers*, 66(4):601–615, April 2017. CODEN

ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ahn:2016:FFK**

[ASM+16] J. Ahn, C. Seo, R. Mayuram, R. Yaseen, J. Kim, and S. Maeng. ForestDB: A fast key-value storage system for variable-length string keys. *IEEE Transactions on Computers*, 65 (3):902–915, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Andreev:2018:PSC**

[ASS+18] Artem Aleksandrovich Andreev, Arvind Sridhar, Mohamed M. Sabry, Marina Zapater, Patrick Ruch, Bruno Michel, and David Atienza. PowerCool: Simulation of cooling and powering of 3D MPSoCs with integrated flow cell arrays. *IEEE Transactions on Computers*, 67(1):73–85, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7967719/`.

**Akkaya:2010:DRN**

[ASTU10] K. Akkaya, F. Senel, A. Thimmapuram, and S. Uludag. Distributed recovery from network partitioning in movable sensor/actor networks via controlled mobility. *IEEE Transactions on Computers*, 59(2):258–271, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184822`.

**Altiparmak:2016:MMF**

[AT16] N. Altiparmak and A. S. Tosun. Multithreaded maximum flow based optimal replica selection algorithm for heterogeneous storage architectures. *IEEE Transactions on Computers*, 65 (5):1543–1557, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Alvarez:2015:HSC**

[AVG+15] L. Alvarez, L. Vilanova, M. Gonzalez, X. Martorell, N. Navarro, and E. Ayguade. Hardware–software coherence protocol for the coexistence of caches and local memories. *IEEE Transactions on Computers*, 64 (1):152–165, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Avresky:2013:GEI**

[Avr13] D. R. Avresky. Guest Editors' introduction: Special section on optimizing the cloud. *IEEE Transactions on Computers*, 62 (6):1058–1059, June 2013. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Andujar:2014:BTU**

[AVS+14] F. J. Andujar, J. A. Villar, J. L. Sanchez, F. J. Alfaro, and J. Duato. Building 3D torus using low-profile expansion cards. *IEEE Transactions on Computers*, 63(11):2701–2715, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Andujar:2016:ARD**

[AVS+16] Francisco J. Andújar, Juan A. Villar, José L. Sánchez, Francisco J. Alfaro, and José Duato. Adaptive routing for $N$-dimensional twin torus. *IEEE Transactions on Computers*, 65(12): 3780–3786, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Aubert:2013:OWF**

[AWFV13] Louis-Marie Aubert, Roger Woods, Scott Fischaber, and Richard Veitch. Optimization of weighted finite state transducer for speech recognition. *IEEE Transactions on Computers*, 62 (8):1607–1615, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Amari:2010:PAM**

[AXS+10] S. V. Amari, Liudong Xing, A. Shrestha, J. Akers, and K. S. Trivedi. Performability analysis of multistate computing systems using multivalued decision diagrams. *IEEE Transactions on Computers*, 59(10): 1419–1433, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374373`.

**Ahn:2016:PHC**

[AYC16] J. Ahn, S. Yoo, and K. Choi. Prediction hybrid cache: An energy-efficient STT-RAM cache architecture. *IEEE Transactions on Computers*, 65(3):940–951, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Baikov:2017:AID**

[Bai17] Nikita Baikov. Algorithm and implementation details for complementary error function. *IEEE Transactions on Computers*, 66 (7):1106–1118, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07792222-abs.html`.

**Bartkewitz:2016:LPL**

[Bar16] Timo Bartkewitz. Leakage prototype learning for profiled differential side-channel cryptanalysis. *IEEE Transactions on Computers*, 65 (6):1761–1774, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Benhani:2019:SAT**

[BBA19] E. M. Benhani, L. Bossuet, and A. Aubert. The security of ARM trustzone in a FPGA-based SoC. *IEEE Transactions on Computers*, 68(8):1238–1248, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Biondi:2016:SAH**

[BBB16] A. Biondi, G. C. Buttazzo, and M. Bertogna. Schedulability analysis of hierarchical real-time systems under shared resources. *IEEE Transactions on Computers*, 65(5):1593–1605, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Braojos:2017:SBH**

[BBB+17] Rubén Braojos, Daniele Bortolotti, Andrea Bartolini, Giovanni Ansaloni, Luca Benini, and David Atienza. A synchronization-based hybrid-memory multi-core architecture for energy-efficient biomedical signal processing. *IEEE Transactions on Computers*, 66 (4):575–585, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Baruah:2012:SRT**

[BBD+12] Sanjoy Baruah, Vincenzo Bonifaci, Gianlorenzo D'Angelo, Haohan Li, Alberto Marchetti-Spaccamela, Nicole Megow, and Leen Stougie. Scheduling real-time mixed-criticality jobs. *IEEE Transactions on Computers*, 61(8):1140–1152, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bremlerr-Barr:2012:SET**

[BBH12] Anat Bremlerr-Barr and Danny Hendler. Space-efficient TCAM-based classification using gray coding. *IEEE Transactions on Computers*, 61(1):18–30, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bauer:2013:TSR**

[BBI+13] Lars Bauer, Claus Braun, Michael E. Imhof, Michael A. Kochte, Eric Schneider, Hongyan Zhang, Jorg Henkel, and Hans-Joachim Wunderlich. Test strategies for reliable runtime reconfigurable architectures. *IEEE Transactions on Computers*, 62 (8):1494–1507, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bartlett:2010:ADL**

[BBK10] M. Bartlett, I. Bate, and D. Kazakov. Accurate determination of loop iterations for worst-case execution time analysis. *IEEE Transactions on Computers*, 59(11): 1520–1532, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432156.

**Betti:2013:RTM**

[BBP+13] Emiliano Betti, Stanley Bak, Rodolfo Pellizzoni, Marco Caccamo, and Lui Sha. Real-time I/O management system with COTS peripherals. *IEEE Transactions on Computers*, 62(1):45–58, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Baldoni:2015:ENO**

[BBPQ15] R. Baldoni, S. Bonomi, M. Platania, and L. Querzoni. Efficient notification ordering for geo-distributed pub/sub systems. *IEEE Transactions on Computers*, 64(10):2796–2808, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Byun:2014:CES**

[BBVL14] Sang-Seon Byun, Ilangko Balashingham, Athanasios V. Vasilakos, and Heung-No Lee. Computation of an equilibrium in spectrum markets for cognitive radio networks. *IEEE Transactions on Computers*, 63(2):304–316, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bolchini:2016:NAI**

[BC16] C. Bolchini and L. Cassano. A novel approach to incremental functional diagnosis for complex electronic boards. *IEEE Transactions on Computers*, 65 (1):42–52, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bhati:2016:DRM**

[BCC+16] I. Bhati, M. Chang, Z. Chishti, S. Lu, and B. Jacob. DRAM refresh mechanisms, penalties, and trade-offs. *IEEE Transactions on Computers*, 65(1):108–121, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bernardi:2016:DFL**

[BCD+16] P. Bernardi, R. Cantoro, S. De Luca, E. Sanchez, and A. Sansonetti. Development flow for on-line core self-test of automotive microcontrollers. *IEEE Transactions on Computers*, 65 (3):744–754, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Barus:2016:CER**

[BCK+16] Arlinta C. Barus, Tsong Yueh Chen, Fei-Ching Kuo, Huai Liu, Robert Merkel, and Gregg Rothermel. A cost-effective random testing method for programs with non-numeric inputs. *IEEE Transactions on Computers*, 65(12):3509–3523, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Benoit:2017:TOM**

[BCL+17] Anne Benoit, Aurelien Cavelan, Valentin Le Fevre, Yves Robert, and Hongyang Sun. Towards optimal multi-level checkpointing. *IEEE Transactions on Computers*, 66 (7):1212–1226, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07795220-abs.html`.

**Baek:2014:RNT**

[BCM14] S. Baek, Sangyeun Cho, and R. Melhem. Refresh now and then. *IEEE Transactions on Computers*, 63(12): 3114–3126, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Boukerche:2010:HAF**

[BCMJ10] A. Boukerche, J. M. Correa, A. Melo, and R. P. Jacobi. A hardware accelerator for the fast retrieval of DIALIGN biological sequence alignments in linear space. *IEEE Transactions on Computers*, 59(6):808–821, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416685`.

**Bruguera:2011:GEI**

[BCS11] Javier Bruguera, Marius Cornea, and Debjit Das Sarma. Guest Editors' introduction: Special section on computer arithmetic. *IEEE Transactions on Computers*, 60(2):145–147, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bernardi:2014:MHT**

[BCSR14] P. Bernardi, L. M. Ciganda, E. Sanchez, and M. S. Reorda. MIHST: A hardware technique for embedded microprocessor functional online self-test. *IEEE Transactions on Computers*, 63(11): 2760–2771, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bernasconi:2015:UFC**

[BCTV15] A. Bernasconi, V. Ciriani, G. Trucco, and T. Villa. Using flexibility in $P$-circuits by Boolean relations. *IEEE Transactions on Computers*, 64(12):3605–3618, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bernasconi:2019:BMP**

[BCTV19] A. Bernasconi, V. Ciriani, G. Trucco, and T. Villa. Boolean minimization of projected sums of products via Boolean relations. *IEEE Transactions on Computers*, 68(9):1269–1282, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bardhan:2015:PEM**

[BD15] S. Bardhan and A. Daniel. Predicting the effect of memory contention in multicore computers using analytic performance models. *IEEE Transactions on Computers*, 64(8):2279–2292, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Biondi:2018:RTA**

[BDB18] Alessandro Biondi, Marco Di Natale, and Giorgio Buttazzo. Response-time analysis of engine control applications under fixed-priority scheduling. *IEEE Transactions on Computers*, 67(5):687–703, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8119938/`.

**Burns:2018:RMC**

[BDBB18] Alan Burns, Robert I. Davis, Sanjoy Baruah, and Iain Bate. Robust mixed-criticality systems. *IEEE Transactions on Computers*, 67(10):1478–1491, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8352522/`.

**Bruguera:2019:GEI**

[BdD19] J. D. Bruguera and F. de Dinechin. Guest Editors introduction: Special section on computer arithmetic. *IEEE Transactions on Computers*, 68(7): 951–952, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bai:2018:CSL**

[BDDL18] Yu Bai, Ronald F. DeMara, Jia Di, and Mingjie Lin. Clockless spintronic logic: A robust and ultra-low power computing paradigm. *IEEE Transactions on Computers*, 67(5):631–645, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8118192/`.

**Beuchat:2011:FAP**

[BDE+11] Jean-Luc Beuchat, Jeremie Detrey, Nicolas Estibals, Eiji Okamoto, and Francisco Rodriguez-Henriquez. Fast architectures for the $\eta_T$ pairing over small-characteristic supersingular elliptic curves. *IEEE Transactions on Computers*, 60 (2):266–281, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bruneo:2013:WBS**

[BDL+13] Dario Bruneo, Salvatore Distefano, Francesco Longo, Antonio Puliafito, and Marco Scarpa. Workload-based software rejuvenation in cloud systems. *IEEE Transactions on Computers*, 62 (6):1072–1085, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Berthou:2019:SLK**

[BDM+19] G. Berthou, T. Delizy, K. Marquet, T. Risset, and G. Salagnac. Sytare: A lightweight kernel for NVRAM-based transiently-powered systems. *IEEE Transactions on Computers*, 68(9):1390–1403, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bossuet:2016:EPA**

[BDMLN16] Lilian Bossuet, Nilanjan Datta, Cuauhtemoc Mancillas-López, and Mridul Nandi. ELmD: A pipelineable authenticated encryption and its hardware implementation. *IEEE Transactions on Computers*, 65(11):3318–3331, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bertossi:2015:SPP**

[BDP15] A. A. Bertossi, D. Diodati, and C. M. Pinotti. Storage placement in path networks. *IEEE Transactions on Computers*, 64 (4):1201–1207, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Banerjee:2019:AAS**

[BEHL+19] Subho Sankar Banerjee, Mohamed El-Hadedy, Jong Bin

Lim, Zbigniew T. Kalbarczyk, Deming Chen, Steven S. Lumetta, and Ravishankar K. Iyer. ASAP: Accelerated short-read alignment on programmable hardware. *IEEE Transactions on Computers*, 68(3):331–346, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8490591/`.

**Bos:2019:ACI**

[BF19] J. W. Bos and S. J. Friedberger. Arithmetic considerations for isogeny-based cryptography. *IEEE Transactions on Computers*, 68(7): 979–990, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Berger:2016:EGF**

[BFMT16] Thierry P. Berger, Julien Francq, Marine Minier, and Gaël Thomas. Extended generalized Feistel networks using matrix representation to propose a new lightweight block cipher: Lilliput. *IEEE Transactions on Computers*, 65(7):2074–2089, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bombieri:2011:AAR**

[BFP11] Nicola Bombieri, Franco Fummi, and Graziano Pravadelli. Automatic abstraction of RTL IPs into equivalent TLM descriptions. *IEEE Transactions on Computers*, 60(12):1730–1743, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5560642`.

**Brown:2015:SPM**

[BFR+15] A. D. Brown, S. B. Furber, J. S. Reeve, J. D. Garside, K. J. Dugan, L. A. Plana, and S. Temple. SpiNNaker-programming model. *IEEE Transactions on Computers*, 64(6):1769–1782, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bingham:2012:MET**

[BG12] Brad D. Bingham and Mark R. Greenstreet. Modeling energy-time trade-offs in VLSI computation. *IEEE Transactions on Computers*, 61(4):530–547, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Burgess:2019:HPA**

[BGHL19] N. Burgess, C. Goodyer, C. N. Hinds, and D. R. Lutz. High-precision anchored accumulators for reproducible floating-point summation. *IEEE Transactions on Computers*, 68(7):967–978, July 2019. CODEN ITCOB4.

ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bertran:2013:SMG**

[BGM+13] Ramon Bertran, Marc Gonzelez, Xavier Martorell, Nacho Navarro, and Eduard Ayguade. A systematic methodology to generate decomposable and responsive power models for CMPs. *IEEE Transactions on Computers*, 62 (7):1289–1302, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bianco:2013:PCC**

[BGMR13] Andrea Bianco, Paolo Giaccone, Guido Masera, and Marco Ricca. Power control for crossbar-based input-queued switches. *IEEE Transactions on Computers*, 62(1):74–82, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bosio:2010:CFL**

[BGPV10] B. Bosio, P. Girard, S. Pravossoudovitch, and A. Virazel. A comprehensive framework for logic diagnosis of arbitrary defects. *IEEE Transactions on Computers*, 59 (3):289–300, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5342412`.

**Burns:2015:DFI**

[BGRH15] A. Burns, M. Gutierrez, M. Aldea Rivas, and M. Gonzalez Harbour. A deadline-floor inheritance protocol for EDF scheduled embedded real-time systems with resource sharing. *IEEE Transactions on Computers*, 64 (5):1241–1253, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Brisebarre:2017:ESC**

[BHR17] Nicolas Brisebarre, Guillaume Hanrot, and Olivier Robert. Exponential sums and correctly-rounded functions. *IEEE Transactions on Computers*, 66(12): 2044–2057, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7891945/`.

**Bini:2015:QUU**

[Bin15] E. Bini. The quadratic utilization upper bound for arbitrary deadline real-time tasks. *IEEE Transactions on Computers*, 64(2):593–599, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bessa:2017:FNF**

[BIP+17] Iury Bessa, Hussama Ismail, Reinaldo Palhares, Lucas Cordeiro, and João Edgar Chaves Filho. Formal non-fragile stability verification of digital control systems with uncertainty. *IEEE Transactions on Computers*, 66(3):545–552, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Brumley:2010:CAI**

[BJ10] B. B. Brumley and K. U. Jarvinen. Conversion algorithms and implementations for Koblitz curve cryptography. *IEEE Transactions on Computers*, 59 (1):81–92, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255226`.

**Bircher:2012:CSP**

[BJ12] W. Lloyd Bircher and Lizy K. John. Complete system power estimation using processor performance events. *IEEE Transactions on Computers*, 61(4):563–577, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bogdanov:2012:BLD**

[BK12] Andrey Bogdanov and Ilya Kizhvatov. Beyond the limits of DPA: Combined side-channel collision attacks. *IEEE Transactions on Computers*, 61(8):1153–1164, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Balston:2013:PSC**

[BKH+13] Kyle Balston, Mehdi Karimibiuki, Alan J. Hu, Andre Ivanov, and Steven J. E. Wilton. Post-silicon code coverage for multiprocessor system-on-chip designs. *IEEE Transactions on Computers*, 62(2):242–246, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bogdanov:2013:SDS**

[BKL+13] Andrey Bogdanov, Miroslav Knezevic, Gregor Leander, Deniz Toz, Kerem Varici, and Ingrid Verbauwhede. SPONGENT: The design space of lightweight cryptographic hashing. *IEEE Transactions on Computers*, 62(10):2041–2053, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bolchini:2016:GEI**

[BKP16] C. Bolchini, S. Kundu, and S. Pontarelli. Guest editorial: *IEEE Transactions on Computers* and *IEEE Transactions on Nanotechnology* joint special section on defect and fault tolerance in VLSI and nanotechnology systems. *IEEE Transactions on Computers*, 65(3):677–678, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Benkrid:2013:GEI**

[BKPMC13] Khaled Benkrid, Didier Keymeulen, Umeshkumar D. Patel, and David Merodio-Codinachs. Guest Editors' introduction: Special section on adaptive hardware and systems. *IEEE Transactions on Computers*, 62 (8):1478–1480, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Busch:2012:ACD**

[BKV12] Costas Busch, Rajgopal Kannan, and Athanasios V. Vasilakos. Approximating congestion + dilation in networks via "quality of routing" games. *IEEE Transactions on Computers*, 61(9): 1270–1283, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bellal:2019:IAA**

[BLB+19] R. Bellal, E. Lamini, H. Belbachir, S. Tagzout, and A. Belouchrani. Improved affine arithmetic-based precision analysis for polynomial function evaluation. *IEEE Transactions on Computers*, 68(5):702–712, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bakhshalipour:2018:FDD**

[BLKM+18] Mohammad Bakhshalipour, Pejman Lotfi-Kamran, Abbas Mazloumi, Farid Samandi, Mahmood Naderan-Tahan, Mehdi Modarressi, and Hamid Sarbazi-Azad. Fast data delivery for many-core processors. *IEEE Transactions on Computers*, 67(10): 1416–1429, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8331120/`.

**Brisebarre:2016:CBB**

[BLMM16] Nicolas Brisebarre, Christoph Lauter, Marc Mezzarobba, and Jean-Michel Muller. Comparison between binary and decimal floating-point numbers. *IEEE Transactions on Computers*, 65 (7):2032–2044, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Baek:2015:SAC**

[BLN+15] Seungcheol Baek, Hyung Gyu Lee, C. Nicopoulos, Junghee Lee, and Jongman Kim. Size-aware cache management for compressed cache architectures. *IEEE Trans-*

*actions on Computers*, 64 (8):2337–2352, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Boldo:2011:EAE**

[BM11] Sylvie Boldo and Jean-Michel Muller. Exact and approximated error of the FMA. *IEEE Transactions on Computers*, 60(2):157–164, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Baldi:2013:TDP**

[BM13a] Mario Baldi and Guido Marchetto. Time-driven priority router implementation: Analysis and experiments. *IEEE Transactions on Computers*, 62 (5):1017–1030, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bolchini:2013:RDS**

[BM13b] Cristiana Bolchini and Antonio Miele. Reliability-driven system-level synthesis for mixed-critical embedded systems. *IEEE Transactions on Computers*, 62(12):2489–2502, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Benso:2011:GEI**

[BMM11] Alfredo Benso, Yiorgos Makris, and Pinaki Mazumder. Guest Editors' introduction: Special section on chips and architectures for emerging technologies and applications. *IEEE Transactions on Computers*, 60(4):450–451, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Benoit:2010:SCB**

[BMP+10] A. Benoit, L. Marchal, J.-F. Pineau, Y. Robert, and F. Vivien. Scheduling concurrent bag-of-tasks applications on heterogeneous platforms. *IEEE Transactions on Computers*, 59 (2):202–217, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184819`.

**Bolchini:2011:NDM**

[BMS11] Cristiana Bolchini, Antonio Miele, and Chiara Sandionigi. A novel design methodology for implementing reliability-aware systems on SRAM-based FPGAs. *IEEE Transactions on Computers*, 60(12): 1744–1758, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5674027.

**Banerjee-Mishra:2012:CUP**

[BMS12] Tania Banerjee-Mishra and Sartaj Sahni. Consistent updates for packet classifiers. *IEEE Transactions on Computers*, 61(9): 1284–1295, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bendjoudi:2014:FBF**

[BMT14] Ahcene Bendjoudi, Nouredine Melab, and El-Ghazali Talbi. FTH-B&B: A fault-tolerant hierarchical branch and bound for large scale unreliable environments. *IEEE Transactions on Computers*, 63(9):2302–2315, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bisi:2017:SAH**

[BMZ17] Elia Bisi, Filippo Melzani, and Vittorio Zaccaria. Symbolic analysis of higher-order side channel countermeasures. *IEEE Transactions on Computers*, 66 (6):1099–1105, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7769194/.

**Bajard:2010:SSC**

[BNP10] J.-C. Bajard, C. Negre, and T. Plantard. Subquadratic space complexity binary field multiplier using double polynomial representation. *IEEE Transactions on Computers*, 59(12):1585–1597, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5487504.

**Ben-Porat:2013:VNM**

[BPBBL13] Udi Ben-Porat, Anat Bremler-Barr, and Hanoch Levy. Vulnerability of network mechanisms to sophisticated DDoS attacks. *IEEE Transactions on Computers*, 62(5):1031–1043, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bui:2012:RTS**

[BPC12] Bach Duy Bui, Rodolfo Pellizzoni, and Marco Caccamo. Real-time scheduling of concurrent transactions in multidomain ring buses. *IEEE Transactions on Computers*, 61(9): 1311–1324, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bieschewski:2016:EEM**

[BPG16] Stefan Bieschewski, Joan-Manuel Parcerisa, and Antonio González. An energy-efficient memory unit for clustered microarchitectures. *IEEE Transactions on Computers*, 65(8):2631–2637, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bobrek:2010:SCL**

[BPT10] A. Bobrek, J. M. Paul, and D. E. Thomas. Stochastic contention level simulation for single-chip heterogeneous multiprocessors. *IEEE Transactions on Computers*, 59(10):1402–1418, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396328.

**Babecki:2016:EMC**

[BQP+16] Christopher Babecki, Wenchao Qian, Somnath Paul, Robert Karam, and Swarup Bhunia. An embedded memory-centric reconfigurable hardware accelerator for security applications. *IEEE Transactions on Computers*, 65(10):3196–3202, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Balasangameshwara:2013:PDL**

[BR13] Jasma Balasangameshwara and Nedunchezhian Raju. Performance-driven load balancing with a primary-backup approach for computational grids with low communication cost and replication cost. *IEEE Transactions on Computers*, 62 (5):990–1003, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Brabec:2010:SRC**

[Bra10] T. Brabec. Speculatively redundant continued logarithm representation. *IEEE Transactions on Computers*, 59(11):1441–1454, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467052.

**Bayrak:2015:AAP**

[BRN+15] A. G. Bayrak, F. Regazzoni, D. Novo, P. Brisk, F.-X. Standaert, and P. Ienne. Automatic application of power analysis countermeasures. *IEEE Transactions on Computers*, 64(2):329–341, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bolchini:2010:GEI**

[BS10] Cristiana Bolchini and Donatella Sciuto. Guest Editors' introduction: Special section on system-level design of reliable architectures.

IEEE Transactions on Computers, 59(5):577–578, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5437559.

**Bhattacherjee:2014:CAT**

[BS14] Sanjay Bhattacherjee and Palash Sarkar. Concrete analysis and trade-offs for the (complete tree) layered subset difference broadcast encryption scheme. *IEEE Transactions on Computers*, 63(7):1709–1722, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Banerjee:2015:PEP**

[BS15] T. Banerjee and S. Sahni. Pubsub: An efficient publish/subscribe system. *IEEE Transactions on Computers*, 64(4):1119–1132, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bhattacherjee:2016:RCO**

[BS16] Sanjay Bhattacherjee and Palash Sarkar. Reducing communication overhead of the subset difference scheme. *IEEE Transactions on Computers*, 65(8):2575–2587, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Briceno:2014:RAC**

[BSM+14] L. D. Briceno, H. J. Siegel, A. A. Maciejewski, Ye Hong, B. Lock, C. Panaccione, F. Wedyan, M. N. Teli, and Chen Zhang. Resource allocation in a client/server system for massive multiplayer online games. *IEEE Transactions on Computers*, 63(12):3127–3142, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Banerjee:2014:PDT**

[BSS14] Tania Banerjee, Sartaj Sahni, and Gunasekaran Seetharaman. PC-DUOS+: A TCAM architecture for packet classifiers. *IEEE Transactions on Computers*, 63(6):1527–1540, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Banerjee:2015:PTP**

[BSS15] T. Banerjee, S. Sahni, and G. Seetharaman. PC-TRIO: A power efficient TCAM architecture for packet classifiers. *IEEE Transactions on Computers*, 64(4):1104–1118, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bigou:2016:BTP**

[BT16] Karim Bigou and Arnaud Tisserand. Binary-ternary plus-minus modular inversion in RNS. *IEEE Transactions on Computers*, 65(11): 3495–3501, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Benini:2014:EGB**

[BTBB14] Luca Benini, Andrea Tilli, Andrea Bartolini, and Francesco Beneventi. An effective gray-box identification procedure for multicore thermal modeling. *IEEE Transactions on Computers*, 63 (5):1097–1110, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bouguerra:2013:CAC**

[BTW13] Mohamed-Slim Bouguerra, Denis Trystram, and Frederic Wagner. Complexity analysis of checkpoint scheduling with variable costs. *IEEE Transactions on Computers*, 62(6):1269–1275, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bhuiyan:2015:DWS**

[BWCW15] M. Z. A. Bhuiyan, Guojun Wang, Jiannong Cao, and Jie Wu. Deploying wireless sensor networks with fault-tolerance for structural health monitoring. *IEEE Transactions on Computers*, 64(2):382–395, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bhuiyan:2015:LAP**

[BWV15] M. Z. A. Bhuiyan, Guojun Wang, and A. V. Vasilakos. Local area prediction-based mobile target tracking in wireless sensor networks. *IEEE Transactions on Computers*, 64(7):1968–1982, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Bouvier:2014:DFB**

[BZ14] Cyril Bouvier and Paul Zimmermann. Division-free binary-to-decimal conversion. *IEEE Transactions on Computers*, 63(8): 1895–1901, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Beldianu:2015:PEO**

[BZ15] S. F. Beldianu and S. G. Ziavras. Performance-energy optimizations for shared vector accelerators in multicores. *IEEE Transactions on Computers*, 64(3):805–817, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cassidy:2012:BAL**

[CA12a] Andrew S. Cassidy and Andreas G. Andreou. Beyond Amdahl's Law: An objective function that links multiprocessor performance gains to delay and energy. *IEEE Transactions on Computers*, 61(8):1110–1126, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2012:MCC**

[CA12b] Xi E. Chen and Tor M. Aamodt. Modeling cache contention and throughput of multiprogrammed manycore processors. *IEEE Transactions on Computers*, 61(7):913–927, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Caplan:2018:MSM**

[CAbZM18] Jonah Caplan, Zaid Albayati, Haibo Zeng, and Brett H. Meyer. Mapping and scheduling mixed-criticality systems with on-demand redundancy. *IEEE Transactions on Computers*, 67(4):582–588, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8066347/`.

**Christoforou:2014:AMR**

[CAGM14] Evgenia Christoforou, Antonio Fernandez Anta, Chryssis Georgiou, and Miguel A. Mosteiro. Algorithmic mechanisms for reliable master-worker Internet-based computing. *IEEE Transactions on Computers*, 63(1):179–195, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cao:2012:RMP**

[Cao12] Yongzhi Cao. Reliability of mobile processes with noisy channels. *IEEE Transactions on Computers*, 61(9):1217–1230, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Courtois:2019:RRR**

[CATB19] J. Courtois, L. Abbas-Turki, and J. Bajard. Resilience of randomized RNS arithmetic with respect to side-channel leaks of cryptographic computation. *IEEE Transactions on Computers*, 68(12):1720–1730, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:TCG**

[CB15] Lin Chen and Kaigui Bian. The telephone coordination game revisited: From random to deterministic algorithms. *IEEE Transactions on Computers*, 64(10): 2968–2980, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Casini:2019:HTD**

[CBB19] D. Casini, A. Biondi, and G. Buttazzo. Handling transients of dynamic real-time workload under EDF scheduling. *IEEE Transactions on Computers*, 68 (6):820–835, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cotuk:2014:ITP**

[CBTU14] H. Cotuk, K. Bicakci, B. Tavli, and E. Uzun. The impact of transmission power control strategies on lifetime of wireless sensor networks. *IEEE Transactions on Computers*, 63(11): 2866–2879, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Coutinho:2016:GOR**

[CBVL16] R. W. L. Coutinho, A. Boukerche, L. F. M. Vieira, and A. A. F. Loureiro. Geographic and opportunistic routing for underwater sensor networks. *IEEE Transactions on Computers*, 65 (2):548–561, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2014:SSF**

[CBZ14] Ho-Lin Chen, Jehoshua Bruck, and Hongchao Zhou. Synthesis of stochastic flow networks. *IEEE Transactions on Computers*, 63 (5):1234–1247, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chiao:2011:RNF**

[CC11] Mong-Ling Chiao and Da-Wei Chang. ROSE: a novel flash translation layer for NAND flash memory based on hybrid address translation. *IEEE Transactions on Computers*, 60(6):753–766, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:NCM**

[CC16] C. Chen and C. Chu. A novel computational model for non-linear divisible loads on a linear network. *IEEE Transactions on Computers*, 65(1):53–65, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Choudhury:2014:TBC**

[CCAM14] Mihir R. Choudhury, Vikas Chandra, Robert C. Aitken, and Kartik Mohanram.

Time-borrowing circuit designs and hardware prototyping for timing error resilience. *IEEE Transactions on Computers*, 63 (2):497–509, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:DOS**

[CCC15] L. Chen, A. Cui, and C. Chang. Design of optimal scan tree based on compact test patterns for test time reduction. *IEEE Transactions on Computers*, 64(12): 3417–3429, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2017:EWH**

[CCC+17] Che-Wei Chang, Geng-You Chen, Yi-Jung Chen, Chia-Wei Yeh, Pei Yin Eng, Ana Cheung, and Chia-Lin Yang. Exploiting write heterogeneity of morphable MLC/SLC SSDs in datacenters with service-level objectives. *IEEE Transactions on Computers*, 66(8): 1457–1463, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7869253/`.

**Chen:2018:WWR**

[CCC+18] Tseng-Yi Chen, Yuan-Hao Chang, Shuo-Han Chen, Chih-Ching Kuo, Ming-Chang Yang, Hsin-Wen Wei, and Wei-Kuan Shih. wrJFS: A write-reduction journaling file system for byte-addressable NVRAM. *IEEE Transactions on Computers*, 67(7):1023–1038, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8260880/`.

**Cinque:2012:AGP**

[CCD12] Marcello Cinque, Domenico Cotroneo, and Catello Di Martino. Automated generation of performance and dependability models for the assessment of wireless sensor networks. *IEEE Transactions on Computers*, 61(6):870–884, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Charif:2018:FLC**

[CCE+18] Amir Charif, Alexandre Coelho, Masoumeh Ebrahimi, Nader Bagherzadeh, and Nacer-Eddine Zergainoh. First-last: A cost-effective adaptive routing solution for TSV-based three-dimensional networks-on-chip. *IEEE Transactions on Computers*, 67(10):1430–1444, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https:`

//ieeexplore.ieee.org/document/8333738/.

**Chen:2011:MJP**

[CCH11] Kuo-Yi Chen, J. Morris Chang, and Ting-Wei Hou. Multithreading in Java: Performance and scalability on multicore systems. *IEEE Transactions on Computers*, 60(11):1521–1534, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5661769.

**Chang:2015:BSA**

[CCH+15a] C. Chang, J. Cheng, T. Huang, X. Huang, D. Lee, and C. Chen. Bit-stuffing algorithms for crosstalk avoidance in high-speed switching. *IEEE Transactions on Computers*, 64(12):3404–3416, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:CDG**

[CCH15b] Chun-An Chen, Guey-Yun Chang, and Sun-Yuan Hsieh. Conditional-diagnosis in graphs by using the comparison diagnosis model. *IEEE Transactions on Computers*, 64 (6):1622–1632, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Carmona:2010:NRB**

[CCK10] J. Carmona, J. Cortadella, and M. Kishinevsky. New region-based algorithms for deriving bounded Petri nets. *IEEE Transactions on Computers*, 59(3):371–384, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5235141.

**Chang:2016:DRF**

[CCK+16a] Y. M. Chang, Y. H. Chang, T. W. Kuo, Y. C. Li, and H. P. Li. Disturbance relaxation for 3D flash memory. *IEEE Transactions on Computers*, 65(5):1467–1483, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:TMR**

[CCK+16b] Kuan-Hsun Chen, Jian-Jia Chen, Florian Kriebel, Semeen Rehman, Muhammad Shafique, and Jörg Henkel. Task mapping for redundant multithreading in multi-cores with reliability and performance heterogeneity. *IEEE Transactions on Computers*, 65(11): 3441–3455, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2013:LDA**

[CCL+13] Yunji Chen, Tianshi Chen, Ling Li, Lei Li, Liang Yang, Menghao Su, and Weiwu Hu. LDet: Determinizing asynchronous transfer for postsilicon debugging. *IEEE Transactions on Computers*, 62(9): 1732–1744, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2018:EEB**

[CCL+18] Shuo-Han Chen, Yuan-Hao Chang, Yu-Pei Liang, Hsin-Wen Wei, and Wei-Kuan Shih. An erase efficiency boosting strategy for 3D charge trap NAND flash. *IEEE Transactions on Computers*, 67(9): 1246–1258, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8322264/`.

**Cope:2010:PCG**

[CCLH10] B. Cope, P. Y. K. Cheung, W. Luk, and L. Howes. Performance comparison of graphics processors to reconfigurable logic: a case study. *IEEE Transactions on Computers*, 59(4):433–448, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374368`.

**Carara:2014:DCS**

[CCM14] Everton Alceu Carara, Ney Laert Vilar Calazans, and Fernando Gehm Moraes. Differentiated communication services for NoC-based MPSoCs. *IEEE Transactions on Computers*, 63(3):595–608, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chamberlain:2014:DSS**

[CCO+14] Brent C. Chamberlain, Giuseppe Carenini, Gunilla Oberg, David Poole, and Hamed Taheri. A decision support system for the design and evaluation of sustainable wastewater solutions. *IEEE Transactions on Computers*, 63(1):129–141, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Clegg:2013:SMM**

[CCP+13] Richard G. Clegg, Stuart Clayman, George Pavlou, Lefteris Mamatas, and Alex Galis. On the selection of management/monitoring nodes in highly dynamic networks. *IEEE Transactions on Computers*, 62 (6):1207–1220, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Coelho:2017:DCU**

[CCR+17] Diego F. G. Coelho, Renato J. Cintra, Nilanka Rajapaksha, Gihan J. Mendis, Arjuna Madanayake, and Vassil S. Dimitrov. DFT computation using Gauss–Eisenstein basis: FFT algorithms and VLSI architectures. *IEEE Transactions on Computers*, 66(8): 1442–1448, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7869325/`.

**Chen:2019:EIR**

[CCRL19] K. Chen, L. Chen, P. Reviriego, and F. Lombardi. Efficient implementations of reduced precision redundancy (RPR) multiply and accumulate (MAC). *IEEE Transactions on Computers*, 68(5):784–790, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Carretero:2011:IEE**

[CCV+11] Javier Carretero, Pedro Chaparro, Xavier Vera, Jaume Abella, and Antonio Gonzalez. Implementing end-to-end register data-flow continuous self-test. *IEEE Transactions on Computers*, 60 (8):1194–1206, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2010:ALD**

[CCW+10] Songqing Chen, Shiping Chen, Xinyuan Wang, Zhao Zhang, and Sushil Jajodia. An application-level data transparent authentication scheme without communication overhead. *IEEE Transactions on Computers*, 59(7):943–954, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453339`.

**Chiueh:2014:SFI**

[cCWS14] Tzi cker Chiueh, Xin Wang, and Zhiyong Shan. Shuttle: Facilitating inter-application interactions for OS-level virtualization. *IEEE Transactions on Computers*, 63 (5):1220–1233, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:MGB**

[CCY+16] Tseng-Yi Chen, Yuan-Hao Chang, Ming-Chang Yang, Yun-Jhu Chen, Hsin-Wen Wei, and Wei-Kuan Shih. Multi-grained block management to enhance the space utilization of file systems on PCM storages. *IEEE Transactions on Computers*, 65 (6):1831–1845, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Choi:2018:CCN**

[CDK+18] Wonje Choi, Karthi Duraisamy, Ryan Gary Kim, Janardhan Rao Doppa, Partha Pratim Pande, Diana Marculescu, and Radu Marculescu. On-chip communication network for efficient training of deep convolutional networks on heterogeneous manycore systems. *IEEE Transactions on Computers*, 67(5):672–686, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8119941/.

**Cui:2017:HPP**

[CDL+17] Xiaoping Cui, Wenwen Dong, Weiqiang Liu, Earl E. Swartzlander, and Fabrizio Lombardi. High performance parallel decimal multipliers using hybrid BCD codes. *IEEE Transactions on Computers*, 66(12): 1994–2004, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7931610/.

**Chai:2015:WID**

[CDQB15] Y. Chai, Z. Du, X. Qin, and D. A. Bader. WEC: Improving durability of SSD cache drives by caching write-efficient data. *IEEE Transactions on Computers*, 64(11):3304–3316, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Calinescu:2018:EOT**

[CFL+18] Gruia Calinescu, Chenchen Fu, Minming Li, Kai Wang, and Chun Jason Xue. Energy optimal task scheduling with normally-off local memory and sleep-aware shared memory with access conflict. *IEEE Transactions on Computers*, 67 (8):1121–1135, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8290729/.

**Cheshmikhani:2019:LRP**

[CFMA19] Elham Cheshmikhani, Hamed Farbeh, Seyed Ghassem Miremadi, and Hossein Asadi. TA-LRW: A replacement policy for error rate reduction in STT-MRAM caches. *IEEE Transactions on Computers*, 68 (3):455–470, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8489928/.

**Casteigts:2014:MTL**

[CFMS14] Arnaud Casteigts, Paola Flocchini, Bernard Mans, and Nicola Santoro. Measuring temporal lags in delay-tolerant networks. *IEEE Transactions on Computers*, 63(2):397–410, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cano:2014:ERH**

[CFR+14] Jose Cano, Jose Flich, Antoni Roca, Jose Duato, Marcello Coppola, and Riccardo Locatelli. Efficient routing in heterogeneous SoC designs with small implementation overhead. *IEEE Transactions on Computers*, 63(3):557–569, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chung:2014:DSG**

[CFW14] Sung Hoon Chung, Terry L. Friesz, and Robert D. Weaver. Dynamic sustainability games for renewable resources — a computational approach. *IEEE Transactions on Computers*, 63(1):155–166, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2018:CLA**

[CG18] Quan Chen and Minyi Guo. Contention and locality-aware work-stealing for iterative applications in multi-socket computers. *IEEE Transactions on Computers*, 67(6):784–798, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8214252/`.

**Choi:2010:PCP**

[CGJ+10] Jeonghwan Choi, S. Govindan, Jinkyu Jeong, B. Urgaonkar, and A. Sivasubramaniam. Power consumption prediction and power-aware packing in consolidated environments. *IEEE Transactions on Computers*, 59(12):1640–1654, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453350`.

**Chu:2013:SAS**

[CGL+13] Rui Chu, Lin Gu, Yunhao Liu, Mo Li, and Xicheng Lu. SenSmart: Adaptive stack management for multitasking sensor networks. *IEEE Transactions on Computers*, 62(1):137–150, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2018:UBS**

[CGL+18] Gang Chen, Nan Guan, Di Liu, Qingqiang He, Kai Huang, Todor Stefanov, and Wang Yi. Utilization-based scheduling of flexible mixed-criticality real-time tasks.

*IEEE Transactions on Computers*, 67(4):543–558, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8068215/.

**Chen:2015:SPC**

[CGT+15] Tianshi Chen, Qi Guo, O. Temam, Yue Wu, Yungang Bao, Zhiwei Xu, and Yunji Chen. Statistical performance comparisons of computers. *IEEE Transactions on Computers*, 64 (5):1442–1455, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2011:DCC**

[CH11] Chun-An Chen and Sun-Yuan Hsieh. $(t, k)$-diagnosis for component-composition graphs under the MM* model. *IEEE Transactions on Computers*, 60(12): 1704–1717, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5601693.

**Chen:2013:CCG**

[CH13] Chun-An Chen and Sun-Yuan Hsieh. Component-composition graphs: $(t, k)$-diagnosability and its application. *IEEE Transactions on Computers*, 62 (6):1097–1110, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Creech:2014:SAH**

[CH14] Gideon Creech and Jiankun Hu. A semantic approach to host-based intrusion detection systems using contiguous and discontiguous system call patterns. *IEEE Transactions on Computers*, 63(4):807–819, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2010:DRN**

[Cha10a] Guey-Yun Chang. $(t, k)$-diagnosability for regular networks. *IEEE Transactions on Computers*, 59(9): 1153–1157, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396325.

**Chang:2010:HAN**

[Cha10b] Li-Pin Chang. A hybrid approach to NAND-flash-based solid-state disks. *IEEE Transactions on Computers*, 59(10):1337–1349, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5383350.

**Chang:2014:BTM**

[Cha14] Yuan-Hao Chang. Booting time minimization for real-time embedded systems with non-volatile memory. *IEEE Transactions on Computers*, 63(4):847–859, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2015:RAB**

[CHC+15] En-Jui Chang, Hsien-Kai Hsin, Chih-Hao Chao, Shu-Yen Lin, and An-Yeu Wu. Regional ACO-based cascaded adaptive routing for traffic balancing in mesh-based network-on-chip systems. *IEEE Transactions on Computers*, 64(3):868–875, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2012:IDF**

[CHCK12] Dongdong Chen, Liu Han, Younhee Choi, and Seok-Bum Ko. Improved decimal floating-point logarithmic converter based on selection by rounding. *IEEE Transactions on Computers*, 61(5):607–621, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2013:REF**

[CHH+13] Yuan-Hao Chang, Po-Chun Huang, Pei-Han Hsu, Lue-Jane Lee, Tei-Wei Kuo, and David Hung-Chang Du. Reliability enhancement of flash-memory storage systems: An efficient version-based design. *IEEE Transactions on Computers*, 62(12):2503–2515, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2010:IFW**

[CHK10] Yuan-Hao Chang, Jen-Wei Hsieh, and Tei-Wei Kuo. Improving flash wear-leveling by proactively moving static data. *IEEE Transactions on Computers*, 59(1):53–65, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255228`.

**Chen:2017:TAV**

[CHL17] Ke Chen, Jie Han, and Fabrizio Lombardi. Two approximate voting schemes for reliable computing. *IEEE Transactions on Computers*, 66(7):1227–1239, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07819499-abs.html`.

**Chen:2016:DAR**

[CHLL16] Linbin Chen, Jie Han, Weiqiang Liu, and Fabrizio Lombardi. On the design of approximate restoring dividers for error-tolerant applications. *IEEE Transactions on Computers*, 65 (8):2522–2533, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2014:NNC**

[CHLT14] Henry C. H. Chen, Yuchong Hu, Patrick P. C. Lee, and Yang Tang. NCCloud: A network-coding-based storage system in a cloud-of-clouds. *IEEE Transactions on Computers*, 63(1):31–44, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cenk:2014:ESS**

[CHN14] Murat Cenk, M. Anwar Hasan, and Christophe Negre. Efficient subquadratic space complexity binary polynomial multipliers based on block recombination. *IEEE Transactions on Computers*, 63(9): 2273–2287, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2019:GEI**

[CHTD19] Y. Chang, J. Hu, M. B. Tahoori, and R. F. DeMara. Guest editorial: *IEEE Transactions on Computers* special section on emerging non-volatile memory technologies: From devices to architectures and systems. *IEEE Transactions on Computers*, 68(8):1111–1113, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Coleman:2016:LCT**

[CI16] J. N. Coleman and R. Che Ismail. LNS with co-transformation competes with floating-point. *IEEE Transactions on Computers*, 65(1):136–146, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cilardo:2011:EPT**

[Cil11] Alessandro Cilardo. Exploring the potential of threshold logic for cryptography-related operations. *IEEE Transactions on Computers*, 60(4):452–462, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cilardo:2013:FPP**

[Cil13] Alessandro Cilardo. Fast parallel GF($2^m$) polynomial multiplication for all degrees. *IEEE Transactions on Computers*, 62(5):929–943, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cosma:2012:ASC**

[CJ12] Georgina Cosma and Mike Joy. An approach to source-code plagiarism detection and investigation using latent semantic analysis. *IEEE Transactions on Computers*, 61(3):379–394, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cheng:2013:EHM**

[CJ13] Chi Cheng and Tao Jiang. An efficient homomorphic MAC with small key size for authentication in network coding. *IEEE Transactions on Computers*, 62(10): 2096–2100, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See comments [LCLL15, Kim15].

**Chen:2016:ULC**

[CJA+16] Hsing-Min Chen, Supreet Jeloka, Akhil Arunkumar, David Blaauw, Carole-Jean Wu, Trevor Mudge, and Chaitali Chakrabarti. Using low cost erasure and error correction schemes to improve reliability of commodity DRAM systems. *IEEE Transactions on Computers*, 65(12):3766–3779, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:SFA**

[CJG16] H. Chen, H. Jin, and L. Guo. Sink-free audio-on-demand over wireless sensor networks. *IEEE Transactions on Computers*, 65(5):1606–1618, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cui:2015:CCM**

[CJK15] Ting Cui, Chenhui Jin, and Zhiyin Kong. On compact Cauchy matrices for substitution-permutation networks. *IEEE Transactions on Computers*, 64 (7):2098–2102, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Choi:2019:DPM**

[CJK19] J. Choi, J. Jang, and L. Kim. DC-PCM: Mitigating PCM write disturbance with low performance overhead by using detection cells. *IEEE Transactions on Computers*, 68(12):1741–1754, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chakraborty:2017:BDD**

[CJSM17] Rajat Subhra Chakraborty, Ratan Rahul Jeldi, Indrasish Saha, and Jimson Mathew. Binary decision diagram assisted modeling of FPGA-based physically unclonable function by genetic programming. *IEEE Transactions on Computers*, 66(6):971–981, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7553573/.

**Chang:2011:MSR**

[CK11] Yuan-Hao Chang and Tei-Wei Kuo. A management strategy for the reliability and performance improvement of MLC-based flash-memory storage systems. *IEEE Transactions on Computers*, 60(3):305–320, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chung:2015:BWO**

[CK15] J. Chung and L. Kim. Bit-width optimization by divide-and-conquer for fixed-point digital signal processing systems. *IEEE Transactions on Computers*, 64(11): 3091–3101, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2017:EAA**

[CKD+17] X. Chen, N. Khoshavi, R. F. DeMara, J. Wang, D. Huang, W. Wen, and Y. Chen. Energy-aware adaptive restore schemes for MLC STT-RAM cache. *IEEE Transactions on Computers*, 66(5): 786–798, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cheng:2015:DAM**

[CKH15] Sheng-Wei Cheng, Ling-Chia Ku, and Pi-Cheng Hsiu. Dynamic antenna management for uplink energy efficiency on 802.11n mobile devices. *IEEE Transactions on Computers*, 64(10): 2767–2780, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2014:LMS**

[CKKS14] Yeim-Kuan Chang, Fang-Chen Kuo, Han-Jhen Kuo, and Cheng-Chien Su. LayeredTrees: Most specific prefix-based pipelined design for on-chip IP address lookups. *IEEE Transactions on Computers*, 63(12): 3039–3052, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chatterjee:2015:FAP**

[CKM15] S. Chatterjee, K. Karabina, and A. Menezes. Fault attacks on pairing-based protocols revisited. *IEEE Transactions on Computers*, 64 (6):1707–1714, June 2015. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Chiarandini:2014:IPB**

[CKN14] Marco Chiarandini, Niels H. Kjeldsen, and Napoleao Nepomuceno. Integrated planning of biomass inventory and energy production. *IEEE Transactions on Computers*, 63(1):102–114, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2010:PCP**

[CL10] Gang Chen and Feng Liu. Proofs of correctness and properties of integer adder circuits. *IEEE Transactions on Computers*, 59 (1):134–136, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255231`.

**Chen:2012:NDL**

[CL12] Y-Chuang Chen and Shun-Fu Liu. A note on diagnosability of large fault sets on star graphs. *IEEE Transactions on Computers*, 61(6):911–912, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cui:2016:MPP**

[CLC+16] X. Cui, W. Liu, X. Chen, E. E. Swartzlander, and F. Lombardi. A modified partial product generator for redundant binary multipliers. *IEEE Transactions on Computers*, 65 (4):1165–1171, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2019:ECN**

[CLCS19] Y. Chen, Y. Lu, P. Chen, and J. Shu. Efficient and consistent NVMM cache for SSD-based file system. *IEEE Transactions on Computers*, 68(8):1147–1158, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chiou:2014:LLD**

[CLL+14] Che Wun Chiou, Wen-Yo Lee, Chiou-Yng Lee, Jim-Min Lin, Mehran Mozaffari Kermani, Reza Azarderakhsh, and Jeng-Shyang Pan. Low-latency digit-serial systolic double basis multiplier over GF($2^m$) using subquadratic Toeplitz matrix–vector product approach. *IEEE Transactions on Computers*, 63 (5):1169–1181, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See comments [RM15b].

**Chen:2019:CEA**

[CLM+19] H. Chen, S. Lee, T. Mudge, C. Wu, and C. Chakrabarti. Configurable-ECC: Architecting a flexible ECC scheme to support different sized accesses in high bandwidth memory systems. *IEEE Transactions on Computers*, 68(5):646–659, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**CoutodaSilva:2011:EHP**

[CLMM11] Ana Paula Couto da Silva, Emilio Leonardi, Marco Mellia, and Michela Meo. Exploiting heterogeneity in P2P video streaming. *IEEE Transactions on Computers*, 60(5):667–679, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2018:FOB**

[CLOL18] Cen Chen, Kenli Li, Aijia Ouyang, and Keqin Li. FlinkCL: An OpenCL-based in-memory computing architecture on heterogeneous CPU–GPU clusters for big data. *IEEE Transactions on Computers*, 67(12): 1765–1779, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8362980/`.

**Chipara:2013:RTQ**

[CLR13] Octav Chipara, Chenyang Lu, and Gruia-Catalin Roman. Real-time query scheduling for wireless sensor networks. *IEEE Transactions on Computers*, 62(9): 1850–1865, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2010:DMS**

[CLS10] Yeim-Kuan Chang, Yung-Chieh Lin, and Cheng-Chien Su. Dynamic multiway segment tree for IP lookups and the fast pipelined search engine. *IEEE Transactions on Computers*, 59 (4):492–506, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5278660`.

**Cao:2014:OPA**

[CLS14] Junwei Cao, Keqin Li, and Ivan Stojmenovic. Optimal power allocation and load distribution for multiple heterogeneous multicore server processors across clouds and data centers. *IEEE Transactions on Computers*, 63 (1):45–58, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:FSM**

[CLW+15] Dan Chen, Xiaoli Li, Lizhe Wang, S. U. Khan, Juan Wang, Ke Zeng, and Chang Cai. Fast and scalable multi-way analysis of massive neural data. *IEEE Transactions on Computers*, 64(3):707–719, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:VCL**

[CLW+16a] Xiaofeng Chen, Jin Li, Jian Weng, Jianfeng Ma, and Wenjing Lou. Verifiable computation over large database with incremental updates. *IEEE Transactions on Computers*, 65(10): 3184–3195, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cui:2016:KAS**

[CLW16b] Baojiang Cui, Zheli Liu, and Lingyu Wang. Key-Aggregate Searchable Encryption (KASE) for group data sharing via cloud storage. *IEEE Transactions on Computers*, 65(8):2374–2385, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cao:2019:DRR**

[CLW+19] Y. Cao, C. Li, J. Wang, W. Zhang, Q. Chen, J. Leng, B. Yao, Y. Shen, and M. Guo. DR refresh: Releasing DRAM potential by enabling read accesses under refresh. *IEEE Transactions on Computers*, 68(11): 1584–1596, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2014:ASI**

[CLX14] Qi Chen, Haipeng Luo, and Zhen Xiao. Automatic scaling of Internet applications for cloud computing services. *IEEE Transactions on Computers*, 63 (5):1111–1123, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2019:RFM**

[CLZ19] Xubin Chen, Yin Li, and Tong Zhang. Reducing flash memory write traffic by exploiting a few MBs of capacitor-powered write buffer inside solid-state drives (SSDs). *IEEE Transactions on Computers*, 68 (3):426–439, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8470129/`.

**Chen:2011:PLT**

[CM11] Mingsong Chen and Prabhat Mishra. Property learning techniques for efficient generation of directed tests. *IEEE Transactions on Computers*, 60(6):852–864, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Camarero:2013:NTM**

[CMB13] Cristobal Camarero, Carmen Martinez, and Ramon Beivide. *L*-networks: A topological model for regular 2D interconnection networks. *IEEE Transactions on Computers*, 62(7):1362–1375, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chakraborty:2013:EHI**

[CMLRHS13] Debrup Chakraborty, Cuauhtemoc Mancillas-Lopez, Francisco Rodriguez-Henriquez, and Palash Sarkar. Efficient hardware implementations of BRW polynomials and tweakable enciphering schemes. *IEEE Transactions on Computers*, 62 (2):279–294, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chakraborty:2015:SSC**

[CMLS15] D. Chakraborty, C. Mancillas-Lopez, and P. Sarkar. STES: A stream cipher based low cost scheme for securing stored data. *IEEE Transactions on Computers*, 64(9): 2691–2707, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Condo:2015:UEP**

[CMM15] C. Condo, G. Masera, and P. Montuschi. Unequal error protection of memories in LDPC decoders. *IEEE Transactions on Computers*, 64(10):2981–2993, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cao:2016:OMA**

[CMO+16] Xiaolin Cao, Ciara Moore, Máire O'Neill, Elizabeth O'Sullivan, and Neil Hanley. Optimised multiplication architectures for accelerating fully homomorphic encryption. *IEEE Transactions on Computers*, 65(9): 2794–2806, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chatterjee:2017:IPB**

[CMRH17] Sanjit Chatterjee, Alfred Menezes, and Francisco Rodríguez-Henríquez. On instantiating pairing-based protocols with elliptic curves of embedding degree one. *IEEE Transactions on Computers*, 66(6):1061–1070, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http:`

//ieeexplore.ieee.org/document/7762073/.

**Casale:2010:MDS**

[CMS10] G. Casale, Ningfang Mi, and E. Smirni. Model-driven system capacity planning under workload burstiness. *IEEE Transactions on Computers*, 59(1):66–80, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255229.

**Coelho:2018:CDB**

[CND+18] Diego F. G. Coelho, Sushmabhargavi Nimmalapalli, Vassil S. Dimitrov, Arjuna Madanayake, Renato J. Cintra, and Arnaud Tisserand. Computation of 2D $8 \times 8$ DCT based on the Loeffler factorization using algebraic integer encoding. *IEEE Transactions on Computers*, 67(12): 1692–1702, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8360454/.

**Cenk:2013:ITW**

[CNH13] Murat Cenk, Christophe Negre, and M. Anwar Hasan. Improved three-way split formulas for binary polynomial and Toeplitz matrix vector products. *IEEE Transactions on Computers*, 62 (7):1345–1361, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2014:PHA**

[CNJ14] Jian Chen, Arun Arvind Nair, and Lizy K. John. Predictive heterogeneity-aware application scheduling for chip multiprocessors. *IEEE Transactions on Computers*, 63(2):435–447, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Choi:2018:TRR**

[COLK18] Inhyuk Choi, Hyunggoy Oh, Young-Woo Lee, and Sungho Kang. Test resource reused debug scheme to reduce the post-silicon debug cost. *IEEE Transactions on Computers*, 67(12): 1835–1839, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8359333/.

**Cucinotta:2010:QCP**

[CP10] T. Cucinotta and L. Palopoli. QoS control for pipelines of tasks using multiple resources. *IEEE Transactions on Computers*, 59 (3):416–430, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184818.

**Chen:2016:TAO**

[CPL16] Wuhui Chen, Incheon Paik, and Zhenni Li. Tology-aware optimal data placement algorithm for network traffic optimization. *IEEE Transactions on Computers*, 65 (8):2603–2617, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2017:CAS**

[CPL17] Wuhui Chen, Incheon Paik, and Zhenni Li. Cost-aware streaming workflow allocation on geo-distributed data centers. *IEEE Transactions on Computers*, 66(2): 256–271, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Clark:2016:EMR**

[CPRH16] L. T. Clark, D. W. Patterson, C. Ramamurthy, and K. E. Holbert. An embedded microprocessor radiation hardened by microarchitecture and circuits. *IEEE Transactions on Computers*, 65(2):382–395, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cazorla:2010:PEP**

[CPS+10] F. J. Cazorla, A. Pajuelo, O. J. Santana, E. Fernandez, and M. Valero. On the problem of evaluating the performance of multiprogrammed workloads. *IEEE Transactions on Computers*, 59(12): 1722–1728, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432159.

**Chetto:2014:NES**

[CQ14] Maryline Chetto and Audrey Queudet. A note on EDF scheduling for real-time energy harvesting systems. *IEEE Transactions on Computers*, 63(4):1037–1040, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:DBL**

[CQW+15] Renhai Chen, Zhiwei Qin, Yi Wang, Duo Liu, Zili Shao, and Yong Guan. On-demand block-level address mapping in large-scale NAND flash storage systems. *IEEE Transactions on Computers*, 64(6):1729–1741, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cuesta:2013:IED**

[CRG+13] Blas Cuesta, Alberto Ros,

Maria E. Gomez, Antonio Robles, and Jose Duato. Increasing the effectiveness of directory caches by avoiding the tracking of noncoherent memory blocks. *IEEE Transactions on Computers*, 62(3):482–495, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cheng:2016:EPA**

[CRJZ16] D. Cheng, J. Rao, C. Jiang, and X. Zhou. Elastic power-aware resource provisioning of heterogeneous workloads in self-sustainable datacenters. *IEEE Transactions on Computers*, 65(2): 508–521, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cohen:2010:SAN**

[CRK10] I. Cohen, O. Rottenstreich, and I. Keslassy. Statistical approach to networks-on-chip. *IEEE Transactions on Computers*, 59(6): 748–761, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416678.

**Crowley:2014:UEP**

[Cro14] Mark Crowley. Using equilibrium policy gradients for spatiotemporal planning in forest ecosystem management. *IEEE Transactions on Computers*, 63(1):142–154, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2011:EMT**

[CS11a] Yawen Chen and Hong Shen. Embedding meshes and tori on double-loop networks of the same size. *IEEE Transactions on Computers*, 60 (8):1157–1168, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2011:PIM**

[CS11b] Zhimin Chen and Patrick Schaumont. A parallel implementation of Montgomery multiplication on multicore systems: Algorithm, analysis, and prototype. *IEEE Transactions on Computers*, 60(12):1692–1703, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669278.

**Chen:2015:MPF**

[CS15] Kang Chen and Haiying Shen. Maximizing P2P file access availability in mobile ad hoc networks though replication for efficient file sharing. *IEEE Transactions on Computers*, 64

(4):1029–1042, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2013:GEE**

[CSCW13] Po-Yuan Chen, Chin-Lung Su, Chao-Hsun Chen, and Cheng-Wen Wu. Generalization of an enhanced ECC methodology for low power PSRAM. *IEEE Transactions on Computers*, 62 (7):1318–1331, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chou:2011:DEF**

[CSJ+11] Chih-Hsun Chou, Kuo-Feng Ssu, Hewijin Christine Jiau, Wei-Tong Wang, and Chao Wang. A dead-end free topology maintenance protocol for geographic forwarding in wireless sensor networks. *IEEE Transactions on Computers*, 60(11): 1610–1621, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5601700`.

**Cardosa:2012:EST**

[CSPC12] Michael Cardosa, Aameek Singh, Himabindu Pucha, and Abhishek Chandra. Exploiting spatio-temporal tradeoffs for energy-aware MapReduce in the Cloud. *IEEE Transactions on Computers*, 61(12):1737–1751, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2013:UVS**

[CSS13] Zhimin Chen, Ambuj Sinha, and Patrick Schaumont. Using virtual secure circuit to protect embedded software from side-channel attacks. *IEEE Transactions on Computers*, 62(1):124–136, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:RDM**

[CSW+15] Ching-Yi Chen, Hsiu-Chuan Shih, Cheng-Wen Wu, Chih-He Lin, Pi-Feng Chiu, Shyh-Shyuan Sheu, and F. T. Chen. RRAM defect modeling and failure analysis based on march test and a novel squeeze-search scheme. *IEEE Transactions on Computers*, 64(1):1–11, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cederman:2013:SLF**

[CT13] Daniel Cederman and Philippas Tsigas. Supporting lock-free composition of concurrent data objects: Moving data between containers. *IEEE Transactions on Computers*, 62(9):1866–

1878, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:RSE**

[CTD+16] Juan Chen, Yuhua Tang, Yong Dong, Jingling Xue, Zhiyuan Wang, and Wenhao Zhou. Reducing static energy in supercomputer interconnection networks using topology-aware partitioning. *IEEE Transactions on Computers*, 65(8): 2588–2602, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2014:ECE**

[CTH14] Nai-Wen Chang, Cheng-Yen Tsai, and Sun-Yuan Hsieh. On 3-extra connectivity and 3-extra edge connectivity of folded hypercubes. *IEEE Transactions on Computers*, 63(6):1594–1600, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2017:TAS**

[CTL+17] Jiageng Chen, Jesen Teh, Zhe Liu, Chunhua Su, Azman Samsudin, and Yang Xiang. Towards accurate statistical analysis of security margins: New searching strategies for differential attacks. *IEEE Transactions on Computers*, 66(10): 1763–1777, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7914659/.

**Chuang:2013:PSD**

[CTS13] Yue-Ru Chuang, Hsueh-Wen Tseng, and Shiann-Tsong Sheu. A performance study of Discrete-Error-Checking Scheme (DECS) with the optimal division locations for IEEE 802.16-based multihop networks. *IEEE Transactions on Computers*, 62(12): 2354–2365, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2018:ROM**

[CvdBC18] Kuan-Hsun Chen, Georg von der Brüggen, and Jian-Jia Chen. Reliability optimization on multi-core systems with multi-tasking and redundant multi-threading. *IEEE Transactions on Computers*, 67(4):484–497, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8094023/.

**Causapruno:2015:ISA**

[CVGZ15] G. Causapruno, M. Vacca, M. Graziano, and M. Zamboni. Interleaving in systolic arrays: A throughput breakthrough. *IEEE Transactions on Computers*, 64(7):1940–1953, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2013:THE**

[CVH+13] Dehao Chen, Neil Vachharajani, Robert Hundt, Xinliang Li, Stephane Eranian, Wenguang Chen, and Weimin Zheng. Taming hardware event samples for precise and versatile feedback directed optimizations. *IEEE Transactions on Computers*, 62 (2):376–389, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Carretero:2010:MOT**

[CVMA10] J. Carretero, X. Vera, P. C. Monferrer, and J. Abella. Microarchitectural online testing for failure detection in memory order buffers. *IEEE Transactions on Computers*, 59(5):623–637, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255233`.

**Candel:2019:EMC**

[CVPS19] F. Candel, A. Valero, S. Petit, and J. Sahuquillo. Efficient management of cache accesses to boost GPGPU memory subsystem performance. *IEEE Transactions on Computers*, 68(10): 1442–1454, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2010:GCF**

[CW10] Yang Chang and Andy Wellings. Garbage collection for flexible hard real-time systems. *IEEE Transactions on Computers*, 59 (8):1063–1075, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5383349`.

**Cheng:2015:PCU**

[CW15] Yu-Chieh Cheng and Pi-Chung Wang. Packet classification using dynamically generated decision trees. *IEEE Transactions on Computers*, 64(2):582–586, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cheng:2016:SMM**

[CW16] Yu-Chieh Cheng and Pi-Chung Wang. Scalable multi-match packet classification using TCAM and SRAM. *IEEE Transactions on Computers*, 65(7):2257–2269, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cui:2015:RBS**

[CWCS15] Yan Cui, Yingxin Wang, Yu Chen, and Yuanchun Shi. Requester-based spin lock: A scalable and energy efficient locking scheme on multicore systems. *IEEE Transactions on Computers*, 64 (1):166–179, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Carvajal:2014:ECA**

[CWF14] Gonzalo Carvajal, Chun Wah Wu, and Sebastian Fischmeister. Evaluation of communication architectures for switched real-time Ethernet. *IEEE Transactions on Computers*, 63(1):218–229, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2017:HDS**

[CWL+17] Renhai Chen, Yi Wang, Duo Liu, Zili Shao, and Song Jiang. Heating dispersal for self-healing NAND flash memory. *IEEE Transactions on Computers*, 66 (2):361–367, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cheng:2013:RGL**

[CWTT13] Yuan-Po Cheng, Chia-Yi Wu, Yao-Jen Tang, and Ming-Jer Tsai. Retrieval-guaranteed location-aware information brokerage scheme in 3D wireless ad hoc networks. *IEEE Transactions on Computers*, 62(4):798–812, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cao:2014:CCA**

[CWX+14] Jiannong Cao, Hejun Wu, Wenzheng Xu, Weigang Wu, and Xiaola Lin. CACC: A cooperative approach to cache consistency in WMNs. *IEEE Transactions on Computers*, 63(4):860–873, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chu:2013:ECR**

[CWY13] Shan Chu, Xin Wang, and Yuanyuan Yang. Exploiting cooperative relay for high performance communications in MIMO ad hoc networks. *IEEE Transactions on Computers*, 62(4):716–729, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2011:FFT**

[CWZ11] Tingting Chen, Fan Wu, and Sheng Zhong. FITS: a finite-time reputation system for cooperation in wireless ad hoc networks. *IEEE Transactions on Computers*, 60(7):1045–1056, July 2011. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Chen:2013:DPN**

[CWZ13] Tingting Chen, Fan Wu, and Sheng Zhong. On designing protocols for noncooperative, multiradio channel assignment in multiple collision domains. *IEEE Transactions on Computers*, 62(7):1403–1416, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cai:2013:DTC**

[CWZC13] Zhiping Cai, Zhijun Wang, Kai Zheng, and Jiannong Cao. A distributed TCAM coprocessor architecture for integrated longest prefix matching, policy filtering, and content filtering. *IEEE Transactions on Computers*, 62(3):417–427, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:MCM**

[CXLL16] Zhiguang Chen, Nong Xiao, Yutong Lu, and Fang Liu. Me-CLOCK: A memory-efficient framework to implement replacement policies for large caches. *IEEE Transactions on Computers*, 65 (8):2665–2671, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2015:LVS**

[CXLX15] Wenzhi Chen, Lei Xu, Guoxi Li, and Yang Xiang. A lightweight virtualization solution for Android devices. *IEEE Transactions on Computers*, 64(10):2741–2751, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:SCS**

[CXYC16] Fei Chen, Tao Xiang, Yuanyuan Yang, and Sherman S. M. Chow. Secure cloud storage meets with secure network coding. *IEEE Transactions on Computers*, 65(6):1936–1948, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cesare:2013:MEE**

[CXZ13] Silvio Cesare, Yang Xiang, and Wanlei Zhou. Malwise — an effective and efficient classification system for packed and polymorphic malware. *IEEE Transactions on Computers*, 62 (6):1193–1206, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cho:2013:AGT**

[CYA13] Hansu Cho, Lochi Yu, and Samar Abdi. Automatic generation of transducer models for bus-based MPSoC design. *IEEE Transactions on*

*Computers*, 62(2):211–224, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chiu:2011:OSP**

[CYC11] Ge-Ming Chiu, Li-Hsing Yen, and Tai-Lin Chin. Optimal storage placement for tree-structured networks with heterogeneous channel costs. *IEEE Transactions on Computers*, 60(10): 1431–1444, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5661768.

**Chen:2016:PSA**

[CYC+16] D. D. Chen, G. X. Yao, R. C. Cheung, D. Pao, and C. K. Koc. Parameter space for the architecture of FFT-based Montgomery modular multiplication. *IEEE Transactions on Computers*, 65 (1):147–160, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2011:TTA**

[CYCC11] Yi-Jung Chen, Chia-Lin Yang, Jaw-Wei Chi, and Jian-Jia Chen. TACLC: Timing-aware cache leakage control for hard real-time systems. *IEEE Transactions on Computers*, 60(6):767–782, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2014:ACE**

[CYHC14] Yean-Ru Chen, Jia-Jen Yeh, Pao-Ann Hsiung, and Sao-Jie Chen. Accelerating coverage estimation through partial model checking. *IEEE Transactions on Computers*, 63(7):1613–1625, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chim:2014:VVB**

[CYHL14] T. W. Chim, S. M. Yiu, Lucas C. K. Hui, and Victor O. K. Li. VSPN: VANET-based secure and privacy-preserving navigation. *IEEE Transactions on Computers*, 63(2):510–524, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2010:TET**

[CYJ+10] Hanhua Chen, Jun Yan, Hai Jin, Yunhao Liu, and L. M. Ni. TSS: Efficient term set search in large peer-to-peer textual collections. *IEEE Transactions on Computers*, 59(7):969–980, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453340.

**Chung:2014:AIT**

[CYL+14] Eui-Young Chung, Sungjoo Yoo, Hyuk-Jun Lee, Kwanhu Bang, Dong gun Kim, and Sang-Hoon Park. An adaptive idle-time exploiting method for low latency NAND flash-based storage devices. *IEEE Transactions on Computers*, 63(5):1085–1096, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chang:2014:CSM**

[CZ14] J. Morris Chang and Zhiming Zhang. A cool scheduler for multi-core systems exploiting program phases. *IEEE Transactions on Computers*, 63(5):1061–1073, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Chen:2016:IEN**

[CZ16] Binglong Chen and Chang-An Zhao. An improvement of the elliptic net algorithm. *IEEE Transactions on Computers*, 65(9):2903–2909, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cheng:2017:CPR**

[CZL+17] Dazhao Cheng, Xiaobo Zhou, Palden Lama, Jun Wu, and Changjun Jiang. Cross-platform resource scheduling for Spark and MapReduce on YARN. *IEEE Transactions on Computers*, 66(8):1341–1353, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7857034/`.

**Chen:2016:ERC**

[CZP+16] Mingsong Chen, Xinqian Zhang, Geguang Pu, Xin Fu, and Prabhat Mishra. Efficient resource constrained scheduling using parallel structure-aware pruning techniques. *IEEE Transactions on Computers*, 65(7):2059–2073, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Cantoro:2018:TRM**

[CZP+18] Riccardo Cantoro, Farrokh Ghani Zadegan, Marco Palena, Paolo Pasini, Erik Larsson, and Matteo Sonza Reorda. Test of reconfigurable modules in scan networks. *IEEE Transactions on Computers*, 67(12):1806–1817, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8357895/`.

**Cheng:2019:TCI**

[CZS+19] Xi Cheng, Min Zhou, Xiaoyu Song, Ming Gu, and Jiaguang Sun. Tolerating C integer error via precision elevation. *IEEE Transactions on Computers*, 68 (2):270–286, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8443077/`.

**Devadas:2012:IVF**

[DA12] Vinay Devadas and Hakan Aydin. On the interplay of voltage/frequency scaling and device power management for frame-based real-time embedded applications. *IEEE Transactions on Computers*, 61(1):31–44, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**deAlbuquerque:2014:MMS**

[dAJM14] Edler Lins de Albuquerque, Orlando S. Junior, and Raquel A. F. Mini. MuSA: Multivariate sampling algorithm for wireless sensor networks. *IEEE Transactions on Computers*, 63(4):968–978, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dieguez:2018:SLP**

[DALD18] Adrián Pérez Diéguez, Margarita Amor, Jacobo Lobeiras, and Ramón Doallo. Solving large problem sizes of index-digit algorithms on GPU: FFT and tridiagonal system solvers. *IEEE Transactions on Computers*, 67 (1):86–101, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7970194/`.

**Dutta:2014:CIL**

[DAPS14] Parikshit Dutta, Elise Arnaud, Emmanuel Prados, and Mathieu Saujot. Calibration of an integrated land-use and transportation model using maximum-likelihood estimation. *IEEE Transactions on Computers*, 63(1):167–178, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dargie:2015:SME**

[Dar15] W. Dargie. A stochastic model for estimating the power consumption of a processor. *IEEE Transactions on Computers*, 64 (5):1311–1322, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dani:2014:TSW**

[DAS14] Aparna Mandke Dani, Bharadwaj Amrutur, and Y. N. Srikant. Toward a scalable working set size estimation method and its application for chip multiprocessors. *IEEE Transactions on Computers*, 63 (6):1567–1579, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dai:2017:ATE**

[DCCK17] Wangchen Dai, Donald Donglong Chen, Ray C. C. Cheung, and Çetin Kaya Koç. Area-time efficient architecture of FFT-based Montgomery multiplication. *IEEE Transactions on Computers*, 66(3):375–388, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dai:2018:FBM**

[DCCK18] Wangchen Dai, Donglong Chen, Ray C. C. Cheung, and Çetin Kaya Koç. FFT-based McLaughlin's Montgomery exponentiation without conditional selections. *IEEE Transactions on Computers*, 67(9): 1301–1314, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8307235/`.

**Distler:2016:REB**

[DCK16] Tobias Distler, Christian Cachin, and Rüdiger Kapitza. Resource-efficient Byzantine fault tolerance. *IEEE Transactions on Computers*, 65(9): 2807–2819, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dong:2011:SPF**

[DCL+11] Wei Dong, Chun Chen, Xue Liu, Yunhao Liu, Jiajun Bu, and Kougen Zheng. SenSpire OS: a predictable, flexible, and efficient operating system for wireless sensor networks. *IEEE Transactions on Computers*, 60(12): 1788–1801, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DellAmico:2016:PPS**

[DCM16] Matteo Dell'Amico, Damiano Carra, and Pietro Michiardi. PSBS: Practical size-based scheduling. *IEEE Transactions on Computers*, 65(7):2199–2212, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Doudalis:2012:EEM**

[DCV+12] Ioannis Doudalis, James Clause, Guru Venkataramani, Milos Prvulovic, and Alessandro Orso. Effective and efficient memory protection using dynamic tainting. *IEEE Transactions on Computers*, 61(1):87–100, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Deb:2013:DEE**

[DCY+13] Sujay Deb, Kevin Chang, Xinmin Yu, Suman Prasad Sah, Miralem Cosic, Amlan Ganguly, Partha Pratim Pande, Benjamin Belzer, and Deukhyoun Heo. Design of an energy-efficient CMOS-compatible NoC architecture with millimeter-wave wireless interconnects. *IEEE Transactions on Computers*, 62(12):2382–2396, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**deDinechin:2011:CFP**

[dDLM11] Florent de Dinechin, Christoph Lauter, and Guillaume Melquiond. Certifying the floating-point implementation of an elementary function using Gappa. *IEEE Transactions on Computers*, 60(2):242–253, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DAngelo:2014:FPM**

[DDN14] Gianlorenzo D'Angelo, Gabriele Di Stefano, and Alfredo Navarra. Flow problems in multi-interface networks. *IEEE Transactions on Computers*, 63(2):361–374, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DAngelo:2011:RRT**

[DDNP11] Gianlorenzo D'Angelo, Gabriele Di Stefano, Alfredo Navarra, and Cristina M. Pinotti. Recoverable robust timetables: An algorithmic approach on trees. *IEEE Transactions on Computers*, 60(3):433–446, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dutt:2019:AMO**

[DDNT19] Sunil Dutt, Satyabrata Dash, Sukumar Nandi, and Gaurav Trivedi. Analysis, modeling and optimization of equal segment based approximate adders. *IEEE Transactions on Computers*, 68(3):314–330, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8468108/`.

**DePestel:2017:LBE**

[DEE17] Sander De Pestel, Stijn Eyerman, and Lieven Eeckhout. Linear branch entropy: Characterizing and optimizing branch behavior in a micro-architecture independent way. *IEEE Transactions on Computers*, 66(3):458–472, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DiGuglielmo:2013:UUV**

[DFP+13] Luigi Di Guglielmo, Franco Fummi, Graziano Pravadelli, Francesco Stefanni, and Sara Vinco. UNIVERCM: The UNIversal VERsatile Computational Mode for heterogeneous system integration. *IEEE Transactions on Computers*, 62(2):225–241, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dietrich:2015:TSC**

[DGC+15] B. Dietrich, D. Goswami, S. Chakraborty, A. Guha, and M. Gries. Time series characterization of gaming workload for runtime power management. *IEEE Transactions on Computers*, 64 (1):260–273, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Du:2016:ACD**

[DHC+16] Bowen Du, Runhe Huang, Xi Chen, Zhipu Xie, Ye Liang, Weifeng Lv, and Jianhua Ma. Active CTDaaS: A data service framework based on transparent IoD in city traffic. *IEEE Transactions on Computers*, 65(12): 3524–3536, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DelBarrio:2016:PCS**

[DHM16] Alberto A. Del Barrio, Román Hermida, and Seda Ogrenci Memik. A partial carry-save on-the-fly correction multi-speculative multiplier. *IEEE Transactions on Computers*, 65(11):3251–3264, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dai:2019:HHV**

[DHW+19] G. Dai, T. Huang, Y. Wang, H. Yang, and J. Wawrzynek. HyVE: Hybrid vertex-edge memory hierarchy for energy-efficient graph processing. *IEEE Transactions on Computers*, 68(8):1131–1146, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dimitrov:2011:AEM**

[DJA11] Vassil S. Dimitrov, Kimmo U. Jarvinen, and Jithra Adikari. Area-efficient multipliers based on multiple-radix representations. *IEEE Transactions on Computers*, 60 (2):189–201, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dimitrov:2014:FIF**

[DJA14] Vassil Dimitrov, Kimmo Jarvinen, and Reza Azarderakhsh. Fast inversion in

GF($2^m$) with normal basis using hybrid-double multipliers. *IEEE Transactions on Computers*, 63(4):1041–1047, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dimitrov:2008:PSP**

[DJJ+08] V. S. Dimitrov, K. U. Jarvinen, M. J. Jacobson, W. Chan, and Zhun Huang. Provably sublinear point multiplication on Koblitz curves and its hardware implementation. *IEEE Transactions on Computers*, 57 (11):1469–1481, November 2008. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=4487060`. See comments [Lee12].

**Dutta:2017:NLC**

[DJN17] Avijit Dutta, Ashwin Jha, and Mridul Nandi. A new look at counters: Don't run like Marathon in a hundred meter race. *IEEE Transactions on Computers*, 66(11): 1851–1864, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7936672/`.

**Dubach:2011:EAC**

[DJO11] Christophe Dubach, Timothy M. Jones, and Michael F. P. O'Boyle. An empirical architecture-centric approach to microarchitectural design space exploration. *IEEE Transactions on Computers*, 60(10): 1445–1458, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5674026`.

**Dimitrakopoulos:2013:MSA**

[DKG13] Giorgos Dimitrakopoulos, Emmanouil Kalligeros, and Kostas Galanopoulos. Merged switch allocation and traversal in network-on-chip switches. *IEEE Transactions on Computers*, 62(10):2001–2012, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Deshpande:2013:RRF**

[DKH+13] Mayur Deshpande, Kyungbaek Kim, Bijit Hore, Sharad Mehrotra, and Nalini Venkatasubramanian. ReCREW: A reliable flash-dissemination system. *IEEE Transactions on Computers*, 62(7):1432–1446, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dumitriu:2016:RTR**

[DKK16] Victor Dumitriu, Lev Kirischian, and Valeri Kirischian. Runtime recovery mechanism for transient and permanent hardware faults based on distributed, self-organized dynamic partially reconfigurable systems. *IEEE Transactions on Computers*, 65(9): 2835–2847, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DiTomaso:2015:RPE**

[DKLB15] D. DiTomaso, A. K. Kodi, A. Louri, and R. Bunescu. Resilient and power-efficient multi-function channel buffers in network-on-chip architectures. *IEEE Transactions on Computers*, 64(12): 3555–3568, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Di:2015:OCC**

[DKW15] Sheng Di, D. Kondo, and Cho-Li Wang. Optimization of composite cloud service processing with virtual machines. *IEEE Transactions on Computers*, 64 (6):1755–1768, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dong:2013:RIR**

[DLC+13] Wei Dong, Yunhao Liu, Chun Chen, Jiajun Bu, Chao Huang, and Zhiwei Zhao. R2: Incremental reprogramming using relocatable code in networked embedded systems. *IEEE Transactions on Computers*, 62(9): 1837–1849, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dong:2012:DCW**

[DLL+12] Dezun Dong, Xiangke Liao, Kebin Liu, Yunhao Liu, and Weixia Xu. Distributed coverage in wireless ad hoc and sensor networks by topological graph approaches. *IEEE Transactions on Computers*, 61(10):1417–1428, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Saint-Genies:2017:ELT**

[dLSGDR17] Hugues de Lassus Saint-Geniès, David Defour, and Guillaume Revy. Exact lookup tables for the evaluation of trigonometric and hyperbolic functions. *IEEE Transactions on Computers*, 66(12):2058–2071, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7927421/`.

**DiFrancesco:2015:SMA**

[DMA+15] P. Di Francesco, S. McGettrick, U. K. Anyanwu, J. C. O'Sullivan, A. B. MacKenzie, and L. A. DaSilva. A split MAC approach for SDR platforms. *IEEE Transactions on Computers*, 64 (4):912–924, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dehyadegari:2015:AST**

[DMK+15] M. Dehyadegari, A. Marongiu, M. R. Kakoee, S. Mohammadi, N. Yazdani, and L. Benini. Architecture support for tightly-coupled multi-core clusters with shared-memory HW accelerators. *IEEE Transactions on Computers*, 64(8): 2132–2144, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dahir:2014:MTP**

[DMXY14] Nizar S. Dahir, Terrence Mak, Fei Xia, and Alexandre Yakovlev. Modeling and tools for power supply variations analysis in networks-on-chip. *IEEE Transactions on Computers*, 63(3):679–690, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Daoud:2011:ULC**

[DN11] Ehab Anis Daoud and Nicola Nicolici. On using lossy compression for repeatable experiments during silicon debug. *IEEE Transactions on Computers*, 60(7):937–950, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Demmel:2015:PRS**

[DN15] J. Demmel and Hong Diep Nguyen. Parallel reproducible summation. *IEEE Transactions on Computers*, 64(7):2060–2070, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Duckham:2011:EDC**

[DNSS11] Matt Duckham, Doron Nussbaum, Jorg-Rudiger Sack, and Nicola Santoro. Efficient, decentralized computation of the topology of spatial regions. *IEEE Transactions on Computers*, 60 (8):1100–1113, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**deOliveira:2016:EMR**

[dOPSR16] D. A. G. de Oliveira, L. L. Pilla, T. Santini, and P. Rech. Evaluation and mitigation of radiation-induced soft errors in graphics processing units. *IEEE Transactions on Computers*, 65 (3):791–804, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Doroz:2015:AFH**

[DOS15] Y. Doroz, E. Ozturk, and B. Sunar. Accelerating fully homomorphic encryption in hardware. *IEEE Transactions on Computers*, 64 (6):1509–1521, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dabosville:2013:NSO**

[DP13] Guillaume Dabosville and Emmanuel Prouff. A new second-order side channel attack based on linear regression. *IEEE Transactions on Computers*, 62(8): 1629–1640, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DeMara:2017:GEI**

[DPO17] Ronald F. DeMara, Marco Platzner, and Marco Ottavi. Guest editorial: *IEEE Transactions on Computers* and *IEEE Transactions on Emerging Topics in Computing* joint special section on innovation in reconfigurable computing fabrics from devices to architectures. *IEEE Transactions on Computers*, 66(6):927–929, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**DiCarlo:2011:SBS**

[DPS11] Stefano Di Carlo, Paolo Prinetto, and Alessandro Savino. Software-based self-test of set-associative cache memories. *IEEE Transactions on Computers*, 60 (7):1030–1044, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Drane:2014:SCF**

[DRC14] T. A. Drane, T. M. Rose, and G. A. Constantinides. On the systematic creation of faithfully rounded truncated multipliers and arrays. *IEEE Transactions on Computers*, 63(10):2513–2525, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Demirci:2016:UEP**

[DRM16] Mustafa Demirci, Pedro Reviriego, and Juan Antonio Maestro. Unequal error protection codes derived from double error correction orthogonal Latin square codes. *IEEE Transactions on Computers*, 65(9): 2932–2938, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Du:2016:OTC**

[DRS+16] Boyang Du, Matteo Sonza Reorda, Luca Sterpone, Luis Parra, Marta Portela-García, Almudena Lindoso, and Luis Entrena. Online test of control flow errors: A new debug interface-based approach. *IEEE Transactions on Computers*, 65

(6):1846–1855, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**deRooij:2012:ARD**

[dRV12] Steven de Rooij and Paul M. B. Vitanyi. Approximating rate-distortion graphs of individual data: Experiments in lossy compression and denoising. *IEEE Transactions on Computers*, 61 (3):395–407, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Doche:2014:NIM**

[DS14] Christophe Doche and Daniel Sutantyo. New and improved methods to analyze and compute double-scalar multiplications. *IEEE Transactions on Computers*, 63 (1):230–242, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dey:2013:KAT**

[DSB13] Soumyajit Dey, Dipankar Sarkar, and Anupam Basu. A Kleene algebra of tagged system actors for reasoning about heterogeneous embedded systems. *IEEE Transactions on Computers*, 62(10): 1917–1931, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Di:2019:MRI**

[DSG+19] Yejia Di, Liang Shi, Congming Gao, Qiao Li, Chun Jason Xue, and Kaijie Wu. Minimizing retention induced refresh through exploiting process variation of flash memory. *IEEE Transactions on Computers*, 68(1):83–98, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8423091/`.

**Deniz:2015:MPA**

[DSKH15] E. Deniz, A. Sen, B. Kahne, and J. Holt. MINIME: Pattern-aware multicore benchmark synthesizer. *IEEE Transactions on Computers*, 64(8):2239–2252, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dubois:2013:EFD**

[DSPB13] Florentine Dubois, Abbas Sheibanyrad, Frederic Petrot, and Maryam Bahmani. Elevator-first: A deadlock-free distributed routing algorithm for vertically partially connected 3D-NoCs. *IEEE Transactions on Computers*, 62 (3):609–615, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Datta:2015:PSO**

[DSR15] K. Datta, I. Sengupta, and H. Rahaman. A post-synthesis optimization technique for reversible circuits exploiting negative control lines. *IEEE Transactions on Computers*, 64(4):1208–1214, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dillow:2014:CGC**

[DSW+14] David A. Dillow, Galen M. Shipman, Feiyi Wang, Sarp Oral, Junghee Lee, and Youngjae Kim. Coordinating garbage collection for arrays of solid-state drives. *IEEE Transactions on Computers*, 63(4):888–901, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Du:2015:PTD**

[DSY+15] A. Y. Du, S. D. Smith, Z. Yang, C. Qiao, and R. Ramesh. Predicting transient downtime in virtual server systems: An efficient sample path randomization approach. *IEEE Transactions on Computers*, 64(12): 3541–3554, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dumas:2014:NRI**

[Dum14] Jean-Guillaume Dumas. On Newton–Raphson iteration for multiplicative inverses modulo prime powers. *IEEE Transactions on Computers*, 63(8):2106–2109, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See corrections [Wal19].

**Duan:2014:MCB**

[DVUS14] Qi Duan, Mohit Virendra, Shambhu Upadhyaya, and Ameya Sanzgiri. Minimum cost blocking problem in multi-path wireless routing protocols. *IEEE Transactions on Computers*, 63(7):1765–1777, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Duarte:2010:CBM**

[DW10] F. Duarte and S. Wong. Cache-based memory copy hardware accelerator for multicore systems. *IEEE Transactions on Computers*, 59(11):1494–1507, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416684`.

**Dai:2018:OPC**

[DWZ18] Wei Dai, William Whyte, and Zhenfei Zhang. Optimizing polynomial convolution for NTRUEncrypt. *IEEE Transactions on Computers*, 67(11):1572–1583, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8303667/.

**Deng:2012:OAC**

[DY12] Xi Deng and Yuanyuan Yang. Online adaptive compression in delay sensitive wireless sensor networks. *IEEE Transactions on Computers*, 61(10): 1429–1442, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Du:2014:IES**

[DY14] Yuyang Du and Hongliang Yu. Increasing endurance and security of phase-change memory with multi-way wear-leveling. *IEEE Transactions on Computers*, 63 (5):1157–1168, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ding:2016:EST**

[DYCG16] Z. Ding, B. Yang, Y. Chi, and L. Guo. Enabling smart transportation systems: A parallel spatio-temporal database approach. *IEEE Transactions on Computers*, 65(5):1377–1391, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**D:2016:QLB**

[DYHX16] S. M. P. D., H. Yu, H. Huang, and D. Xu. A Q-learning based self-adaptive I/O communication for 2.5D integrated many-core microprocessor and memory. *IEEE Transactions on Computers*, 65(4):1185–1196, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Dai:2019:NNN**

[DYJ19] X. Dai, H. Yin, and N. K. Jha. NeST: A neural network synthesis tool based on a grow-and-prune paradigm. *IEEE Transactions on Computers*, 68(10):1487–1497, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**D:2015:MCM**

[DYW15] S. Manoj P. D., H. Yu, and K. Wang. 3D many-core microprocessor power management by space-time multiplexing based demand-supply matching. *IEEE Transactions on Computers*, 64(11):3022–3036, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ding:2010:LBI**

[DZ10] Yiqiang Ding and Wei Zhang. Loop-based instruction prefetching to reduce the worst-case execution time. *IEEE Transactions on Computers*, 59 (6):855–864, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416687`.

**Deng:2016:ECD**

[DZD+16] Peng Deng, Qi Zhu, Abhijit Davare, Anastasios Mourikis, Xue Liu, and Marco Di Natale. An efficient control-driven period optimization algorithm for distributed real-time systems. *IEEE Transactions on Computers*, 65(12): 3552–3566, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Duan:2014:CED**

[DZLP14] Lide Duan, Ying Zhang, Bin Li, and Lu Peng. Comprehensive and efficient design parameter selection for soft error resilient processors via universal rules. *IEEE Transactions on Computers*, 63(9): 2201–2214, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Elarief:2013:LME**

[EBE13] Noha Elarief, Bella Bose, and Samir Elmougy. Limited magnitude error detecting codes over $Z_q$. *IEEE Transactions on Computers*, 62(5):984–989, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Egger:2016:ECL**

[ECJ+16] Bernhard Egger, Younghyun Cho, Changyeon Jo, Eunbyun Park, and Jaejin Lee. Efficient checkpointing of live virtual machines. *IEEE Transactions on Computers*, 65(10):3041–3054, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ebrahimi:2014:PBP**

[EDL+14] Masoumeh Ebrahimi, Masoud Daneshtalab, Pasi Liljeberg, Juha Plosila, Jose Flich, and Hannu Tenhunen. Path-based partitioning methods for 3D networks-on-chip with minimal adaptive routing. *IEEE Transactions on Computers*, 63(3):718–733, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Eyerman:2010:CAO**

[EE10] S. Eyerman and L. Eeckhout. A counter architecture for online DVFS profitability estimation. *IEEE Transactions on Computers*, 59(11): 1576–1583, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432162.

**Ecco:2017:TBT**

[EE17] Leonardo Ecco and Rolf Ernst. Tackling the bus turnaround overhead in real-time SDRAM controllers. *IEEE Transactions on Computers*, 66(11):1961–1974, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7946168/.

**Etsion:2012:ECW**

[EF12] Yoav Etsion and Dror G. Feitelson. Exploiting core working sets to filter the L1 cache with random sampling. *IEEE Transactions on Computers*, 61(11):1535–1550, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Espitau:2018:LAF**

[EFGT18] Thomas Espitau, Pierre-Alain Fouque, Benoît Gérard, and Mehdi Tibouchi. Loop-abort faults on lattice-based signature schemes and key exchange protocols. *IEEE Transactions on Computers*, 67(11):1535–1549, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8354897/.

**Esposito:2016:SCS**

[EFPC16] Christian Esposito, Massimo Ficco, Francesco Palmieri, and Aniello Castiglione. Smart cloud storage service selection based on fuzzy logic, theory of evidence and game theory. *IEEE Transactions on Computers*, 65 (8):2348–2362, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Eppstein:2011:SGG**

[EG11] David Eppstein and Michael T. Goodrich. Succinct greedy geometric routing using hyperbolic geometry. *IEEE Transactions on Computers*, 60(11):1571–1580, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669279.

**Entrena:2012:SES**

[EGVFC+12] L. Entrena, M. Garcia-Valderas, R. Fernandez-Cardenal, A. Lindoso, M. Portela Garcia, and C. Lopez-Ongil.

Soft error sensitivity evaluation of microprocessors by multilevel emulation-based fault injection. *IEEE Transactions on Computers*, 61 (3):313–322, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ebergen:2015:RDA**

[EJ15] J. Ebergen and N. Jamadagni. Radix-2 division algorithms with an over-redundant digit set. *IEEE Transactions on Computers*, 64(9):2652–2663, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ebrahimi:2017:PPE**

[EKA17] Zahra Ebrahimi, Behnam Khaleghi, and Hossein Asadi. PEAF: A power-efficient architecture for SRAM-based FPGAs using reconfigurable hard logic design in dark silicon era. *IEEE Transactions on Computers*, 66 (6):982–995, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7775010/`.

**Egger:2010:SMM**

[EKJ+10] Bernhard Egger, Seungkyun Kim, Choonki Jang, Jaejin Lee, Sang Lyul Min, and Heonshik Shin. Scratch-pad memory management techniques for code in embedded systems without an MMU. *IEEE Transactions on Computers*, 59(8): 1047–1062, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374377`.

**Elnozahy:2012:GEI**

[EM12] Mootaz Elnozahy and Rami Melhem. Guest Editors' introduction: Special section on energy efficient computing. *IEEE Transactions on Computers*, 61(12): 1666–1667, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**El-Razouk:2016:NAD**

[ERRM16] Hayssam El-Razouk and Arash Reyhani-Masoleh. New architectures for digit-level single, hybrid-double, hybrid-triple field multiplications and exponentiation using Gaussian normal bases. *IEEE Transactions on Computers*, 65(8):2495–2509, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**El-Razouk:2015:NHI**

[ERRMG15] H. El-Razouk, A. Reyhani-Masoleh, and Guang Gong.

New hardware implementations of WG and WG-StreamCiphers using polynomial basis. *IEEE Transactions on Computers*, 64 (7):2020–2035, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Eghbal:2015:AFT**

[EYBK15] A. Eghbal, P. M. Yaghini, N. Bagherzadeh, and M. Khayambashi. Analytical fault tolerance assessment and metrics for TSV-based 3D network-on-chip. *IEEE Transactions on Computers*, 64(12):3591–3604, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Flores:2010:HIE**

[FAA10] A. Flores, J. L. Aragon, and M. E. Acacio. Heterogeneous interconnects for energy-efficient message management in CMPs. *IEEE Transactions on Computers*, 59 (1):16–28, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5235139.

**Fedorov:2016:FAE**

[FAK16] Viacheslav V. Fedorov, Monther Abusultan, and Sunil P. Khatri. FTCAM: An area-efficient flash-based ternary CAM design. *IEEE Transactions on Computers*, 65 (8):2652–2658, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fan:2016:CRT**

[Fan16] H. Fan. A Chinese remainder theorem approach to bit-parallel polynomial basis multipliers for irreducible trinomials. *IEEE Transactions on Computers*, 65 (2):343–352, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Flahive:2013:RPG**

[FB13] Mary Flahive and Bella Bose. On resource placement in Gaussian and EJ interconnection networks. *IEEE Transactions on Computers*, 62(3):623–626, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ferguson:2018:DSM**

[FBE+18] Warren E. Ferguson, Jesse Bingham, Levent Erkök, John R. Harrison, and Joe Leslie-Hurd. Digit serial methods with applications to division and square root. *IEEE Transactions on Computers*, 67 (3):449–456, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8060979/.

**Ferrer:2012:PCM**

[FBR+12] Joan-Lluis Ferrer, Elvira Baydal, Antonio Robles, Pedro Lopez, and Jose Duato. Progressive congestion management based on packet marking and validation techniques. *IEEE Transactions on Computers*, 61(9): 1296–1310, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fensch:2013:DPL**

[FBWMM13] Christian Fensch, Nick Barrow-Williams, Robert D. Mullins, and Simon Moore. Designing a physical locality aware coherence protocol for chip-multiprocessors. *IEEE Transactions on Computers*, 62(5):914–928, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fettes:2019:DVF**

[FCB+19] Quintin Fettes, Mark Clark, Razvan Bunescu, Avinash Karanth, and Ahmed Louri. Dynamic voltage and frequency scaling in NoCs with supervised and reinforcement learning techniques. *IEEE Transactions on Computers*, 68 (3):375–389, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8489913/.

**Favalli:2016:BPB**

[FD16] M. Favalli and M. Dalpasso. Boolean and pseudo-Boolean test generation for feedback bridging faults. *IEEE Transactions on Computers*, 65 (3):706–715, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Feng:2018:PCA**

[FEM+18] Huan Feng, David Eyers, Steven Mills, Yongwei Wu, and Zhiyi Huang. Principal component analysis based filtering for scalable, high precision $k$-NN search. *IEEE Transactions on Computers*, 67(2):252–267, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8024082/.

**Feng:2014:ISE**

[Fen14] Gang Feng. Improving space efficiency with path length prediction for finding $k$ shortest simple paths. *IEEE Transactions on Computers*, 63(10):2459–2472, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Flocchini:2012:DMS**

[FEP+12] Paola Flocchini, T. Mesa Enriquez, Linda Pagli, Giuseppe Prencipe, and Nicola Santoro. Distributed minimum spanning tree maintenance for transient node failures. *IEEE Transactions on Computers*, 61(3):408–414, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fritz:2016:IPM**

[FF16] Christopher Fritz and Adly T. Fam. Interlaced partition multiplier. *IEEE Transactions on Computers*, 65 (8):2672–2677, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fanelli:2014:SAC**

[FFCB14] M. Fanelli, L. Foschini, A. Corradi, and A. Boukerche. Self-adaptive context data management in large-scale mobile systems. *IEEE Transactions on Computers*, 63(10):2549–2562, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Farmahini-Farahani:2013:MDH**

[FFISC13] Amin Farmahini-Farahani, Henry J. Duwe III, Michael J. Schulte, and Katherine Compton. Modular design of high-throughput, low-latency sorting units. *IEEE Transactions on Computers*, 62(7):1389–1402, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Friedrichs:2018:MCC**

[FFL18] Stephan Friedrichs, Matthias Függer, and Christoph Lenzen. Metastability-containing circuits. *IEEE Transactions on Computers*, 67(8):1167–1183, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8314764/`.

**Francalanci:2010:HPD**

[FG10] C. Francalanci and P. Giacomazzi. A high-performance deadlock-free multicast routing algorithm for $K$-ary $N$-cubes. *IEEE Transactions on Computers*, 59 (2):174–187, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5156496`.

**Franchini:2013:DIE**

[FGS+13] Silvia Franchini, Antonio Gentile, Filippo Sorbello, Giorgio Vassallo, and Salvatore Vitabile. Design and implementation of an embedded coprocessor with native support for 5D, quadruple-based Clifford algebra. *IEEE Transactions on Computers*, 62(12):2366–2381, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Franchini:2015:CCG**

[FGS+15] S. Franchini, A. Gentile, F. Sorbello, G. Vassallo, and S. Vitabile. ConformalALU: A conformal geometric algebra coprocessor for medical image processing. *IEEE Transactions on Computers*, 64(4):955–970, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fan:2010:AMI**

[FHH10] Chun-I Fan, Ling-Ying Huang, and Pei-Hsiu Ho. Anonymous multireceiver identity-based encryption. *IEEE Transactions on Computers*, 59(9):1239–1249, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396332`.

**Fu:2018:SAR**

[FHL+18] Min Fu, Shujie Han, Patrick P. C. Lee, Dan Feng, Zuoning Chen, and Yu Xiao. A simulation analysis of redundancy and reliability in primary storage deduplication. *IEEE Transactions on Computers*, 67(9):1259–1272, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8300656/`.

**Faz-Hernandez:2018:FSI**

[FHLOJRH18] Armando Faz-Hernández, Julio López, Eduardo Ochoa-Jiménez, and Francisco Rodríguez-Henríquez. A faster software implementation of the supersingular isogeny Diffie–Hellman key exchange protocol. *IEEE Transactions on Computers*, 67(11):1622–1636, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8100879/`.

**Fan:2014:ASA**

[FHR14] Chun-I Fan, Vincent Shi-Ming Huang, and He-Ming Ruan. Arbitrary-state attribute-based encryption with dynamic membership. *IEEE Transactions on Computers*, 63(8):1951–1961, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Frankel:2018:QBE**

[FHW18] Binyamin Frankel, Roi Herman, and Shmuel Wimer.

Queuing-based eDRAM refreshing for ultra-low power processors. *IEEE Transactions on Computers*, 67(9): 1331–1340, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8310027/`.

**Finkler:2010:AFD**

[Fin10] U. Finkler. An analytic framework for detailed resource profiling in large and parallel programs and its application for memory use. *IEEE Transactions on Computers*, 59 (3):358–370, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5276794`.

**Fernandez:2017:CSC**

[FJA+17] Gabriel Fernandez, Javier Jalle, Jaume Abella, Eduardo Quiñones, Tullio Vardanega, and Francisco J. Cazorla. Computing safe contention bounds for multicore resources with round-robin and FIFO arbitration. *IEEE Transactions on Computers*, 66(4):586–600, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Feng:2015:FMC**

[FK15] Gang Feng and T. Korkmaz. Finding multi-constrained multiple shortest paths. *IEEE Transactions on Computers*, 64(9): 2559–2572, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Farbeh:2016:FED**

[FKMK16] Hamed Farbeh, Hyeonggyu Kim, Seyed Ghassem Miremadi, and Soontae Kim. Floating-ECC: Dynamic repositioning of error correcting code bits for extending the lifetime of STT-RAM caches. *IEEE Transactions on Computers*, 65(12): 3661–3675, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Faltings:2014:IMC**

[FLJ14] Boi Faltings, Jason Jingshi Li, and Radu Jurca. Incentive mechanisms for community sensing. *IEEE Transactions on Computers*, 63 (1):115–128, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Feng:2014:PCO**

[FLL14] Yuan Feng, Baochun Li, and Bo Li. Price competition in an oligopoly market with multiple IaaS cloud providers. *IEEE Transactions on Computers*, 63 (1):59–73, January 2014.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Furber:2013:OSS**

[FLP+13] Steve B. Furber, David R. Lester, Luis A. Plana, Jim D. Garside, Eustace Painkras, Steve Temple, and Andrew D. Brown. Overview of the SpiNNaker system architecture. *IEEE Transactions on Computers*, 62(12): 2454–2467, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ferrara:2016:SBO**

[FLS16] Andrea Ferrara, Paolo Liberatore, and Marco Schaerf. The size of BDDs and other data structures in temporal logics model checking. *IEEE Transactions on Computers*, 65(10):3148–3156, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Firdaus:2016:NPV**

[FM16] A. Fajar Firdaus and M. Meribout. A new parallel VLSI architecture for real-time electrical capacitance tomography. *IEEE Transactions on Computers*, 65 (1):30–41, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Farahmandi:2019:ATG**

[FM19] Farimah Farahmandi and Prabhat Mishra. Automated test generation for debugging multiple bugs in arithmetic circuits. *IEEE Transactions on Computers*, 68 (2):182–197, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8453904/.

**Fu:2010:FDO**

[FML10] Haohuan Fu, O. Mencer, and W. Luk. FPGA designs with optimized logarithmic arithmetic. *IEEE Transactions on Computers*, 59(7):1000–1006, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416693.

**Fabiano:2019:ATW**

[FMP19] Nicolas Fabiano, Jean-Michel Muller, and Joris Picot. Algorithms for triple-word arithmetic. *IEEE Transactions on Computers*, 68(11):1573–1583, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://hal.archives-ouvertes.fr/hal-01869009v2.

**Fadlullah:2019:VIA**

[FMTK19] Z. M. Fadlullah, B. Mao, F. Tang, and N. Kato. Value iteration architecture based deep learning for intelligent routing exploiting heterogeneous computing platforms. *IEEE Transactions on Computers*, 68(6):939–950, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fugger:2016:UGP**

[FNS16] M. Fugger, T. Nowak, and U. Schmid. Unfaithful glitch propagation in existing binary circuit models. *IEEE Transactions on Computers*, 65(3):964–978, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Find:2019:BCB**

[FP19] Magnus Gaudal Find and René Peralta. Better circuits for binary polynomial multiplication. *IEEE Transactions on Computers*, 68(4):624–630, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8494787/`.

**Fyrbiak:2018:HOP**

[FRB+18] Marc Fyrbiak, Simon Rokicki, Nicolai Bissantz, Russell Tessier, and Christof Paar. Hybrid obfuscation to protect against disclosure attacks on embedded microprocessors. *IEEE Transactions on Computers*, 67(3):307–321, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7809080/`.

**Frey:2010:DGW**

[FS10] H. Frey and I. Stojmenovic. On delivery guarantees and worst-case forwarding bounds of elementary face routing components in ad hoc and sensor networks. *IEEE Transactions on Computers*, 59(9):1224–1238, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467049`.

**Ferreron:2016:CSC**

[FSGAB+16] A. Ferreron, D. Suarez-Gracia, J. Alastruey-Benede, T. Monreal-Arnal, and P. Ibanez. Concertina: Squeezing in cache content to operate at near-threshold voltage. *IEEE Transactions on Computers*, 65(3):755–769, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fu:2017:SCE**

[FSL+17] Yingxun Fu, Jiwu Shu, Xianghong Luo, Zhirong Shen, and Qingda Hu. Short code: An efficient RAID-6 MDS code for optimizing degraded reads and partial stripe writes. *IEEE Transactions on Computers*, 66 (1):127–137, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Feliu:2016:BAL**

[FSPD16] J. Feliu, J. Sahuquillo, S. Petit, and J. Duato. Bandwidth-aware on-line scheduling in SMT multicores. *IEEE Transactions on Computers*, 65(2):422–434, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Feliu:2017:PFP**

[FSPD17] J. Feliu, J. Sahuquillo, S. Petit, and J. Duato. Perf Fair: A progress-aware scheduler to enhance performance and fairness in SMT multicores. *IEEE Transactions on Computers*, 66(5):905–911, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fraccaroli:2018:NSD**

[FSR+18] Enrico Fraccaroli, Francesco Stefanni, Romeo Rizzi, Davide Quaglia, and Franco Fummi. Network synthesis for distributed embedded systems. *IEEE Transactions on Computers*, 67(9): 1315–1330, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8307094/`.

**Foroutan:2013:ICT**

[FTP13] Sahar Foroutan, Yvain Thonnart, and Frederic Petrot. An iterative computational technique for performance evaluation of networks-on-chip. *IEEE Transactions on Computers*, 62(8):1641–1655, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fujita:2011:BBA**

[Fuj11] Satoshi Fujita. A branch-and-bound algorithm for solving the multiprocessor scheduling problem with improved lower bounding techniques. *IEEE Transactions on Computers*, 60(7):1006–1016, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fan:2012:EHI**

[FVV12] Junfeng Fan, Frederik Vercauteren, and Ingrid Verbauwhede. Efficient hardware implementation of

FP-arithmetic for pairing-friendly curves. *IEEE Transactions on Computers*, 61(5): 676–685, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fang:2014:AEA**

[FYSK14] Hua-Wei Fang, Mi-Yen Yeh, Pei-Lun Suei, and Tei-Wei Kuo. An adaptive endurance-aware $B^+$-tree for flash memory storage systems. *IEEE Transactions on Computers*, 63(11): 2661–2673, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Fang:2014:MRP**

[FZL+14] Kun Fang, Hongzhong Zheng, Jiang Lin, Zhao Zhang, and Zhichun Zhu. Mini-Rank: A power-efficient DDRx DRAM memory architecture. *IEEE Transactions on Computers*, 63 (6):1500–1512, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guralnik:2011:SBV**

[GABK11] Elena Guralnik, Merav Aharoni, Ariel J. Birnbaum, and Anatoly Koyfman. Simulation-based verification of floating-point division. *IEEE Transactions on Computers*, 60(2):176–188, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guzzi:2014:CPP**

[GAC14] P. H. Guzzi, G. Agapito, and M. Cannataro. coreSNP: Parallel processing of microarray data. *IEEE Transactions on Computers*, 63 (12):2961–2974, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ghandali:2015:AHL**

[GAFN15] S. Ghandali, B. Alizadeh, M. Fujita, and Z. Navabi. Automatic high-level data-flow synthesis and optimization of polynomial datapaths using functional decomposition. *IEEE Transactions on Computers*, 64 (6):1579–1593, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ghaderi:2018:SSA**

[GBA18a] Zana Ghaderi, Nader Bagherzadeh, and Ahmad Albaqsami. STABLE: Stress-aware Boolean matching to mitigate BTI-induced SNM reduction in SRAM-based FPGAs. *IEEE Transactions on Computers*, 67(1):102–114, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7974802/`.

**Gupta:2018:OLM**

[GBA+18b] Ujjwal Gupta, Manoj Babu, Raid Ayoub, Michael Kishinevsky, Francesco Paterna, Suat Gumussoy, and Umit Y. Ogras. An online learning methodology for performance modeling of graphics processors. *IEEE Transactions on Computers*, 67(12): 1677–1691, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8365819/`.

**Gottscho:2015:VHP**

[GBD+15] M. Gottscho, L. A. D. Bathen, N. Dutt, A. Nicolau, and P. Gupta. ViPZonE: Hardware power variability-aware virtual memory management for energy savings. *IEEE Transactions on Computers*, 64(5):1483–1496, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gorshkov:2019:GIH**

[GBF+19] A. V. Gorshkov, M. Berezalsky, J. Fedorova, K. Levit-Gurevich, and N. Itzhaki. GPU instruction hotspots detection based on binary instrumentation approach. *IEEE Transactions on Computers*, 68(8):1213–1224, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2018:SMI**

[GBGI18] Xiaochen Guo, Mahdi Nazm Bojnordi, Qing Guo, and Engin Ipek. Sanitizer: Mitigating the impact of expensive ECC checks on STT-MRAM based main memories. *IEEE Transactions on Computers*, 67(6):847–860, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8126226/`.

**Garibotti:2016:EES**

[GBO+16] Rafael Garibotti, Anastasiia Butko, Luciano Ost, Abdoulaye Gamatié, Gilles Sassatelli, and Chris Adeniyi-Jones. Efficient embedded software migration towards clusterized distributed-memory architectures. *IEEE Transactions on Computers*, 65 (8):2645–2651, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ginosar:2014:GEI**

[GC14] Ran Ginosar and Karam S. Chatha. Guest Editors' introduction — special issue on network-on-chip. *IEEE Transactions on Computers*, 63(3):527–528, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gong:2016:ERE**

[GC16] Y. H. Gong and S. W. Chung. Exploiting refresh effect of DRAM read operations: A practical approach to low-power refresh. *IEEE Transactions on Computers*, 65(5):1507–1517, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Goossens:2016:PPT**

[GCAG16] Sven Goossens, Karthik Chandrasekar, Benny Akesson, and Kees Goossens. Power/performance trade-offs in real-time SDRAM command scheduling. *IEEE Transactions on Computers*, 65 (6):1882–1895, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ganguly:2011:SHW**

[GCD+11] Amlan Ganguly, Kevin Chang, Sujay Deb, Partha Pratim Pande, Benjamin Belzer, and Christof Teuscher. Scalable hybrid wireless network-on-chip architectures for multicore systems. *IEEE Transactions on Computers*, 60(10):1485–1502, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5551123.

**Guo:2016:MAR**

[GCF+16] W. Guo, K. Chen, H. Feng, Y. Wu, R. Zhang, and W. Zheng. Mobile application relaunching speedup through flash-aware page swapping. *IEEE Transactions on Computers*, 65 (3):916–928, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2013:ECE**

[GCL+13] Deke Guo, Tao Chen, Dan Li, Mo Li, Yunhao Liu, and Guihai Chen. Expandable and cost-effective network structures for data centers using dual-port servers. *IEEE Transactions on Computers*, 62(7):1303–1317, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gao:2011:TMO**

[GCLC11] Ming Gao, Hsiu-Ming (Sherman) Chang, Peter Lisherness, and Kwang-Ting (Tim) Cheng. Time-multiplexed online checking. *IEEE Transactions on Computers*, 60(9):1300–1312, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5710883.

**Grass:2019:SST**

[GCR+19] Thomas Grass, Trevor E. Carlson, Alejandro Rico, Germán Ceballos, Eduard Ayguadé, Marc Casas, and Miquel Moreto. Sampled simulation of task-based programs. *IEEE Transactions on Computers*, 68(2): 255–269, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8424416/`.

**Gupta:2013:HPH**

[GCS+13] Sourav Sen Gupta, A. Chattopadhyay, K. Sinha, S. Maitra, and B. P. Sinha. High-performance hardware implementation for RC4 stream cipher. *IEEE Transactions on Computers*, 62(4):730–743, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gade:2017:HHW**

[GD17] Sri Harsha Gade and Sujay Deb. HyWin: Hybrid wireless NoC with sandboxed sub-networks for CPU/GPU architectures. *IEEE Transactions on Computers*, 66 (7):1145–1158, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07795230-abs.html`.

**Gao:2016:TRR**

[GDC+16] Y. Gao, W. Dong, C. Chen, J. Bu, and X. Liu. Towards reconstructing routing paths in large scale sensor networks. *IEEE Transactions on Computers*, 65 (1):281–293, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Grieve:2018:ARI**

[GDJZ18] Alex Grieve, Michael Davies, Phillip H. Jones, and Joseph Zambreno. ARMOR: A recompilation and instrumentation-free monitoring architecture for detecting memory exploits. *IEEE Transactions on Computers*, 67(8):1092–1104, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8295231/`.

**Gao:2018:PRR**

[GDLL18] Xinwei Gao, Jintai Ding, Lin Li, and Jiqiang Liu. Practical randomized RLWE-based key exchange against signal leakage attack. *IEEE Transactions on Computers*, 67(11):1584–1593, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8300634/`.

**Guo:2015:JOD**

[GDY15] Songtao Guo, Changyin Dang, and Yuanyuan Yang. Joint optimal data rate and power allocation in lossy mobile ad hoc networks with delay-constrained traffics. *IEEE Transactions on Computers*, 64(3):747–762, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gedik:2014:DFG**

[Ged14] Bugra Gedik. Discriminative fine-grained mixing for adaptive compression of data streams. *IEEE Transactions on Computers*, 63(9): 2228–2244, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ghaderi:2017:SHS**

[GEN+17] Z. Ghaderi, M. Ebrahimi, Z. Navabi, E. Bozorgzadeh, and N. Bagherzadeh. SENSIBle: A highly scalable SENsor DeSIgn for Path-Based Age Monitoring in FPGAs. *IEEE Transactions on Computers*, 66(5):919–926, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Garbacki:2010:DES**

[GEvS10] P. Garbacki, D. H. J. Epema, and M. van Steen. The design and evaluation of a self-organizing superpeer network. *IEEE Transactions on Computers*, 59 (3):317–331, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5291690`.

**Gupta:2011:SRF**

[GFAM11] Shantanu Gupta, Shuguang Feng, Amin Ansari, and Scott Mahlke. StageNet: a reconfigurable fabric for constructing dependable CMPs. *IEEE Transactions on Computers*, 60(1):5–19, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gomony:2017:GAM**

[GGA+17] Manil Dev Gomony, Jamie Garside, Benny Akesson, Neil Audsley, and Kees Goossens. A globally arbitrated memory tree for mixed-time-criticality systems. *IEEE Transactions on Computers*, 66(2):212–225, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Garcia-Guirado:2015:ICC**

[GGFPG15] A. Garcia-Guirado, R. Fernandez-Pascual, and J. M. Garcia. ICCI: In-cache coherence information. *IEEE*

*Transactions on Computers*, 64(4):995–1014, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2014:MLW**

[GGL+14] Song Guo, Minyi Guo, Victor C. M. Leung, Shui Yu, and Yong Xiang. On the multicast lifetime of WANETs with multibeam antennas: Formulation, algorithms, and analysis. *IEEE Transactions on Computers*, 63(8):1988–2001, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Garcia-Garcia:2018:CAF**

[GGSPM18] Adrian Garcia-Garcia, Juan Carlos Saez, and Manuel Prieto-Matias. Contention-aware fair scheduling for asymmetric single-ISA multicore systems. *IEEE Transactions on Computers*, 67(12): 1703–1719, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8360522/`.

**Galal:2011:EEF**

[GH11] Sameh Galal and Mark Horowitz. Energy-efficient floating-point unit design. *IEEE Transactions on Computers*, 60(7):913–922, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2014:OFL**

[GHG+14] Shuo Guo, Liang He, Yu Gu, Bo Jiang, and Tian He. Opportunistic flooding in low-duty-cycle wireless sensor networks with unreliable links. *IEEE Transactions on Computers*, 63(11): 2787–2802, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gerards:2015:IBG**

[GHK15] M. E. T. Gerards, J. L. Hurink, and J. Kuper. On the interplay between global DVFS and scheduling tasks with precedence constraints. *IEEE Transactions on Computers*, 64 (6):1742–1754, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gong:2017:NPA**

[GHL17] Yifan Gong, Bingsheng He, and Dan Li. Network performance aware optimizations on IaaS clouds. *IEEE Transactions on Computers*, 66(4):672–687, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gupta:2019:NPM**

[GIKR19] S. Gupta, M. Imani, H. Kaur, and T. S. Rosing. NNPIM:

A processing in-memory architecture for neural network acceleration. *IEEE Transactions on Computers*, 68(9): 1325–1337, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Giorgi:2012:PMC**

[Gio12] Pascal Giorgi. On polynomial multiplication in Chebyshev basis. *IEEE Transactions on Computers*, 61(6):780–789, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guerrini:2018:RMR**

[GIW18] Eleonora Guerrini, Laurent Imbert, and Théo Winterhalter. Randomized mixed-radix scalar multiplication. *IEEE Transactions on Computers*, 67 (3):418–431, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8031048/.

**Ganesan:2014:AGM**

[GJ14] Karthik Ganesan and Lizy Kurian John. Automatic generation of miniaturized synthetic proxies for target applications to efficiently design multicore processors. *IEEE Transactions on Computers*, 63(4):833–846, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gorgin:2015:CXH**

[GJ15] S. Gorgin and G. Jaberipur. Comment on “High-Speed Parallel Decimal Multiplication With Redundant Internal Encodings”. *IEEE Transactions on Computers*, 64(1):293–294, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [HK13a].

**Gaj:2010:ATE**

[GKB+10] K. Gaj, Soonhak Kwon, P. Baier, P. Kohlbrenner, Hoang Le, M. Khaleeluddin, R. Bachimanchi, and M. Rogawski. Area-time efficient implementation of the elliptic curve method of factoring in reconfigurable hardware for application in the number field sieve. *IEEE Transactions on Computers*, 59(9): 1264–1280, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374380.

**Gulino:2019:OBG**

[GKC19] Andrea Gulino, Abdulrahman Kaitoua, and Stefano Ceri. Optimal binning for genomics. *IEEE Transactions on Computers*, 68 (1):125–138, January 2019.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8410020/.

**Ganesh:2017:LOW**

[GKD+17] Kalidas Ganesh, Youngjae Kim, Monobrata Debnath, Sungyong Park, and Junghee Lee. LAWC: Optimizing write cache using layout-aware I/O scheduling for all flash storage. *IEEE Transactions on Computers*, 66(11): 1890–1902, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7932926/.

**Gilani:2014:EEP**

[GKS14] Syed Zohaib Gilani, Nam Sung Kim, and Michael Schulte. Energy-efficient pixel-arithmetic. *IEEE Transactions on Computers*, 63(8):1882–1894, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gu:2019:GRM**

[GL19] Z. Gu and S. Li. A generalized RNS Mclaughlin modular multiplication with non-coprime moduli sets. *IEEE Transactions on Computers*, 68(11):1689–1696, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gu:2019:TML**

[GLH+19] Chongyan Gu, Weiqiang Liu, Neil Hanley, Robert Hesselbarth, and Máire O'Neill. A theoretical model to link uniqueness and min-entropy for PUF evaluations. *IEEE Transactions on Computers*, 68(2):287–293, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8444682/.

**Gandino:2012:AAS**

[GLP+12] Filippo Gandino, Fabrizio Lamberti, Gianluca Paravati, Jean-Claude Bajard, and Paolo Montuschi. An algorithmic and architectural study on Montgomery exponentiation in RNS. *IEEE Transactions on Computers*, 61(8):1071–1083, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guneysu:2015:LBS**

[GLP15] T. Guneysu, V. Lyubashevsky, and T. Poppelmann. Lattice-based signatures: Optimization and implementation on reconfigurable hardware. *IEEE Transactions on Computers*, 64(7):1954–1967, July 2015.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2016:ECP**

[GLTC16] Peng Guo, Xuefeng Liu, Shaojie Tang, and Jiannong Cao. Enabling coverage-preserving scheduling in wireless sensor networks for structural health monitoring. *IEEE Transactions on Computers*, 65(8):2456–2469, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gao:2013:SSG**

[GLXY13] Lin Gao, Lian Li, Jingling Xue, and Pen-Chung Yew. SEED: A statically greedy and dynamically adaptive approach for speculative loop execution. *IEEE Transactions on Computers*, 62 (5):1004–1016, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gizopoulos:2011:GEI**

[GM11] Dimitris Gizopoulos and Shubhendu Mukherjee. Guest Editors' introduction: Special section on dependable computer architecture. *IEEE Transactions on Computers*, 60(1):3–4, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Golubitsky:2012:SOB**

[GM12] Oleg Golubitsky and Dmitri Maslov. A study of optimal 4-bit reversible Toffoli circuits and their synthesis. *IEEE Transactions on Computers*, 61(9): 1341–1353, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Garcia:2015:SQC**

[GM15] H. J. Garcia and I. L. Markov. Simulation of quantum circuits via stabilizer frames. *IEEE Transactions on Computers*, 64(8): 2323–2336, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gulliver:2014:FFL**

[GNSR14] T. Aaron Gulliver, Stephen W. Neville, Ulrich Speidel, and Niko Rebenich. FLOTT — a fast, low memory T-transform algorithm for measuring string complexity. *IEEE Transactions on Computers*, 63(4):917–926, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gonzalez-Navarro:2013:BID**

[GNTS13] Sonia Gonzalez-Navarro, Charles Tsen, and Michael J. Schulte. Binary integer decimal-based floating-point multiplication. *IEEE Transactions on Computers*, 62 (7):1460–1466, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Granmo:2010:SSN**

[GO10] O.-C. Granmo and B. J. Oommen. Solving stochastic nonlinear resource allocation problems using a hierarchy of twofold resource allocation automata. *IEEE Transactions on Computers*, 59(4):545–560, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374378.

**Gordon:2014:WCP**

[Gor14] Dan Gordon. The well-connected processor array. *IEEE Transactions on Computers*, 63(5):1287–1295, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Giefers:2014:FBR**

[GP14] H. Giefers and M. Platzner. An FPGA-based reconfigurable mesh many-core. *IEEE Transactions on Computers*, 63(12):2919–2932, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Garofalo:2011:ACM**

[GPN11] Valeria Garofalo, Nicola Petra, and Ettore Napoli. Analytical calculation of the maximum error for a family of truncated multipliers providing minimum mean square error. *IEEE Transactions on Computers*, 60(9):1366–1371, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669274.

**Gur:2019:PAI**

[GPR+19] Kamil D. Gür, Yuriy Polyakov, Kurt Rohloff, Gerard W. Ryan, Hadi Sajjadpour, and Erkay Savaş. Practical applications of improved Gaussian sampling for trapdoor lattices. *IEEE Transactions on Computers*, 68 (4):570–584, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8493319/.

**Gaudesi:2017:NTR**

[GPRS17] Marco Gaudesi, Irith Pomeranz, Matteo Sonza Reorda, and Giovanni Squillero. New techniques to reduce the execution time of functional test programs. *IEEE Transactions on Computers*, 66 (7):1268–1273, July 2017. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic). URL https://www.computer.org/csdl/trans/tc/2017/07/07795264-abs.html.

**Govindan:2014:SPP**

[GRL+14] Madhu Saravana Sibi Govindan, Behnam Robatmili, Dong Li, Bertrand A. Maher, Aaron Smith, Stephen W. Keckler, and Doug Burger. Scaling power and performance via processor composability. *IEEE Transactions on Computers*, 63 (8):2025–2038, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gangopadhyay:2016:MBP**

[GRM16] Dipanwita Gangopadhyay and Arash Reyhani-Masoleh. Multiple-bit parity-based concurrent fault detection architecture for parallel CRC computation. *IEEE Transactions on Computers*, 65 (7):2143–2157, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guilley:2010:EPC**

[GSF+10] S. Guilley, L. Sauvage, F. Flament, Vinh-Nga Vong, P. Hoogvorst, and R. Pacalet. Evaluation of power constant dual-rail logics countermeasures against DPA with design time security metrics. *IEEE Transactions on Computers*, 59(9): 1250–1263, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467046.

**Gu:2015:SCN**

[GSG+15] Feng Gu, K. Shaban, N. Ghani, S. Khan, M. R. Naeini, M. M. Hayat, and C. Assi. Survivable cloud network mapping for disaster recovery support. *IEEE Transactions on Computers*, 64(8):2353–2366, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gaiti:2014:TFT**

[GSH+14] Dominique Gaiti, Ahmad Sardouk, Abdelhakim Hafid, Ali El Masri, and Lyes Khoukhi. Toward fuzzy traffic adaptation solution in wireless mesh networks. *IEEE Transactions on Computers*, 63(5):1296–1308, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ganeshpure:2012:PGT**

[GSK12] Kunal P. Ganeshpure, Alodeep Sanyal, and Sandip Kundu. A pattern generation technique for maximizing switching supply currents considering gate delays. *IEEE Transactions on Computers*, 61(7):986–998, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gupta:2010:CSM**

[GSL10] M. Gupta, F. Sanchez, and J. Llosa. CSMT: Simultaneous multithreading for clustered VLIW processors. *IEEE Transactions on Computers*, 59 (3):385–399, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5161255.

**Golubcovs:2013:CMA**

[GSX+13] Stanislavs Golubcovs, Delong Shang, Fei Xia, Andrey Mokhov, and Alex Yakovlev. Concurrent multiresource arbiter: Design and applications. *IEEE Transactions on Computers*, 62(1):31–44, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gallin:2019:GFP**

[GT19] G. Gallin and A. Tisserand. Generation of finely-pipelined GF($PP$) multipliers for flexible curve based cryptography on FPGAs. *IEEE Transactions on Computers*, 68(11):1612–1622, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gonzalez-Toral:2018:SDC**

[GTRMG18] Ricardo González-Toral, Pedro Reviriego, Juan Antonio Maestro, and Zhen Gao. A scheme to design concurrent error detection techniques for the Fast Fourier Transform implemented in SRAM-based FPGAs. *IEEE Transactions on Computers*, 67(7):1039–1045, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8258993/.

**Ghosh:2014:BBB**

[GV14] Santosh Ghosh and Ingrid Verbauwhede. BLAKE-512-based 128-bit CCA2 secure timing attack resistant McEliece cryptoprocessor. *IEEE Transactions on Computers*, 63(5):1124–1133, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**George:2015:FTL**

[GV15] C. George and S. Vadhiyar. Fault tolerance on large scale systems using adaptive process replication. *IEEE Transactions on Computers*, 64(8):2213–2225, August 2015. CODEN IT-

COB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Garcia-Vega:2016:DMO**

[GVGNCVM16] Carlos Garcia-Vega, Sonia Gonzalez-Navarro, Pedro Balboa-La Chica, and Julio Villalba-Moreno. Decimal multiformat online addition. *IEEE Transactions on Computers*, 65(10): 3203–3209, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gadioli:2019:MDA**

[GVPS19] D. Gadioli, E. Vitali, G. Palermo, and C. Silvano. mARGOt: A dynamic autotuning framework for self-aware approximate computing. *IEEE Transactions on Computers*, 68(5):713–728, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gu:2016:PAO**

[GW16] Y. Gu and C. Q. Wu. Performance analysis and optimization of distributed workflows in heterogeneous network environments. *IEEE Transactions on Computers*, 65(4):1266–1282, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guan:2017:BLL**

[GWM+17] Yong Guan, Guohui Wang, Chenlin Ma, Renhai Chen, Yi Wang, and Zili Shao. A block-level log-block management scheme for MLC NAND flash memory storage systems. *IEEE Transactions on Computers*, 66(9): 1464–1477, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7874142/`.

**Ganesh:2013:IAD**

[GWMB13] Lakshmi Ganesh, Hakim Weatherspoon, Tudor Marian, and Ken Birman. Integrated approach to data center power management. *IEEE Transactions on Computers*, 62(6):1086–1096, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gu:2010:NWG**

[GWZ+10] Peng Gu, Jun Wang, Yifeng Zhu, Hong Jiang, and Pengju Shang. A novel weighted-graph-based grouping algorithm for metadata prefetching. *IEEE Transactions on Computers*, 59(1):1–15, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.`

org/stamp/stamp.jsp?tp=&arnumber=5184817.

**Guo:2013:HSM**

[GY13] Zhiyang Guo and Yuanyuan Yang. High-speed multicast scheduling in hybrid optical packet switches with guaranteed latency. *IEEE Transactions on Computers*, 62(10): 1972–1987, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gong:2014:LAA**

[GY14] Dawei Gong and Yuanyuan Yang. On-line AP association algorithms for 802.11n WLANs with heterogeneous clients. *IEEE Transactions on Computers*, 63(11): 2772–2786, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2015:ESR**

[GY15a] Zhiyang Guo and Yuanyuan Yang. Exploring server redundancy in nonblocking multicast data center networks. *IEEE Transactions on Computers*, 64(7):1912–1926, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Guo:2015:NMF**

[GY15b] Zhiyang Guo and Yuanyuan Yang. On nonblocking multicast fat-tree data center networks with server redundancy. *IEEE Transactions on Computers*, 64 (4):1058–1073, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gong:2016:LLM**

[GY16] Dawei Gong and Yuanyuan Yang. Link-layer multicast in large-scale 802.11n wireless LANs with smart antennas. *IEEE Transactions on Computers*, 65 (7):2118–2133, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gao:2016:GOM**

[GYC+16] Xiaofeng Gao, Yongtian Yang, Guihai Chen, Xin Lu, and Jiaofei Zhong. Global optimization for multi-channel wireless data broadcast with AH-tree indexing scheme. *IEEE Transactions on Computers*, 65 (7):2104–2117, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gu:2015:OTP**

[GZB+15] Lin Gu, Deze Zeng, A. Barnawi, Song Guo, and I. Stojmenovic. Optimal task placement with QoS constraints in geo-distributed data centers using DVFS. *IEEE Transactions on Computers*, 64(7):2049–2059, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Gao:2017:HPG**

[GZC+17] Pin Gao, Mingxing Zhang, Kang Chen, Yongwei Wu, and Weimin Zheng. High performance graph processing with locality oriented design. *IEEE Transactions on Computers*, 66 (7):1261–1267, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://www.computer.org/csdl/trans/tc/2017/07/07815299-abs.html.

**Gu:2016:GCC**

[GZG+16] L. Gu, D. Zeng, S. Guo, Y. Xiang, and J. Hu. A general communication cost optimization framework for big data stream processing in geo-distributed data centers. *IEEE Transactions on Computers*, 65(1):19–29, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Harn:2013:GA**

[Har13] Lein Harn. Group authentication. *IEEE Transactions on Computers*, 62(9): 1893–1898, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hadjidj:2011:ERA**

[HB11] Rachid Hadjidj and Hanifa Boucheneb. Efficient reachability analysis for time petri nets. *IEEE Transactions on Computers*, 60 (8):1085–1099, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hussain:2014:GHE**

[HBAD14] Zaid A. Hussain, Bella Bose, and Abdullah Al-Dhelaan. Generalized hypercubes: Edge-disjoint Hamiltonian cycles and Gray codes. *IEEE Transactions on Computers*, 63(2):375–382, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**He:2013:HEH**

[HBCC13] Daojing He, Jiajun Bu, Sammy Chan, and Chun Chen. Handauth: Efficient handover authentication with conditional privacy for wireless networks. *IEEE Transactions on Computers*, 62(3):616–622, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hyman:2011:RMS**

[HBR11] Ransford Hyman, Koustav Bhattacharya, and Nagarajan Ranganathan. Redundancy mining for soft error detection in multicore processors. *IEEE Transactions on Computers*, 60 (8):1114–1125, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hakkarinen:2013:MDC**

[HC13a] D. Hakkarinen and Zizhong Chen. Multilevel diskless checkpointing. *IEEE Transactions on Computers*, 62(4):772–783, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsiao:2013:SLB**

[HC13b] Hung-Chang Hsiao and Che-Wei Chang. A symmetric load balancing algorithm with performance guarantees for distributed hash tables. *IEEE Transactions on Computers*, 62(4):662–675, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2017:ATE**

[HC17] Jingwei Hu and Ray C. C. Cheung. Area-time efficient computation of Niederreiter encryption on QC-MDPC codes for embedded hardware. *IEEE Transactions on Computers*, 66 (8):1313–1325, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7862221/`.

**Hu:2012:LTM**

[HCC+12] Weiwu Hu, Yunji Chen, Tianshi Chen, Cheng Qian, and Lei Li. Linear time memory consistency verification. *IEEE Transactions on Computers*, 61(4):502–516, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Han:2018:CWF**

[HCC+18] Li Han, Louis-Claude Canon, Henri Casanova, Yves Robert, and Frédéric Vivien. Checkpointing workflows for fail-stop errors. *IEEE Transactions on Computers*, 67 (8):1105–1120, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8279499/`.

**Hasan:2010:UBD**

[HCCG10] Y. Hasan, Wei-Mei Chen, J. M. Chang, and B. M. Gharaibeh. Upper bounds for dynamic memory allocation. *IEEE Transactions on Computers*, 59 (4):468–477, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5282494`.

**He:2016:DDD**

[HCD+16] Kun He, Jing Chen, Ruiying Du, Qianhong Wu, Guoliang Xue, and Xiang Zhang. DeyPoS: Deduplicatable dynamic proof of storage for multi-user environments. *IEEE Transactions on Computers*, 65(12): 3631–3645, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2016:DHM**

[HCG+16] K. Hu, H. K. Chandrikakutty, Z. Goodman, R. Tessier, and T. Wolf. Dynamic hardware monitors for network processor protection. *IEEE Transactions on Computers*, 65 (3):860–872, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hong:2015:LDA**

[HCH15] Shengyan Hong, T. Chantem, and Xiaobo Sharon Hu. Local-deadline assignment for distributed real-time systems. *IEEE Transactions on Computers*, 64(7):1983–1997, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Han:2014:SCA**

[HCL+14] Jie Han, Hao Chen, Jinghang Liang, Peican Zhu, Zhixi Yang, and Fabrizio Lombardi. A stochastic computational approach for accurate and efficient reliability evaluation. *IEEE Transactions on Computers*, 63 (6):1336–1350, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsieh:2015:AES**

[HCL15] J. Hsieh, C. Chen, and H. Lin. Adaptive ECC scheme for hybrid SSD's. *IEEE Transactions on Computers*, 64(12):3348–3361, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsin:2015:ACO**

[HCSW15] Hsien-Kai Hsin, En-Jui Chang, Kuan-Yu Su, and An-Yeu Wu. Ant colony optimization-based adaptive network-on-chip routing framework using network information region. *IEEE Transactions on Computers*, 64(8):2119–2131, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Han:2018:RRE**

[HCY18] Hyunseung Han, Jaeyong Chung, and Joon-Sung Yang. READ: Reliability enhancement in 3D-memory exploiting asymmetric SER distribution. *IEEE Transactions on Computers*, 67 (8):1193–1201, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8283794/`.

**Huang:2013:TCT**

[HCZW13] Minsu Huang, Siyuan Chen, Ying Zhu, and Yu Wang. Topology control for time-evolving and predictable delay-tolerant networks. *IEEE Transactions on Computers*, 62(11):2308–2321, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2016:EET**

[HDYS16] J. Huang, Y. Deng, Q. Yang, and J. Sun. An energy-efficient train control framework for smart railway transportation. *IEEE Transactions on Computers*, 65 (5):1407–1417, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Han:2015:SPB**

[HF15] Jiangtao Han and Haining Fan. Shifted polynomial basis multipliers based on subquadratic Toeplitz matrix-vector product approach for all irreducible pentanomials. *IEEE Transactions on Computers*, 64 (3):862–867, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**He:2017:FPH**

[HFG+17] Conghui He, Haohuan Fu, Ce Guo, Wayne Luk, and Guangwen Yang. A fully-pipelined hardware design for Gaussian mixture models. *IEEE Transactions on Computers*, 66(11):1837–1850, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7938761/`.

**Hao:2013:CDA**

[HFZ13] Rong-Xia Hao, Yan-Quan Feng, and Jin-Xin Zhou. Conditional diagnosability of alternating group graphs. *IEEE Transactions on Computers*, 62(4):827–831, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Herrero:2013:TRB**

[HGCT13] Enric Herrero, Jose Gonzalez, Ramon Canal, and Dean Tullsen. Thread row buffers: Improving memory performance isolation and throughput in multiprogrammed environments. *IEEE Transactions on Computers*, 62(9): 1879–1892, September 2013. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Huang:2011:NHA**

[HGEG11] Miaoqing Huang, Kris Gaj, and Tarek El-Ghazawi. New hardware architectures for Montgomery modular multiplication algorithm. *IEEE Transactions on Computers*, 60(7):923–936, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2015:JOR**

[HGL+15] H. Huang, S. Guo, P. Li, B. Ye, and I. Stojmenovic. Joint optimization of rule placement and traffic engineering for QoS provisioning in software defined network. *IEEE Transactions on Computers*, 64(12): 3488–3499, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huynh:2011:RRR**

[HGML11] Minh Huynh, Stuart Goose, Prasant Mohapatra, and Raymond Liao. RRR: Rapid ring recovery submillisecond decentralized recovery for Ethernet ring. *IEEE Transactions on Computers*, 60 (11):1561–1570, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5560640`.

**Hua:2017:SAR**

[HGW+17] Xiayu Hua, Chunhui Guo, Hao Wu, Douglas Lautner, and Shangping Ren. Schedulability analysis for real-time task set on resource with performance degradation and dual-level periodic rejuvenations. *IEEE Transactions on Computers*, 66 (3):553–559, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsu:2017:MIS**

[HH17] Po-Cheng Hsu and Sun-Yuan Hsieh. Multi-inherited search tree for dynamic IP router-tables. *IEEE Transactions on Computers*, 66 (1):114–126, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2018:ACO**

[HHC+18] Kai Huang, Biao Hu, Long Chen, Alois Knoll, and Zhihua Wang. Adas on Cots with OpenCL: A case study with lane detection. *IEEE Transactions on Computers*, 67(4):559–565, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8057795/`.

**Hsu:2011:DET**

[HHCH11] Chun-Lung Hsu, Yu-Sheng Huang, Ming-Da Chang, and Hung-Yen Huang. Design of an error-tolerance scheme for discrete wavelet transform in JPEG 2000 encoder. *IEEE Transactions on Computers*, 60(5):628–638, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Houshmand:2012:MMR**

[HHKW12] Monireh Houshmand, Saied Hosseini-Khayat, and Mark M. Wilde. Minimal-memory requirements for pearl-necklace encoders of quantum convolutional codes. *IEEE Transactions on Computers*, 61 (3):299–312, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsiu:2012:MBO**

[HHLK12] Pi-Cheng Hsiu, Cheng-Kang Hsieh, Der-Nien Lee, and Tei-Wei Kuo. Multilayer bus optimization for real-time embedded systems. *IEEE Transactions on Computers*, 61(11):1638–1650, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hefeeda:2011:DEP**

[HHM11] Mohamed Hefeeda, Cheng-Hsin Hsu, and Kianoosh Mokhtarian. Design and evaluation of a proxy cache for peer-to-peer traffic. *IEEE Transactions on Computers*, 60(7):964–977, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2018:ALB**

[HHW+18] Dan Huang, Dezhi Han, Jun Wang, Jiangling Yin, Xunchao Chen, Xuhong Zhang, Jian Zhou, and Mao Ye. Achieving load balance for parallel data access on distributed file systems. *IEEE Transactions on Computers*, 67(3):388–402, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8027054/`.

**Hsieh:2011:MTN**

[HHY11] Sun-Yuan Hsieh, Yi-Ling Huang, and Ying-Chi Yang. Multiprefix trie: a new data structure for designing dynamic router-tables. *IEEE Transactions on Computers*, 60(5):693–706, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hiasat:2016:SDG**

[Hia16] Ahmad Hiasat. A sign detector for a group of three-moduli sets. *IEEE Transactions on Computers*, 65(12): 3580–3590, December 2016.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hiasat:2017:ERS**

[Hia17] Ahmad Hiasat. Efficient RNS scalers for the extended three-moduli set $(2^n - 1, 2^{n+p}, 2^n + 1)$. *IEEE Transactions on Computers*, 66(7):1253–1260, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07815319-abs.html`.

**Hierons:2011:CTN**

[Hie11] Robert M. Hierons. Controllable testing from nondeterministic finite state machines with multiple ports. *IEEE Transactions on Computers*, 60(12):1818–1822, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hierons:2013:VCF**

[Hie13] Robert M. Hierons. Verifying and comparing finite state machines for systems that have distributed interfaces. *IEEE Transactions on Computers*, 62 (8):1673–1683, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hao:2019:IDP**

[HIJ+19] Y. Hao, T. Isobe, L. Jiao, C. Li, W. Meier, Y. Todo, and Q. Wang. Improved division property based cube attacks exploiting algebraic properties of Superpoly. *IEEE Transactions on Computers*, 68(10):1470–1486, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hollis:2014:EEC**

[HJBM14] Simon J. Hollis, Chris Jackson, Paul Bogdan, and Radu Marculescu. Exploiting emergence in on-chip interconnects. *IEEE Transactions on Computers*, 63 (3):570–582, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2013:EEM**

[HJF+13] Yang Hu, Hong Jiang, Dan Feng, Lei Tian, Hao Luo, and Chao Ren. Exploring and exploiting the multilevel parallelism inside SSDs for improved performance and endurance. *IEEE Transactions on Computers*, 62 (6):1141–1155, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Han:2013:HSP**

[HK13a] Liu Han and Seok-Bum Ko. High-speed parallel decimal multiplication with redundant internal encodings. *IEEE Transactions on Computers*, 62(5):956–968, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See comment [GJ15].

**Hsieh:2013:CDK**

[HK13b] Sun-Yuan Hsieh and Chi-Ya Kao. The conditional diagnosability of $k$-ary $n$-cubes under the comparison diagnosis model. *IEEE Transactions on Computers*, 62(4):839–843, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hong:2015:LCM**

[HK15a] Seokin Hong and Soontae Kim. A low-cost mechanism exploiting narrow-width values for tolerating hard faults in ALU. *IEEE Transactions on Computers*, 64(9):2433–2446, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsieh:2015:DDC**

[HK15b] J. Hsieh and Y. Kuan. DCCS: Double circular caching scheme for DRAM/PRAM hybrid cache. *IEEE Transactions on Computers*, 64(11):3115–3127, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hong:2016:DRL**

[HK16] Seokin Hong and Soontae Kim. Designing a resilient L1 cache architecture to process variation-induced access-time failures. *IEEE Transactions on Computers*, 65(10):2999–3012, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hong:2017:SEA**

[HK17] Jeongkyu Hong and Soontae Kim. Smart ECC allocation cache utilizing cache data space. *IEEE Transactions on Computers*, 66(2):368–374, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Howe:2018:PDG**

[HKR+18] James Howe, Ayesha Khalid, Ciara Rafferty, Francesco Regazzoni, and Máire O'Neill. On practical discrete Gaussian samplers for lattice-based cryptography. *IEEE Transactions on Computers*, 67(3):322–334, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7792671/.

**Hsu:2014:JDA**

[HKWC14] Chih-Cheng Hsu, Ming-Shing Kuo, Shi-Chen Wang, and Cheng-Fu Chou. Joint design of asynchronous sleep-wake scheduling and opportunistic routing in wireless sensor networks. *IEEE Transactions on Computers*, 63(7):1840–1846, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Harn:2010:AGK**

[HL10a] L. Harn and Changlu Lin. Authenticated group key transfer protocol based on secret sharing. *IEEE Transactions on Computers*, 59(6):842–846, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416683.

**Huang:2010:ASR**

[HL10b] Chin-Yu Huang and Chu-Ti Lin. Analysis of software reliability modeling considering testing compression factor and failure-to-fault relationship. *IEEE Transactions on Computers*, 59(2):283–288, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5372166.

**Huang:2017:EER**

[HLA+17] Jing Huang, Renfa Li, Jiyao An, Derrick Ntalasha, Fan Yang, and Keqin Li. Energy-efficient resource utilization for heterogeneous embedded computing systems. *IEEE Transactions on Computers*, 66(9):1518–1531, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7896542/.

**Huang:2019:CVR**

[HLC+19] D. Huang, Q. Liu, J. Choi, N. Podhorszki, S. Klasky, J. Logan, G. Ostrouchov, X. He, and M. Wolf. Can I/O variability be reduced on QoS-less HPC storage systems? *IEEE Transactions on Computers*, 68(5):631–645, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hua:2014:DSA**

[HLF14] Yu Hua, Xue Liu, and Dan Feng. Data similarity-aware computation infrastructure for the cloud. *IEEE Transactions on Computers*, 63(1):3–16, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hua:2014:ASA**

[HLJ14] Yu Hua, Xue Liu, and Hong Jiang. ANTELOPE:

A semantic-aware data cube scheme for cloud data center networks. *IEEE Transactions on Computers*, 63(9): 2146–2159, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2015:CEA**

[HLT+15] Xinyi Huang, J. K. Liu, Shaohua Tang, Yang Xiang, Kaitai Liang, Li Xu, and Jianying Zhou. Cost-effective authentic and anonymous data sharing with forward security. *IEEE Transactions on Computers*, 64(4):971–983, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2017:AST**

[HLWV17] Menglan Hu, Jun Luo, Yang Wang, and Bharadwaj Veeravalli. Adaptive scheduling of task graphs with dynamic resilience. *IEEE Transactions on Computers*, 66(1):17–23, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsieh:2014:MCA**

[HLY14] Jen-Wei Hsieh, Han-Yi Lin, and Dong-Lin Yang. Multi-channel architecture-based FTL for reliable and high-performance SSD. *IEEE Transactions on Computers*, 63(12):3079–3091, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Homma:2010:CPA**

[HMA+10] N. Homma, A. Miyamoto, T. Aoki, A. Satoh, and A. Samir. Comparative power analysis of modular exponentiation algorithms. *IEEE Transactions on Computers*, 59(6):795–807, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5342411`.

**Hong:2011:EOS**

[HMC11] Wonhak Hong, Rajashekhar Modugu, and Minsu Choi. Efficient online self-checking modulo $2^n + 1$ multiplier design. *IEEE Transactions on Computers*, 60(9): 1354–1365, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5962404`.

**Hojabr:2017:CCN**

[HMD+17] Reza Hojabr, Mehdi Modarressi, Masoud Daneshtalab, Ali Yasoubi, and Ahmad Khonsari. Customizing Clos network-on-chip for neural networks. *IEEE Transactions on Computers*, 66(11): 1865–1877, November 2017.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7948744/.

**Hasan:2012:BRA**

[HMNN12] M. Anwar Hasan, Nicolas Meloni, Ashkan Hosseinzadeh Namin, and Christophe Negre. Block recombination approach for subquadratic space complexity binary field multiplication based on Toeplitz matrix–vector product. *IEEE Transactions on Computers*, 61 (2):151–163, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hormigo:2017:ISI**

[HMO+17] Javier Hormigo, Jean-Michel Muller, Stuart Oberman, Nathalie Revol, Arnaud Tisserand, and Julio Villalba-Moreno. Introduction to the special issue on computer arithmetic. *IEEE Transactions on Computers*, 66(12): 1991–1993, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Haghbayan:2017:PRA**

[HMR+17] Mohammad-Hashem Haghbayan, Antonio Miele, Amir M. Rahmani, Pasi Liljeberg, and Hannu Tenhunen. Performance/reliability-aware resource management for many-cores in dark silicon era. *IEEE Transactions on Computers*, 66(9): 1599–1612, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7892847/.

**Huang:2012:LCB**

[HMS+12] Libo Huang, Sheng Ma, Li Shen, Zhiying Wang, and Nong Xiao. Low-cost Binary128 floating-point FMA unit design with SIMD support. *IEEE Transactions on Computers*, 61(5):745–751, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hao:2014:TWS**

[HMZ+14] Zhuo Hao, Yunlong Mao, Sheng Zhong, Li Erran Li, Haifan Yao, and Nenghai Yu. Toward wireless security without computational assumptions — oblivious transfer based on wireless channel characteristics. *IEEE Transactions on Computers*, 63(6):1580–1593, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hasan:2011:LSC**

[HN11] M. Anwar Hasan and Christophe Negre. Low space complexity multiplication over binary fields with Dickson polynomial representation. *IEEE Transactions on Computers*, 60(4):602–607, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hasan:2013:MSM**

[HN13] M. Anwar Hasan and Christophe Negre. Multiway splitting method for Toeplitz matrix vector product. *IEEE Transactions on Computers*, 62 (7):1467–1471, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See comment [PCHS16].

**Huang:2012:EMT**

[HNB+12] Miaoqing Huang, Vikram K. Narayana, Mohamed Bakhouya, Jaafar Gaber, and Tarek El-Ghazawi. Efficient mapping of task graphs onto reconfigurable hardware using architectural variants. *IEEE Transactions on Computers*, 61(9):1354–1360, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hassan:2019:FEE**

[HNV19] A. Hassan, D. Nikolopoulos, and H. Vandierendonck. Fast and energy-efficient OLAP data management on hybrid main memory systems. *IEEE Transactions on Computers*, 68(11):1597–1611, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hassan:2016:RTC**

[HPR16] Ahmed Hassan, Roberto Palmieri, and Binoy Ravindran. Remote transaction commit: Centralizing software transactional memory commits. *IEEE Transactions on Computers*, 65 (7):2228–2240, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2015:ERB**

[HQLX15] J. Huang, X. Qin, X. Liang, and C. Xie. An efficient I/O-redirection-based reconstruction scheme for erasure-coded storage clusters. *IEEE Transactions on Computers*, 64(11):3037–3050, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hajisheykhi:2017:BAR**

[HRK17] Reza Hajisheykhi, Mohammad Roohitavaf, and Sandeep S. Kulkarni. Bounded auditable restoration of distributed systems. *IEEE Transactions on Computers*, 66(2):240–255, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hariri:2011:CED**

[HRM11] Arash Hariri and Arash Reyhani-Masoleh. Concurrent error detection in Montgomery multiplication over binary extension fields. *IEEE Transactions on Computers*, 60(9):1341–1353, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669280`.

**Haghbayan:2016:PAA**

[HRM+16] M. Haghbayan, A. Rahmani, A. Miele, M. Fattah, J. Plosila, P. Liljeberg, and H. Tenhunen. A power-aware approach for online test scheduling in many-core architectures. *IEEE Transactions on Computers*, 65(3):730–743, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**He:2018:CED**

[HS18] Shuibing He and Xian-He Sun. A cost-effective distribution-aware data replication scheme for parallel I/O systems. *IEEE Transactions on Computers*, 67(10): 1374–1387, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8352821/`.

**Homma:2014:TFD**

[HSA14] N. Homma, K. Saito, and T. Aoki. Toward formal design of practical cryptographic hardware based on Galois field arithmetic. *IEEE Transactions on Computers*, 63(10):2604–2613, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**He:2010:IEC**

[HSH+10] Fei He, Xiaoyu Song, W. N. N. Hung, Ming Gu, and Jiaguang Sun. Integrating evolutionary computation with abstraction refinement for model checking. *IEEE Transactions on Computers*, 59(1):116–126, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184807`.

**Han:2014:IBS**

[HSM14] Jinguang Han, Willy Susilo, and Yu Mu. Identity-based secure distributed data storage schemes. *IEEE Transactions on Computers*, 63 (4):941–953, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2012:MEC**

[HT12] Chen-Wei Huang and Shiao-

Li Tsao. Minimizing energy consumption of embedded systems via optimal code layout. *IEEE Transactions on Computers*, 61 (8):1127–1139, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hierons:2016:PAG**

[HT16] Robert M. Hierons and Uraz Cengiz Türker. Parallel algorithms for generating harmonised state identifiers and characterising sets. *IEEE Transactions on Computers*, 65(11): 3370–3383, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hasan:2010:FRA**

[HTA10] O. Hasan, S. Tahar, and N. Abbasi. Formal reliability analysis using theorem proving. *IEEE Transactions on Computers*, 59(5): 579–592, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5306067`.

**Ha:2017:WFP**

[HTA17] Phuong Hoai Ha, Philippas Tsigas, and Otto J. Anshus. Wait-free programming for general purpose computations on graphics processors. *IEEE Transactions on Computers*, 66 (8):1407–1420, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/6357187/`.

**Hsieh:2013:SDC**

[HTC13] Sun-Yuan Hsieh, Cheng-Yen Tsai, and Chun-An Chen. Strong diagnosability and conditional diagnosability of multiprocessor systems and folded hypercubes. *IEEE Transactions on Computers*, 62(7):1472–1477, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hamdioui:2015:TOD**

[HTH15] S. Hamdioui, M. Taouil, and N. Z. Haron. Testing open defects in memristor-based memories. *IEEE Transactions on Computers*, 64 (1):247–259, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hanumaiah:2012:TAD**

[HV12] Vinay Hanumaiah and Sarma Vrudhula. Temperature-aware DVFS for hard real-time applications on multicore processors. *IEEE Transactions on Computers*, 61(10):1484–1494, October 2012. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Hu:2013:RAS**

[HV13] Menglan Hu and Bharadwaj Veeravalli. Requirement-aware scheduling of bag-of-tasks applications on grids with dynamic resilience. *IEEE Transactions on Computers*, 62(10):2108–2114, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hanumaiah:2014:EEO**

[HV14a] Vinay Hanumaiah and Sarma Vrudhula. Energy-efficient operation of multicore processors by DVFS, task migration, and active cooling. *IEEE Transactions on Computers*, 63(2):349–360, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2014:DSH**

[HV14b] Menglan Hu and B. Veeravalli. Dynamic scheduling of hybrid real-time tasks on clusters. *IEEE Transactions on Computers*, 63(12): 2988–2997, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hormigo:2016:NFC**

[HV16] Javier Hormigo and Julio Villalba. New formats for computing with real-numbers under round-to-nearest. *IEEE Transactions on Computers*, 65(7):2158–2168, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hormigo:2013:MRA**

[HVZ13] Javier Hormigo, Julio Villalba, and Emilio L. Zapata. Multioperand redundant adders on FPGAs. *IEEE Transactions on Computers*, 62(10):2013–2025, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsiao:2017:HMF**

[HWCH17] Shen-Fu Hsiao, Chia-Sheng Wen, Yi-Hau Chen, and Kuei-Chun Huang. Hierarchical multipartite function evaluation. *IEEE Transactions on Computers*, 66 (1):89–99, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2016:NBT**

[HWE+16] L. Huang, J. Wang, M. Ebrahimi, M. Daneshtalab, X. Zhang, G. Li, and A. Jantsch. Non-blocking testing for network-on-chip. *IEEE Transactions on Computers*, 65(3):679–692, March 2016. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Hu:2014:CAA**

[HWG+14] Ruimin Hu, Xiaochen Wang, Jichang Guo, Qiang Liu, Tao Zhang, and Haojun Quan. Comments on "Algorithmic Aspects of Hardware/ Software Partitioning: 1D Search Algorithms". *IEEE Transactions on Computers*, 63(4):1055–1056, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [JSC10].

**Hsu:2015:CWF**

[HWK15] A. C.-C. Hsu, D. S. L. Wei, and C.-C. J. Kuo. Coexistence Wi-Fi MAC design for mitigating interference caused by collocated Bluetooth. *IEEE Transactions on Computers*, 64 (2):342–352, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsu:2014:LTP**

[HWL+14] Hong-Chun Hsu, Kuang-Shyr Wu, Cheng-Kuan Lin, Chiou-Yng Lee, and Chien-Ping Chang. A linear time pessimistic diagnosis algorithm for hypermesh multiprocessor systems under the PMC model. *IEEE Transactions on Computers*, 63(12): 2894–2904, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**He:2017:HAC**

[HWS+17] Shuibing He, Yang Wang, Xian-He Sun, Chuanhe Huang, and Chenzhong Xu. Heterogeneity-aware collective I/O for parallel I/O systems with hybrid HDD/ SSD servers. *IEEE Transactions on Computers*, 66 (6):1091–1098, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7778155/`.

**Huang:2015:CCF**

[HWSN15] Zhen Huang, Cheng Wang, M. Stojmenovi, and A. Nayak. Characterization of cascading failures in interdependent cyber-physical systems. *IEEE Transactions on Computers*, 64(8):2158–2168, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**He:2017:HOP**

[HWSX17] Shuibing He, Yang Wang, Xian-He Sun, and Chengzhong Xu. HARL: Optimizing parallel file systems with heterogeneity-aware region-level data layout. *IEEE Transactions on Computers*, 66(6):1048–1060, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-

9956 (electronic). URL http://ieeexplore.ieee.org/document/7779011/.

**Huang:2015:RMR**

[HWX15] Pei Huang, Chen Wang, and Li Xiao. RC-MAC: A receiver-centric MAC protocol for event-driven wireless sensor networks. *IEEE Transactions on Computers*, 64(4):1149–1161, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Han:2012:SAE**

[HWZ+12] Jian-Jun Han, Xiaodong Wu, Dakai Zhu, Hai Jin, Laurence T. Yang, and Jean-Luc Gaudiot. Synchronization-aware energy management for VFI-based multicore real-time systems. *IEEE Transactions on Computers*, 61(12):1682–1696, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2017:OLA**

[HWZ+17] X. Hu, X. Wang, L. Zhou, Y. Luo, C. Ding, S. Jiang, and Z. Wang. Optimizing locality-aware memory management of key–value caches. *IEEE Transactions on Computers*, 66(5):862–875, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2011:EIB**

[HXL11] Xinfa Hu, Guoliang Xing, and Joseph Y.-T. Leung. Exploring the interplay between computation and communication in distributed real-time scheduling. *IEEE Transactions on Computers*, 60(12):1759–1771, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5560641.

**Hua:2012:LSB**

[HXVF12] Yu Hua, Bin Xiao, Bharadwaj Veeravalli, and Dan Feng. Locality-sensitive Bloom filter for approximate membership query. *IEEE Transactions on Computers*, 61(6):817–830, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Haghighi:2015:STD**

[HXVQ15] M. S. Haghighi, Yang Xiang, V. Varadharajan, and B. Quinn. A stochastic time-domain model for burst data aggregation in IEEE 802.15.4 wireless sensor networks. *IEEE Transactions on Computers*, 64(3):627–639, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hsieh:2012:CMT**

[HY12] Sun-Yuan Hsieh and Ying-Chi Yang. A classified multisuffix trie for IP lookup and update. *IEEE Transactions on Computers*, 61(5): 726–731, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Heys:2011:PSC**

[HZ11] Howard M. Heys and Liang Zhang. Pipelined statistical cipher feedback: a new mode for high-speed self-synchronizing stream encryption. *IEEE Transactions on Computers*, 60(11): 1581–1595, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5499465`.

**Han:2016:TSM**

[HZL+16] K. Han, C. Zhang, J. Luo, M. Hu, and B. Veeravalli. Truthful scheduling mechanisms for powering mobile crowdsensing. *IEEE Transactions on Computers*, 65 (1):294–307, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Huang:2012:JTA**

[HZW+12] H. Howie Huang, Nan Zhang, Wei Wang, Gautam Das, and Alexander S. Szalay. Just-in-time analytics on large file systems. *IEEE Transactions on Computers*, 61(11):1651–1664, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Hu:2014:SOC**

[HZX+14] Jingtong Hu, Qingfeng Zhuge, Chun Jason Xue, Wei-Che Tseng, Shouzhen Gu, and Edwin H.-M. Sha. Scheduling to optimize cache utilization for non-volatile main memories. *IEEE Transactions on Computers*, 63(8):2039–2051, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ishebabi:2010:HFC**

[IB10] Harold Ishebabi and Christophe Bobda. Heuristics for flexible CMP synthesis. *IEEE Transactions on Computers*, 59 (8):1091–1104, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453336`.

**Iturbe:2013:RNR**

[IBH+13] Xabier Iturbe, Khaled Benkrid, Chuan Hong, Ali Ebrahim, Raul Torrego, Imanol Martinez, Tughrul Arslan, and

Jon Perez. R3TOS: A novel reliable reconfigurable real-time operating system for highly adaptive, efficient, and dependable computing on FPGAs. *IEEE Transactions on Computers*, 62 (8):1542–1556, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ibrahim:2016:IWR**

[Ibr16] Walid Ibrahim. Identifying the worst reliability input vectors and the associated critical logic gates. *IEEE Transactions on Computers*, 65(6):1748–1760, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ishii:2017:FMA**

[IDG+17] Masahiro Ishii, Jérémie Detrey, Pierrick Gaudry, Atsuo Inomata, and Kazutoshi Fujikawa. Fast modular arithmetic on the Kalray MPPA-256 processor for an energy-efficient implementation of ECM. *IEEE Transactions on Computers*, 66(12): 2019–2030, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7927487/.

**Ingelsson:2015:AFT**

[IGLM15] U. Ingelsson, S. K. Goel, E. Larsson, and E. J. Marinissen. Abort-on-fail test scheduling for modular SOCs without and with preemption. *IEEE Transactions on Computers*, 64(12): 3335–3347, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Iqbal:2016:DCA**

[IHR+16] Muhammad Faisal Iqbal, Jim Holt, Jee Ho Ryoo, Gustavo de Veciana, and Lizy K. John. Dynamic core allocation and packet scheduling in multicore network processors. *IEEE Transactions on Computers*, 65(12): 3646–3660, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ikodinovic:2015:MCA**

[Iko15] I. Ikodinovic. Methodology for cycle-accurate DRAM performance analysis. *IEEE Transactions on Computers*, 64(7):2084–2091, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Imana:2018:FBP**

[Ima18] José L. Imaña. Fast bit-parallel binary multipliers based on Type-I pentanomials. *IEEE Transactions on Computers*, 67(6):898–904, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8125152/.

**Ilic:2017:BRC**

[IPS17] Aleksandar Ilic, Frederico Pratas, and Leonel Sousa. Beyond the roofline: Cache-aware power and energy-efficiency modeling for multi-cores. *IEEE Transactions on Computers*, 66(1):52–58, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Iakymchuk:2016:VDT**

[IRMM+16] Taras Iakymchuk, Alfredo Rosado-Muñoz, Manuel Bataller-Mompéan, José Vicente Francés-Víllora, and Emmanuel Ovie Osimiry. Versatile direct and transpose matrix multiplication with chained operations: An optimized architecture using circulant matrices. *IEEE Transactions on Computers*, 65(11):3470–3479, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Im:2011:FAR**

[IS11] Soojun Im and Dongkun Shin. Flash-aware RAID techniques for dependable and high-performance flash memory SSD. *IEEE Transactions on Computers*, 60(1):80–92, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Islam:2014:CES**

[IS14] Mafijul Md. Islam and Per Stenstrom. Characterizing and exploiting small-value memory instructions. *IEEE Transactions on Computers*, 63(7):1640–1655, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ibrahim:2015:AEE**

[ISC15] W. Ibrahim, M. Shousha, and J. W. Chinneck. Accurate and efficient estimation of logic circuits reliability bounds. *IEEE Transactions on Computers*, 64(5):1217–1229, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jongerius:2018:AMC**

[JAD+18] Rik Jongerius, Andreea Anghel, Gero Dittmann, Giovanni Mariani, Erik Vermij, and Henk Corporaal. Analytic multi-core processor model for fast design-space exploration. *IEEE Transactions on Computers*, 67(6):755–770, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8168422/.

**Jawad:2015:JJT**

[JAJK15] M. Jawad, S. M. Ali, J. A. Jorgenson, and S. U. Khan. JEM: Just in time/just enough energy management methodology for computing systems. *IEEE Transactions on Computers*, 64 (6):1798–1804, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jadidi:2018:PPE**

[JAKD18] Amin Jadidi, Mohammad Arjomand, Mahmut T. Kandemir, and Chita R. Das. Performance and power-efficient design of dense non-volatile cache in CMPs. *IEEE Transactions on Computers*, 67 (7):1054–1061, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8265206/`.

**Jin:2015:HSH**

[JAS+15] Seongwook Jin, Jeongseob Ahn, Jinho Seol, Sanghoon Cha, Jaehyuk Huh, and Seungryoul Maeng. H-SVM: Hardware-assisted secure virtual machines under a vulnerable hypervisor. *IEEE Transactions on Computers*, 64(10):2833–2846, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jin:2011:MDL**

[JC11] Lei Jin and Sangyeun Cho. Macro data load: An efficient mechanism for enhancing loaded data reuse. *IEEE Transactions on Computers*, 60(4):526–537, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jaiswal:2012:FBH**

[JC12] Manish Kumar Jaiswal and Nitin Chandrachoodan. FPGA-based high-performance and scalable block LU decomposition architecture. *IEEE Transactions on Computers*, 61(1):60–72, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jerez:2015:LCS**

[JCK15] J. L. Jerez, G. A. Constantinides, and E. C. Kerrigan. A low complexity scaling method for the Lanczos kernel in fixed-point arithmetic. *IEEE Transactions on Computers*, 64 (2):303–315, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2016:PIA**

[JCM16] Tao Jiang, Xiaofeng Chen, and Jianfeng Ma. Public integrity auditing for shared dynamic cloud data with group user revocation. *IEEE Transactions on Computers*, 65(8):2363–2373, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jang:2013:EAS**

[JCY+13] Hyung Beom Jang, Jinhang Choi, Ikroh Yoon, Sung-Soo Lim, Seungwon Shin, Naehyuck Chang, and Sung Woo Chung. Exploiting application/system-dependent ambient temperature for accurate microarchitectural simulation. *IEEE Transactions on Computers*, 62(4):705–715, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jarvinen:2015:GAC**

[JDA15] K. Jarvinen, V. Dimitrov, and R. Azarderakhsh. A generalization of addition chains and fast inversions in binary fields. *IEEE Transactions on Computers*, 64(9): 2421–2432, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jafri:2016:TTB**

[JDA+16] Syed M. A. H. Jafri, Masoud Daneshtalab, Naeem Abbas, Guillermo Serrano Leon, and Ahmed Hemani. TransMap: Transformation based remapping and parallelism for high utilization and energy efficiency in CGRAs. *IEEE Transactions on Computers*, 65(11): 3456–3469, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jessa:2015:QRS**

[Jes15] M. Jessa. On the quality of random sequences produced with a combined random bit generator. *IEEE Transactions on Computers*, 64(3):791–804, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jalili:2014:NPD**

[JGG+14] Mahdi Jalili, Joobin Gharibshah, Seyed Morsal Ghavami, Mohammadreza Beheshtifar, and Reza Farshi. Nationwide prediction of drought conditions in Iran based on remote sensing data. *IEEE Transactions on Computers*, 63(1):90–101, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jeong:2011:APL**

[JGHD11] Jaehoon Jeong, Shuo Guo, Tian He, and David H. C. Du. Autonomous passive localization algorithm for road sensor networks. *IEEE Transactions on Computers*, 60(11):1622–1637, November 2011. CODEN ITCOB4.

ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669282.

**Jha:2013:CXM**

[Jha13] Pranava K. Jha. Comments on "Multiple-Radix Gray Codes in Lee Metric". *IEEE Transactions on Computers*, 62(1):200, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [ABA07].

**Jiang:2016:ARB**

[JHQL16] Honglan Jiang, Jie Han, Fei Qiao, and Fabrizio Lombardi. Approximate radix-8 Booth multipliers for low-power and high-performance operation. *IEEE Transactions on Computers*, 65 (8):2638–2644, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ju:2014:PAM**

[JJC14] Miao Ju, Hun Jung, and Hao Che. A performance analysis methodology for multicore, multithreaded processors. *IEEE Transactions on Computers*, 63(2): 276–289, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jang:2011:ERC**

[JJK+11] Jae-Wan Jang, Myeongjae Jeon, Hyo-Sil Kim, Heeseung Jo, Jin-Soo Kim, and Seungryoul Maeng. Energy reduction in consolidated servers through memory-aware virtual machine scheduling. *IEEE Transactions on Computers*, 60(4):552–564, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jing:2016:EEE**

[JJZ+16] N. Jing, L. Jiang, T. Zhang, C. Li, F. Fan, and X. Liang. Energy-efficient eDRAM-Based on-chip storage architecture for GPGPUs. *IEEE Transactions on Computers*, 65(1):122–135, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jarray:2015:CEM**

[JK15] A. Jarray and A. Karmouch. Cost-efficient mapping for fault-tolerant virtual networks. *IEEE Transactions on Computers*, 64 (3):668–681, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Joardar:2019:LBA**

[JKD+19] B. K. Joardar, R. G. Kim, J. R. Doppa, P. P. Pande, D. Marculescu, and R. Marculescu. Learning-based application-agnostic 3D NoC design for heterogeneous manycore systems. *IEEE*

Transactions on Computers, 68(6):852–866, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jo:2010:TFT**

[JKJ+10] Heeseung Jo, Hwanju Kim, Jae-Wan Jang, Joonwon Lee, and Seungryoul Maeng. Transparent fault tolerance of device drivers for virtual machines. *IEEE Transactions on Computers*, 59(11): 1466–1479, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432158.

**Jeannerod:2011:CFP**

[JKMR11] Claude-Pierr Jeannerod, Hervé Knochel, Christophe Monat, and Guillaume Revy. Computing floating-point square roots via bivariate polynomial evaluation. *IEEE Transactions on Computers*, 60(2):214–227, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jin:2010:DAC**

[JKY10] Yuho Jin, Eun Jung Kim, and Ki Hwan Yum. Design and analysis of on-chip networks for large-scale cache systems. *IEEE Transactions on Computers*, 59 (3):332–344, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5235140.

**Jankowski:2011:PAG**

[JL11] Krzysztof Jankowski and Pierre Laurent. Packed AES-GCM algorithm suitable for AES/PCLMULQDQ instructions. *IEEE Transactions on Computers*, 60 (1):135–138, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jeon:2010:LUS**

[JLC10] Hyeran Jeon, Woo Hyong Lee, and Sung Woo Chung. Load unbalancing strategy for multicore embedded processors. *IEEE Transactions on Computers*, 59(10): 1434–1440, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374370.

**Jeong:2019:OCG**

[JLKR19] I. Jeong, C. Lee, K. Kim, and W. W. Ro. OverCome: Coarse-grained instruction commit with handover register renaming. *IEEE Transactions on Computers*, 68 (12):1802–1816, December 2019. CODEN ITCOB4.

ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2019:LPU**

[JLLH19] H. Jiang, L. Liu, F. Lombardi, and J. Han. Low-power unsigned divider and square root circuit designs using adaptive approximation. *IEEE Transactions on Computers*, 68(11):1635–1646, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2010:SSS**

[JLMH10] J.-H. R. Jiang, Chih-Chun Lee, A. Mishchenko, and Chung-Yang Huang. To SAT or not to SAT: Scalable exploration of functional dependency. *IEEE Transactions on Computers*, 59(4):457–467, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374381`.

**Jeannerod:2011:MEP**

[JLMP11] Claude-Pierre Jeannerod, Nicolas Louvet, Jean-Michel Muller, and Adrien Panhaleux. Midpoints and exact points of some algebraic functions in floating-point arithmetic. *IEEE Transactions on Computers*, 60(2):228–241, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Joldes:2016:AAE**

[JMMP16] M. Joldes, O. Marty, J. M. Muller, and V. Popescu. Arithmetic algorithms for extended precision using floating-point expansions. *IEEE Transactions on Computers*, 65(4):1197–1210, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Johansson:2017:AEA**

[Joh17] Fredrik Johansson. Arb: Efficient arbitrary-precision midpoint–radius interval arithmetic. *IEEE Transactions on Computers*, 66(8):1281–1292, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7891956/`.

**Jiang:2013:DSO**

[JP13] Weirong Jiang and Viktor K. Prasanna. Data structure optimization for power-efficient IP lookup architectures. *IEEE Transactions on Computers*, 62(11):2169–2182, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jaberipur:2010:RDF**

[JPG10] G. Jaberipur, B. Parhami, and S. Gorgin. Redundant-digit floating-point addition scheme based on a stored rounding value. *IEEE Transactions on Computers*, 59(5):694–706, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5278659`.

**Jin:2013:OBF**

[JPLP13] Hao Jin, Deng Pan, Jason Liu, and Niki Pissinou. OpenFlow-based flow-level bandwidth provisioning for CICQ switches. *IEEE Transactions on Computers*, 62(9):1799–1812, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jo:2017:DLB**

[JR17] Myung Hyun Jo and Won Woo Ro. Dynamic load balancing of dispatch scheduling for solid state disks. *IEEE Transactions on Computers*, 66(6):1034–1047, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7776864/`.

**Jun:2014:EID**

[JRC14] Minje Jun, Won W. Ro, and Eui-Young Chung. Exploiting implementation diversity and partial connection of routers in application-specific network-on-chip topology synthesis. *IEEE Transactions on Computers*, 63(6):1434–1445, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiao:2018:CCL**

[JRJ+18] Xun Jiao, Abbas Rahimi, Yu Jiang, Jianguo Wang, Hamed Fatemi, Jose Pineda de Gyvez, and Rajesh K. Gupta. CLIM: A cross-level workload-aware timing error prediction model for functional units. *IEEE Transactions on Computers*, 67(6):771–783, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8207606/`.

**Joo:2014:RPE**

[JRP+14] Yongsoo Joo, Junhee Ryu, Sangsoo Park, Heonshik Shin, and Kang G. Shin. Rapid prototyping and evaluation of intelligence functions of active storage devices. *IEEE Transactions on Computers*, 63(9):2356–2368, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2015:SMC**

[JRS+15] Wanchun Jiang, Fengyuan Ren, Ran Shu, Yongwei Wu, and Chuang Lin. Sliding mode congestion control for data center Ethernet networks. *IEEE Transactions on Computers*, 64(9): 2675–2690, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2014:ABC**

[JRW+14] Wanchun Jiang, Fengyuan Ren, Yongwei Wu, Chuang Lin, and I. Stojmenovic. Analysis of backward congestion notification with delay for enhanced Ethernet networks. *IEEE Transactions on Computers*, 63(11): 2674–2684, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jalili:2017:EAS**

[JSA17] Majid Jalili and Hamid Sarbazi-Azad. Endurance-aware security enhancement in non-volatile memories using compression and selective encryption. *IEEE Transactions on Computers*, 66(7):1132–1144, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07792116-abs.html`.

**Jigang:2010:AAH**

[JSC10] Wu Jigang, T. Srikanthan, and Guang Chen. Algorithmic aspects of hardware/software partitioning: 1D search algorithms. *IEEE Transactions on Computers*, 59(4):532–544, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5332226`. See comments [HWG+14].

**Jang:2017:RAW**

[JSC+17] Jaemin Jang, Wongyu Shin, Jungwhan Choi, Jinwoong Suh, Yongkee Kwon, Yongju Kim, and Lee-Sup Kim. Refresh-aware write recovery memory controller. *IEEE Transactions on Computers*, 66(4):688–701, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jang:2019:SIW**

[JSC+19] J. Jang, W. Shin, J. Choi, Y. Kim, and L. Kim. Sparse-insertion write cache to mitigate write disturbance errors in phase change memory. *IEEE Transactions on Computers*, 68(5):752–764, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jayalath:2014:CAR**

[JSE14] Chamikara Jayalath, Julian Stephen, and Patrick Eugster. From the cloud to the atmosphere: Running MapReduce across data centers. *IEEE Transactions on Computers*, 63(1):74–87, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jeong:2017:DEV**

[JSH+17] Jaeyong Jeong, Youngsun Song, Sangwook Shane Hahn, Sungjin Lee, and Jihong Kim. Dynamic erase voltage and time scaling for extending lifetime of NAND flash-based SSDs. *IEEE Transactions on Computers*, 66(4):616–630, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jangjaimon:2015:ECR**

[JT15] I. Jangjaimon and Nian-Feng Tzeng. Effective cost reduction for elastic clouds under spot instance pricing through adaptive checkpointing. *IEEE Transactions on Computers*, 64 (2):396–409, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jung:2016:EDC**

[Jun16] M. Jung. Exploring design challenges in getting solid state drives closer to CPU. *IEEE Transactions on Computers*, 65(4):1103–1115, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jung:2016:NTB**

[JW16] J. Jung and Y. Won. `nvramdisk`: A transactional block device driver for non-volatile RAM. *IEEE Transactions on Computers*, 65 (2):589–600, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jun:2012:PCA**

[JWC12] Minje Jun, Deumji Woo, and Eui-Young Chung. Partial connection-aware topology synthesis for on-chip cascaded crossbar network. *IEEE Transactions on Computers*, 61(1):73–86, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2015:RTD**

[JWH+15] Guiyuan Jiang, Jigang Wu, Yajun Ha, Yi Wang, and Jizhou Sun. Reconfiguring three-dimensional processor arrays for fault-tolerance: Hardness and heuristic algorithms. *IEEE Transactions on Computers*, 64(10): 2926–2939, October 2015. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Jiang:2016:TEO**

[JWL+16] W. Jiang, J. Wu, F. Li, G. Wang, and H. Zheng. Trust evaluation in online social networks using generalized network flow. *IEEE Transactions on Computers*, 65(3):952–963, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2016:FOT**

[JWWZ16] W. Jiang, J. Wu, G. Wang, and H. Zheng. Forming opinions via trusted friends: Time-evolving rating prediction using fluid dynamics. *IEEE Transactions on Computers*, 65(4):1211–1224, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jing:2017:RAH**

[JYL+17] W. Jing, K. Yang, Y. Lin, B. Lee, S. Yoon, Y. Ye, Y. Du, and B. Chen. Retention-Aware Hybrid Main Memory (RAHMM): Big DRAM and little SCM. *IEEE Transactions on Computers*, 66(5):912–918, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Jiang:2010:INF**

[JZLD10] Song Jiang, Xuechen Zhang, Shuang Liang, and K. Davis. Improving networked file system performance using a locality-aware cooperative cache protocol. *IEEE Transactions on Computers*, 59 (11):1508–1519, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416680`.

**Kuan:2019:HEE**

[KA19] K. Kuan and T. Adegbija. HALLS: An energy-efficient highly adaptable last level STT-RAM cache for multicore systems. *IEEE Transactions on Computers*, 68(11): 1623–1634, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2015:DAM**

[KAH+15] Dae Hyun Kim, K. Athikulwongse, M. B. Healy, M. M. Hossain, Moongon Jung, I. Khorosh, G. Kumar, Young-Joon Lee, D. L. Lewis, Tzu-Wei Lin, Chang Liu, S. Panth, M. Pathak, Minzhen Ren, Guanhao Shen, Taigon Song, Dong Hyuk Woo, Xin Zhao, Joungho Kim, Ho Choi, G. H. Loh, H.-H. S. Lee, and Sung Kyu Lim. Design and analysis of 3D-

MAPS (3D Massively Parallel Processor with Stacked Memory). *IEEE Transactions on Computers*, 64 (1):112–125, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kamali:2018:DHT**

[KAH18a] Hadi Mardani Kamali, Kimia Zamiri Azar, and Shaahin Hessabi. DuCNoC: A high-throughput FPGA-based NoC simulator using dual-clock lightweight router micro-architecture. *IEEE Transactions on Computers*, 67(2):208–221, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8000664/`.

**Khdr:2018:AAB**

[KAH18b] Heba Khdr, Hussam Amrouch, and Jörg Henkel. Aging-aware boosting. *IEEE Transactions on Computers*, 67(9):1217–1230, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8319494/`.

**Khdr:2019:DGS**

[KAH19] Heba Khdr, Hussam Amrouch, and Jörg Henkel. Dynamic guardband selection: Thermal-aware optimization for unreliable multi-core systems. *IEEE Transactions on Computers*, 68(1):53–66, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8395346/`.

**Koziel:2018:HPS**

[KAK18] Brian Koziel, Reza Azarderakhsh, and Mehran Mozaffari Kermani. A high-performance and scalable hardware architecture for isogeny-based cryptography. *IEEE Transactions on Computers*, 67(11):1594–1609, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8315051/`.

**Kosmidis:2014:ECD**

[KAQC14] L. Kosmidis, J. Abella, E. Quinones, and F. J. Cazorla. Efficient cache designs for probabilistically analysable real-time systems. *IEEE Transactions on Computers*, 63(12): 2998–3011, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kong:2013:AAS**

[KASZ13] Jingfei Kong, Onur Aciicmez, Jean-Pierre Seifert, and Huiyang Zhou. Architecting against software cache-based side-channel attacks. *IEEE Transactions on Computers*, 62(7):1276–1288, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2010:AEH**

[KBH+10] Dong Kim, Kwanhu Bang, Seung-Hwan Ha, Sungroh Yoon, and Eui-Young Chung. Architecture exploration of high-performance PCs with a solid-state disk. *IEEE Transactions on Computers*, 59(7):878–890, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5438986.

**Kim:2019:EMA**

[KBO+19] M. S. Kim, A. A. D. Barrio, L. T. Oliveira, R. Hermida, and N. Bagherzadeh. Efficient Mitchell's approximate log multipliers for convolutional neural networks. *IEEE Transactions on Computers*, 68(5):660–675, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kouretas:2013:LPL**

[KBP13] Ioannis Kouretas, Charalambos Basetas, and Vassilis Paliouras. Low-power logarithmic number system addition/subtraction and their impact on digital filters. *IEEE Transactions on Computers*, 62(11):2196–2209, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kshemkalyani:2013:PDA**

[KC13] Ajay D. Kshemkalyani and Jiannong Cao. Predicate detection in asynchronous pervasive environments. *IEEE Transactions on Computers*, 62(9):1823–1836, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kao:2014:DBP**

[KC14] Yu-Hsiang Kao and H. Jonathan Chao. Design of a bufferless photonic clos network-on-chip architecture. *IEEE Transactions on Computers*, 63(3):764–776, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2018:SWT**

[KCE+18] Kang-Wook Kim, Youngeun Cho, Jeongyoon Eo, Chang-Gun Lee, and Junghee Han. System-wide time versus density tradeoff in real-time multicore fluid scheduling. *IEEE Transactions on Computers*, 67(7):1007–1022, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8259245/.

**Koo:2016:TRE**

[KCK16] N. Koo, G. H. Cho, and S. Kwon. On $r$-th root extraction algorithm in $\mathbf{F}_q$ for $q \equiv lr^s (\bmod r^{s+1})$ with $0 < l < r$ and small $s$. *IEEE Transactions on Computers*, 65(1):322–325, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2019:EDA**

[KCKL19] M. Kim, J. Choi, H. Kim, and H. Lee. An effective DRAM address remapping for mitigating rowhammer errors. *IEEE Transactions on Computers*, 68(10): 1428–1441, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2016:WNV**

[KCL+16] R. G. Kim, W. Choi, G. Liu, E. Mohandesi, P. P. Pande, D. Marculescu, and R. Marculescu. Wireless NoC for VFI-enabled multicore chip design: Performance evaluation and design tradeoffs. *IEEE Transactions on Computers*, 65(4):1323–1336, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2019:STC**

[KCL19] M. Kim, I. Chang, and H. Lee. Segmented tag cache: A novel cache organization for reducing dynamic read energy. *IEEE Transactions on Computers*, 68(10): 1546–1552, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2014:CEC**

[KCRG14] Seung Hun Kim, Dongmin Choi, Won Woo Ro, and Jean-Luc Gaudiot. Complexity-effective contention management with dynamic backoff for transactional memory systems. *IEEE Transactions on Computers*, 63(7):1696–1708, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2015:NVF**

[KCRG15] Keunsoo Kim, B. Y. Cho, Won Woo Ro, and J.-L. Gaudiot. Network variation and fault tolerant performance acceleration in mobile devices with simultaneous remote execution. *IEEE Transactions on Computers*, 64(10):2862–2874, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kinsy:2013:OHA**

[KCS+13] Michel A. Kinsy, Myong Hyon Cho, Keun Sup Shim, Mieszko Lis, G. Edward Suh, and Srinivas Devadas. Optimal and heuristic application-aware oblivious routing. *IEEE Transactions on Computers*, 62 (1):59–73, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kuo:2014:MET**

[KCS14] Fang-Chen Kuo, Yeim-Kuan Chang, and Cheng-Chien Su. A memory-efficient TCAM coprocessor for IPv4/IPv6 routing table update. *IEEE Transactions on Computers*, 63(9):2110–2121, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kang:2017:PDS**

[KCW+17] Wang Kang, Liang Chang, Zhaohao Wang, Weifeng Lv, Guanyu Sun, and Weisheng Zhao. Pseudo-differential sensing framework for STT-MRAM: A cross-layer perspective. *IEEE Transactions on Computers*, 66 (3):531–544, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2018:MOL**

[KCY18] Sae-Eun Kim, Jaeyong Chung, and Joon-Sung Yang. Mitigating observability loss of toggle-BasedX-masking via scan chain partitioning. *IEEE Transactions on Computers*, 67(8): 1184–1192, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8280565/`.

**Kozhaya:2019:RBB**

[KDEV19] David Kozhaya, Jérémie Decouchant, and Paulo Esteves-Verissimo. RT-ByzCast: Byzantine-resilient real-time reliable broadcast. *IEEE Transactions on Computers*, 68(3):440–454, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8470958/`.

**Kara:2019:ALS**

[KE19] Orhun Kara and Muhammed F. Esgin. On analysis of lightweight stream ciphers with keyed update. *IEEE Transactions on Computers*, 68(1):99–110, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8400392/`.

**Kizilkale:2016:MDM**

[KEK16] Can Kızılkale, Ömer Eğecioğlu, and Çetin Kaya Koç. A matrix decomposition method for optimal normal basis multiplication. *IEEE Transactions on Computers*, 65 (11):3239–3250, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Khemka:2015:UFR**

[KFB+15] B. Khemka, R. Friese, L. D. Briceno, A. A. Maciejewski, G. A. Koenig, G. Okonski, M. M. Hilton, R. Rambharos, S. Poole, and C. Groer. Utility functions and resource management in an oversubscribed heterogeneous computing environment. *IEEE Transactions on Computers*, 64 (8):2394–2407, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Khan:2014:DRS**

[KGC14] Faisal Khan, Soheil Ghiasi, and Chen-Nee Chuah. A dynamically reconfigurable system for closed-loop measurements of network traffic. *IEEE Transactions on Computers*, 63(2):263–275, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kadric:2016:APF**

[KGD16] Edin Kadric, Paul Gurniak, and André DeHon. Accurate parallel floating-point accumulation. *IEEE Transactions on Computers*, 65(11): 3224–3238, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2014:SLS**

[KGGJ14] Hyungjun Kim, Boris Grot, Paul V. Gratz, and Daniel A. Jimenez. Spatial locality speculation to reduce energy in chip-multiprocessor networks-on-chip. *IEEE Transactions on Computers*, 63(3):543–556, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kashif:2015:SSL**

[KGP15] H. Kashif, S. Gholamian, and H. Patel. SLA: A stage-level latency analysis for real-time communication in a pipelined resource model. *IEEE Transactions on Computers*, 64 (4):1177–1190, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Khedr:2016:SSH**

[KGV16] Alhassan Khedr, Glenn Gulak, and Vinod Vaikuntanathan. SHIELD: Scalable homomorphic implementation of encrypted data-classifiers. *IEEE Transactions on Computers*, 65(9): 2848–2858, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kwon:2010:SEB**

[KH10] Taekyoung Kwon and Jin Hong. Secure and efficient broadcast authentication in wireless sensor networks. *IEEE Transactions on Computers*, 59 (8):1120–1133, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5313804.

**Koohi:2014:AOW**

[KH14] Somayyeh Koohi and Shaahin Hessabi. All-optical wavelength-routed architecture for a power-efficient network on chip. *IEEE Transactions on Computers*, 63(3):777–792, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2018:EDS**

[KH18] Changdae Kim and Jaehyuk Huh. Exploring the design space of fair scheduling supports for asymmetric multicore systems. *IEEE Transactions on Computers*, 67 (8):1136–1152, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8265024/.

**Kim:2016:EPE**

[KHPP16] Intae Kim, Seong Oun Hwang, Jong Hwan Park, and Chanil Park. An efficient predicate encryption with constant pairing computations and minimum costs. *IEEE Transactions on Computers*, 65(10): 2947–2958, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**kanduri:2018:ATA**

[khR+18] Anil kanduri, Mohammad-Hashem haghbayan, Amir M. Rahmani, Muhammad Shafique, Axel Jantsch, and Pasi Liljeberg. adBoost: Thermal aware performance boosting through dark silicon patterning. *IEEE Transactions on Computers*, 67 (8):1062–1077, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8292829/.

**Kumm:2017:OCM**

[KHZ17] Martin Kumm, Martin Hardieck, and Peter Zipf. Optimization of constant matrix multiplication with low power and high throughput. *IEEE Transactions* on Computers, 66(12):2072–2080, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7919250/.

**Kaseridis:2014:CFA**

[KIJ14] Dimitris Kaseridis, Muhammad Faisal Iqbal, and Lizy Kurian John. Cache friendliness-aware management of shared last-level caches for high performance multi-core systems. *IEEE Transactions on Computers*, 63(4):874–887, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2015:CEH**

[Kim15] Y. Kim. Comments on "An Efficient Homomorphic MAC with Small Key Size for Authentication in Network Coding. *IEEE Transactions on Computers*, 64(12):3619–3620, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [CJ13].

**Kim:2011:XEC**

[KJL11] Hwanju Kim, Heeseung Jo, and Joonwon Lee. XHive: Efficient cooperative caching for virtual machines. *IEEE Transactions on Computers*, 60(1):106–119, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Khan:2010:TRR**

[KK10] O. Khan and S. Kundu. Thread relocation: a runtime architecture for tolerating hard errors in chip multiprocessors. *IEEE Transactions on Computers*, 59(5):651–665, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5010431.

**Kumm:2018:ACT**

[KK18] Martin Kumm and Johannes Kappauf. Advanced compressor tree synthesis for FPGAs. *IEEE Transactions on Computers*, 67(8):1078–1091, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8263391/.

**Kim:2015:SCF**

[KkC15a] Jae Min Kim, Young Geun kim, and Sung Woo Chung. Stabilizing CPU frequency and voltage for temperature-aware DVFS in mobile devices. *IEEE Transactions on Computers*, 64(1):286–292, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kong:2015:EEL**

[KKC15b] Joonho Kong, F. Koushanfar, and Sung Woo Chung. An energy-efficient last-level cache architecture for process variation-tolerant 3D microprocessors. *IEEE Transactions on Computers*, 64(9):2460–2475, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2017:EEE**

[KKC17] Young Geun Kim, Minyong Kim, and Sung Woo Chung. Enhancing energy efficiency of multimedia applications in heterogeneous mobile multi-core processors. *IEEE Transactions on Computers*, 66 (11):1878–1889, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7937919/`.

**Kalyanaraman:2014:SRC**

[KKH+14] Shivkumar Kalyanaraman, Isaac Keslassy, Inbal Horev, Pu Li, and Ori Rottenstreich. The switch reordering contagion: Preventing a few late packets from ruining the whole party. *IEEE Transactions on Computers*, 63(5):1262–1276, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Koh:2019:DCM**

[KKJH19] Kwangwon Koh, Kangho Kim, Seunghyub Jeon, and Jaehyuk Huh. Disaggregated cloud memory with elastic block management. *IEEE Transactions on Computers*, 68(1):39–52, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8400405/`.

**Kekely:2016:SDM**

[KKP+16] L. Kekely, J. Kucera, V. Pus, J. Korenek, and A. V. Vasilakos. Software defined monitoring of application protocols. *IEEE Transactions on Computers*, 65 (2):615–626, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kong:2014:DGD**

[KKS14] Inwook Kong, Seong-Wan Kim, and E. E. Swartzlander. Design of Goldschmidt dividers with quantum-dot cellular automata. *IEEE Transactions on Computers*, 63(10):2620–2625, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kumarage:2015:GEA**

[KKT15] H. Kumarage, I. Khalil, and Z. Tari. Granular evaluation of anomalies in wireless sensor networks using dynamic data partitioning with an entropy criteria. *IEEE Transactions on Computers*, 64(9): 2573–2585, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2016:DAH**

[KKY+16] H. Kim, G. Kim, H. Yeo, J. Kim, and S. Maeng. Design and analysis of hybrid flow control for hierarchical ring network-on-chip. *IEEE Transactions on Computers*, 65(2):480–494, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kumar:2013:NHM**

[KL13] T. Nandha Kumar and Fabrizio Lombardi. A novel heuristic method for application-dependent testing of a SRAM-based FPGA interconnect. *IEEE Transactions on Computers*, 62 (1):163–172, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kuo:2016:RSF**

[KL16] Wen-Hsing Kuo and Yung-Hsuan Lin. Resource-saving file management scheme for online video provisioning on content delivery networks. *IEEE Transactions on Computers*, 65(6):1910–1920, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2016:CLR**

[KLC+16] J. Kim, E. Lee, J. Choi, D. Lee, and S. H. Noh. Chip-level RAID with flexible stripe size and parity placement for enhanced SSD reliability. *IEEE Transactions on Computers*, 65 (4):1116–1130, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2018:GAP**

[KLC18] Mincheol Kim, Ling Liu, and Wonik Choi. A GPU-aware parallel index for processing high-dimensional big data. *IEEE Transactions on Computers*, 67(10): 1388–1402, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8332539/`.

**Kim:2014:CLE**

[KLJ+14] Seung Hun Kim, Sang Hyong Lee, Minje Jun, Byunghoon Lee, Won Woo Ro, Eui-Young Chung, and Jean-Luc Gaudiot. C-Lock: Energy efficient synchronization for embedded multicore systems. *IEEE Transactions on Computers*, 63

(8):1962–1974, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2014:LON**

[KLK+14] Gwangsun Kim, Michael Mihn-Jong Lee, John Kim, Jae W. Lee, Dennis Abts, and Michael Marty. Low-overhead network-on-chip support for location-oblivious task placement. *IEEE Transactions on Computers*, 63(6):1487–1500, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2017:TIB**

[KLK17] Jesung Kim, Jongmin Lee, and Soontae Kim. TLB index-based tagging for reducing data cache and TLB energy consumption. *IEEE Transactions on Computers*, 66(7):1200–1211, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07805198-abs.html`.

**Kang:2018:SAS**

[KLK18] Mincheol Kang, Wonyoung Lee, and Soontae Kim. Subpage-aware solid state drive for improving lifetime and performance. *IEEE Transactions on Computers*, 67(10):1492–1505, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8338090/`.

**Kim:2013:SED**

[KLKL13] Minjang Kim, Nagesh B. Lakshminarayana, Hyesoon Kim, and Chi-Keung Luk. SD3: An efficient dynamic data-dependence profiling mechanism. *IEEE Transactions on Computers*, 62(12): 2516–2530, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2011:RPN**

[KLLK11] Jong-Chan Kim, Duhee Lee, Chang-Gun Lee, and Kanghee Kim. RT-PLRU: a new paging scheme for real-time execution of program codes on NAND flash memory for portable media players. *IEEE Transactions on Computers*, 60 (8):1126–1141, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kornerup:2012:CCR**

[KLLM12] Peter Kornerup, Vincent Lefèvre, Nicolas Louvet, and Jean-Michel Muller. On the computation of correctly rounded sums. *IEEE Transactions on Computers*, 61 (3):289–298, March 2012. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Kuo:2016:CDA**

[KLT16] Tung-Wei Kuo, Kate Ching-Ju Lin, and Ming-Jer Tsai. On the construction of data aggregation tree with minimum energy cost in wireless sensor networks: NP-completeness and approximation algorithms. *IEEE Transactions on Computers*, 65(10):3109–3121, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kargahi:2011:POB**

[KM11] Mehdi Kargahi and Ali Movaghar. Performance optimization based on analytical modeling in a real-time system with constrained time/utility functions. *IEEE Transactions on Computers*, 60(8):1169–1181, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kedar:2017:SSP**

[KMC17] Gil Kedar, Avi Mendelson, and Israel Cidon. SPACE: Semi-PArtitioned CachE for energy efficient, hard real-time systems. *IEEE Transactions on Computers*, 66(4):717–730, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Karimi:2011:WCC**

[KMJ+11] Naghmeh Karimi, Michail Maniatakos, Abhijit Jas, Chandra Tirumurti, and Yiorgos Makris. Workload-cognizant concurrent error detection in the scheduler of a modern microprocessor. *IEEE Transactions on Computers*, 60(9):1274–1287, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669287.

**Khalgui:2011:RME**

[KMLH11] Mohamed Khalgui, Olfa Mosbahi, ZhiWu Li, and Hans-Michael Hanisch. Reconfigurable multiagent embedded control systems: From modeling to implementation. *IEEE Transactions on Computers*, 60(4):538–551, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kliuchnikov:2016:PAS**

[KMM16] V. Kliuchnikov, D. Maslov, and M. Mosca. Practical approximation of single-qubit unitaries by single-qubit quantum Clifford and $T$ circuits. *IEEE Transactions on Computers*, 65

(1):161–172, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kornerup:2011:PAO**

[KMP11] Peter Kornerup, Jean-Michel Muller, and Adrien Panhaleux. Performing arithmetic operations on round-to-nearest representations. *IEEE Transactions on Computers*, 60(2):282–291, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kinsman:2011:CVM**

[KN11a] Adam B. Kinsman and Nicola Nicolici. Computational vector-magnitude-based range determination for scientific abstract data types. *IEEE Transactions on Computers*, 60(11): 1652–1663, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669288`.

**Kinsman:2011:TOT**

[KN11b] Adam B. Kinsman and Nicola Nicolici. Trade-offs in test data compression and deterministic X-masking of responses. *IEEE Transactions on Computers*, 60(4):498–507, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ko:2012:MTC**

[KN12] Ho Fai Ko and Nicola Nicolici. Mapping trigger conditions onto trigger units during post-silicon validation and debugging. *IEEE Transactions on Computers*, 61(11):1563–1575, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kinsman:2013:NBF**

[KN13] Phillip J. Kinsman and Nicola Nicolici. NoC-based FPGA acceleration for Monte Carlo simulations with applications to SPECT imaging. *IEEE Transactions on Computers*, 62(3):524–535, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2014:ENL**

[KO14] Jihye Kim and Hyunok Oh. An efficient non-linear cost compression algorithm for multi level cell memory. *IEEE Transactions on Computers*, 63(4):820–832, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kornerup:2015:RHR**

[Kor15] P. Kornerup. Reviewing high-radix signed-digit adders. *IEEE Transactions on Computers*, 64(5):1502–1505, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2013:PMM**

[KP13] Sook-Yeon Kim and Jung-Heum Park. Paired many-to-many disjoint path covers in recursive circulants ($G(2^m, 4)$). *IEEE Transactions on Computers*, 62(12): 2468–2475, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2015:CBR**

[KP15] Chulmin Kim and Kyu Ho Park. Credit-based runtime placement of virtual machines on a single NUMA system for QoS of data access performance. *IEEE Transactions on Computers*, 64(6):1633–1646, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kaitoua:2017:FSG**

[KPBC17] Abdulrahman Kaitoua, Pietro Pinoli, Michele Bertoni, and Stefano Ceri. Framework for supporting genomic operations. *IEEE Transactions on Computers*, 66 (3):443–457, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Khdr:2017:PDA**

[KPS+17] Heba Khdr, Santiago Pagani, Éricles Sousa, Vahid Lari, Anuj Pathania, Frank Hannig, Muhammad Shafique, Jürgen Teich, and Jörg Henkel. Power density-aware resource management for heterogeneous tiled multicores. *IEEE Transactions on Computers*, 66(3):488–501, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2018:SBL**

[KRP18] Jaehyung Kim, Hongchan Roh, and Sanghyun Park. Selective I/O bypass and load balancing method for write-through SSD caching in big data analytics. *IEEE Transactions on Computers*, 67(4):589–595, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8100898/`.

**Karmakar:2018:CTD**

[KRR+18] Angshuman Karmakar, Sujoy Sinha Roy, Oscar Reparaz, Frederik Vercauteren, and Ingrid Verbauwhede. Constant-time discrete Gaussian sampling. *IEEE Transac-*

*tions on Computers*, 67(11): 1561–1571, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8314133/`.

**Kang:2010:ISG**

[KS10a] Kyungtae Kang and Lui Sha. An interleaving structure for guaranteed QoS in real-time broadcasting systems. *IEEE Transactions on Computers*, 59(5):666–678, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5276796`.

**Kong:2010:RMR**

[KS10b] Inwook Kong and E. E. Swartzlander. A rounding method to reduce the required multiplier precision for Goldschmidt division. *IEEE Transactions on Computers*, 59(12): 1703–1708, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453345`.

**Kim:2012:SMR**

[KS12] Eric P. Kim and Naresh R. Shanbhag. Soft N-modular redundancy. *IEEE Transactions on Computers*, 61 (3):323–336, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kharbutli:2014:LLA**

[KS14] Mazen Kharbutli and Rami Sheikh. LACS: A locality-aware cost-sensitive cache replacement algorithm. *IEEE Transactions on Computers*, 63(8):1975–1987, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kong:2014:LPV**

[KSC+14] Joonho Kong, Taeweon Suh, Sung Woo Chung, Junhee Lee, and Hyung Beom Jang. Leveraging process variation for performance and energy: In the perspective of overclocking. *IEEE Transactions on Computers*, 63 (5):1316–1322, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kayi:2015:ACC**

[KSEG15] A. Kayi, O. Serres, and T. El-Ghazawi. Adaptive cache coherence mechanisms with producer-consumer sharing optimization for chip multiprocessors. *IEEE Transactions on Computers*, 64(2):316–328, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2012:PAM**

[KSJ+12] Jaehong Kim, Sangwon Seo, Dawoon Jung, Jin-Soo Kim, and Jaehyuk Huh. Parameter-aware I/O management for solid state disks (SSDs). *IEEE Transactions on Computers*, 61(5):636–649, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kayaalp:2015:SBP**

[KSN+15] M. Kayaalp, T. Schmitt, J. Nomani, D. Ponomarev, and N. Abu Ghazaleh. Signature-based protection from code reuse attacks. *IEEE Transactions on Computers*, 64(2):533–546, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kang:2012:DIE**

[KSS12] Woochul Kang, Sang Hyuk Son, and John A. Stankovic. Design, implementation, and evaluation of a QoS-aware real-time embedded database. *IEEE Transactions on Computers*, 61 (1):45–59, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kyrkou:2012:PHA**

[KT12] Christos Kyrkou and Theocharis Theocharides. A parallel hardware architecture for real-time object detection with support vector machines. *IEEE Transactions on Computers*, 61(6):831–842, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kito:2019:CED**

[KT19] N. Kito and N. Takagi. Concurrent error detectable carry select adder with easy testability. *IEEE Transactions on Computers*, 68 (7):1105–1110, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kavousianos:2014:TDT**

[KTA+14] Xrysovalantis Kavousianos, Yiorgos Tsiatouhas, Angela Arapoyanni, Andreas Floros, and Stefanos Valadimas. The time dilation technique for timing error tolerance. *IEEE Transactions on Computers*, 63(5):1277–1286, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kanoun:2016:BDS**

[KTAvdS16] Karim Kanoun, Cem Tekin, David Atienza, and Mihaela van der Schaar. Big-data streaming applications scheduling based on staged multi-armed bandits. *IEEE Transactions on Computers*, 65(12):3591–3605, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kurka:2012:FAA**

[Kůr12] Petr Kůrka. Fast arithmetical algorithms in Möbius number systems. *IEEE Transactions on Computers*, 61(8):1097–1109, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Knezevic:2010:FIM**

[KVV10] M. Knezevic, F. Vercauteren, and I. Verbauwhede. Faster interleaved modular multiplication based on Barrett and Montgomery reduction methods. *IEEE Transactions on Computers*, 59(12):1715–1721, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453352.

**Karpovsky:2014:DSS**

[KW14] M. Karpovsky and Zhen Wang. Design of strongly secure communication and computation channels by nonlinear error detecting codes. *IEEE Transactions on Computers*, 63(11):2716–2728, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kim:2016:ECI**

[KWC+16] Dongwook Kim, Youjip Won, Jaehyuk Cha, Sungroh Yoon, Jongmoo Choi, and Sooyong Kang. Exploiting compression-induced internal fragmentation for power-off recovery in SSD. *IEEE Transactions on Computers*, 65(6):1720–1733, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kang:2015:OCS**

[KwPK+15] Shin-Haeng Kang, Hae woo Park, Sungchan Kim, Hyunok Oh, and Soonhoi Ha. Optimal checkpoint selection with dual-modular redundancy hardening. *IEEE Transactions on Computers*, 64(7):2036–2048, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Kuang:2019:STD**

[KYZC19] Liwei Kuang, Laurence T. Yang, Qing Zhu, and Jinjun Chen. Secure tensor decomposition for big data using transparent computing paradigm. *IEEE Transactions on Computers*, 68(4):585–596, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8310010/.

**Lamberti:2011:RCT**

[LAAM11] Fabrizio Lamberti, Nikos Andrikos, Elisardo Antelo, and Paolo Montuschi. Reducing the computation time in (short bit-width) two's complement multipliers. *IEEE Transactions on Computers*, 60(2):148–156, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2013:PAE**

[LB13] Bin Liu and Bevan M. Baas. Parallel AES encryption engines for many-core processor arrays. *IEEE Transactions on Computers*, 62 (3):536–547, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2015:AGP**

[LB15a] D. T. Lee and J. Bruck. Algorithms for generating probabilities with multivalued stochastic relay circuits. *IEEE Transactions on Computers*, 64(12):3376–3388, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lubicz:2015:TOB**

[LB15b] D. Lubicz and N. Bochard. Towards an oscillator based TRNG with a certified entropy rate. *IEEE Transactions on Computers*, 64 (4):1191–1200, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2014:CDW**

[LBN14] Soyoon Lee, Hyokyung Bahn, and Sam H. Noh. CLOCK-DWF: A write-history-aware page replacement algorithm for hybrid PCM and DRAM memory architectures. *IEEE Transactions on Computers*, 63(9): 2187–2200, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lukic:2015:RCE**

[LBS15] M. Lukic, A. Barnawi, and I. Stojmenovic. Robot coordination for energy-balanced matching and sequence dispatch of robots to events. *IEEE Transactions on Computers*, 64 (5):1416–1428, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Laga:2017:MMR**

[LBSK17] Arezki Laga, Jalil Boukhobza, Frank Singhoff, and Michel Koskas. MONTRES: Merge ON-the-Run External Sorting algorithm for large data volumes on SSD based storage systems. *IEEE Transactions on Computers*, 66(10): 1689–1702, October 2017. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7932112/.

**Liao:2011:IBG**

[LBWH11] Xue-Liang Liao, Shi Bai, Yu-Ping Wang, and Shi-Min Hu. ISRA-based grouping: a disk reorganization approach for disk energy conservation and disk performance enhancement. *IEEE Transactions on Computers*, 60 (2):292–304, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2016:TRT**

[LC16a] Yu Li and Albert Mo Kim Cheng. Transparent real-time task scheduling on temporal resource partitions. *IEEE Transactions on Computers*, 65(5):1646–1655, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:RQR**

[LC16b] Huai Liu and Tsong Yueh Chen. Randomized quasi-random testing. *IEEE Transactions on Computers*, 65(6):1896–1909, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lhotak:2010:OER**

[LCA10] O. Lhotak, S. Curial, and J. N. Amaral. An optimal encoding to represent a single set in an ROBDD. *IEEE Transactions on Computers*, 59(4):574–575, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184823.

**Lee:2010:PFT**

[LCC10] Hyunjin Lee, Sangyeun Cho, and B. R. Childers. PERFECTORY: a fault-tolerant directory memory architecture. *IEEE Transactions on Computers*, 59(5):638–650, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255232.

**Liu:2013:IAG**

[LCCJ13] Yining Liu, Chi Cheng, Jianyu Cao, and Tao Jiang. An improved authenticated group key transfer protocol based on secret sharing. *IEEE Transactions on Computers*, 62(11):2335–2336, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2013:CEL**

[LCH13] Jui-Chieh Lin, Sao-Jie Chen, and Yu Hen Hu. Cycle-efficient LFSR implementation on word-based microarchitecture. *IEEE Transactions on Computers*, 62 (4):832–838, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:SDD**

[LCH+15] J. Li, X. Chen, X. Huang, S. Tang, Y. Xiang, M. M. Hassan, and A. Alelaiwi. Secure distributed deduplication systems with improved reliability. *IEEE Transactions on Computers*, 64(12): 3569–3579, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liang:2014:OSW**

[LCHC14] Qilian Liang, Xiuzhen Cheng, Scott C. H. Huang, and Dechang Chen. Opportunistic sensing in wireless sensor networks: Theory and application. *IEEE Transactions on Computers*, 63 (8):2002–2010, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2011:DFS**

[LCHX11] Alex X. Liu, Fei Chen, Jee-Hyun Hwang, and Tao Xie. Designing fast and scalable XACML policy evaluation engines. *IEEE Transactions on Computers*, 60(12): 1802–1817, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Leu:2015:HSS**

[LCL15] Jenq-Shiou Leu, Jheng-Huei Chen, and Kuen-Han Li. Hybrid search scheme for social networks supported by dynamic weighted distributed label clustering. *IEEE Transactions on Computers*, 64(9):2586–2594, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2017:HDL**

[LCL17] Shih-Hsiang Lin, Pei-Yin Chen, and Yu-Ning Lin. Hardware design of low-power high-throughput sorting unit. *IEEE Transactions on Computers*, 66(8): 1383–1395, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7862779/`.

**Luo:2019:TRE**

[LCLC19] J. Luo, H. Cheng, I. Lin, and D. Chang. TAP: Reducing the energy of asymmetric hybrid last-level cache via thrashing aware placement and migration. *IEEE*

*Transactions on Computers*, 68(12):1704–1719, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:CEH**

[LCLL15] Chen Li, Le Chen, Rongxing Lu, and Hui Li. Comment on "An Efficient Homomorphic MAC with Small Key Size for Authentication in Network Coding". *IEEE Transactions on Computers*, 64(3):882–883, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [CJ13].

**Liu:2011:MEL**

[LCT11] Bing-Hong Liu, Min-Lun Chen, and Ming-Jer Tsai. Message-efficient location prediction for mobile objects in wireless sensor networks using a maximum likelihood technique. *IEEE Transactions on Computers*, 60(6):865–878, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Luo:2015:CEL**

[LCW+15] Chuan Luo, Shaowei Cai, Wei Wu, Zhong Jie, and Kaile Su. CCLS: An efficient local search algorithm for weighted maximum satisfiability. *IEEE Transactions on Computers*, 64 (7):1830–1843, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:DAI**

[LCW+16] W. Liu, L. Chen, C. Wang, M. O'Neill, and F. Lombardi. Design and analysis of inexact floating-point adders. *IEEE Transactions on Computers*, 65(1):308–314, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liao:2010:SPK**

[LCwW10] Xiaofeng Liao, Fei Chen, and Kwok wo Wong. On the security of public-key algorithms based on Chebyshev polynomials over the finite field $Z_N$. *IEEE Transactions on Computers*, 59(10): 1392–1401, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5487511`.

**Li:2016:TCM**

[LCX+16] J. Li, J. J. Chen, M. Xiong, G. Li, and W. Wei. Temporal consistency maintenance upon partitioned multiprocessor platforms. *IEEE Transactions on Computers*, 65(5):1632–1645, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2013:FMT**

[LCY+13] Chen-Wei Lin, Hung-Hsin Chen, Hao-Yu Yang, Chin-Yuan Huang, Mango C.-T. Chao, and Rei-Fu Huang. Fault models and test methods for subthreshold SRAMs. *IEEE Transactions on Computers*, 62 (3):468–481, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:IRP**

[LCY+16] R. S. Liu, M. Y. Chuang, C. L. Yang, C. H. Li, K. C. Ho, and H. P. Li. Improving read performance of NAND flash SSDs by exploiting error locality. *IEEE Transactions on Computers*, 65(4):1090–1102, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2019:DAA**

[LCY+19] W. Liu, T. Cao, P. Yin, Y. Zhu, C. Wang, E. E. Swartzlander, and F. Lombardi. Design and analysis of approximate redundant binary multipliers. *IEEE Transactions on Computers*, 68(6):804–819, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2019:MDF**

[LcZLG19] G. Li, c. Zhou, J. Li, and B. Guo. Maintaining data freshness in distributed cyber-physical systems. *IEEE Transactions on Computers*, 68(7):1077–1090, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:RWN**

[LDB+17] Xian Li, Karthi Duraisamy, Joe Baylon, Turbo Majumder, Guopeng Wei, Paul Bogdan, Deukhyoun Heo, and Partha Pratim Pande. A reconfigurable wireless NoC for large scale microbiome community analysis. *IEEE Transactions on Computers*, 66(10):1653–1666, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7932130/`.

**Li:2017:DPA**

[LDL+17] Guohui Li, Chenggang Deng, Jianjun Li, Quan Zhou, and Wei Wei. Deadline and period assignment for update transactions in co-scheduling environment. *IEEE Transactions on Computers*, 66 (7):1119–1131, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07797455-abs.html`.

**Li:2016:PPP**

[LDMQ16] Y. Li, W. Dai, Z. Ming, and M. Qiu. Privacy protection for preventing data over-collection in Smart City. *IEEE Transactions on Computers*, 65(5):1339–1350, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2010:EMV**

[LDP10] Bin Li, Lide Duan, and Lu Peng. Efficient microarchitectural vulnerabilities prediction using boosted regression trees and patient rule inductions. *IEEE Transactions on Computers*, 59(5):593–607, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255234`.

**Lee:2012:CPS**

[Lee12] Mun-Kyu Lee. Comments on "Provably Sublinear Point Multiplication on Koblitz Curves and Its Hardware Implementation". *IEEE Transactions on Computers*, 61(4):591–592, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [DJJ+08].

**Lee:2017:ISA**

[Lee17] Jinkyu Lee. Improved schedulability analysis using carry-in limitation for non-preemptive fixed-priority multiprocessor scheduling. *IEEE Transactions on Computers*, 66(10):1816–1823, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7927414/`.

**Lefevre:2017:CRA**

[Lef17] Vincent Lefèvre. Correctly rounded arbitrary-precision floating-point summation. *IEEE Transactions on Computers*, 66(12):2111–2124, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7891894/`.

**Liu:2016:BEA**

[LFH+16] P. Liu, L. Fang, M. C. Huang, Q. Hu, and G. Jiang. Building expressive and area-efficient directories with hybrid representation and adaptive multi-granular tracking. *IEEE Transactions on Computers*, 65(3):847–859, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ling:2013:NPS**

[LFJ+13] Zhen Ling, Xinwen Fu, Weijia Jia, Wei Yu, Dong Xuan, and Junzhou Luo. Novel packet size-based covert channel attacks against anonymizer. *IEEE Transactions on Computers*, 62(12): 2411–2426, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Luo:2015:VRA**

[LGF+15] Juan Luo, Yaling Guo, Shan Fu, Keqin Li, and Wenfeng He. Virtual resource allocation based on link interference in Cayley wireless data centers. *IEEE Transactions on Computers*, 64(10): 3016–3021, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:EEC**

[LGH15] Peng Li, Song Guo, and Jiankun Hu. Energy-efficient cooperative communications for multimedia applications in multi-channel wireless networks. *IEEE Transactions on Computers*, 64 (6):1670–1679, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2017:ECC**

[LGH+17] Z. Liu, J. Großschädl, Z. Hu, K. Järvinen, H. Wang, and I. Verbauwhede. Elliptic curve cryptography with efficiently computable endomorphisms and its hardware implementations for the Internet of Things. *IEEE Transactions on Computers*, 66(5):773–785, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Loghi:2010:ALP**

[LGMP10] M. Loghi, O. Golubeva, E. Macii, and M. Poncino. Architectural leakage power minimization of scratchpad memories by application-driven subbanking. *IEEE Transactions on Computers*, 59(7):891–904, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416686`.

**Liu:2018:SAI**

[LGS+18] Di Liu, Nan Guan, Jelena Spasic, Gang Chen, Songran Liu, Todor Stefanov, and Wang Yi. Scheduling analysis of imprecise mixed-criticality real-time tasks. *IEEE Transactions on Computers*, 67(7):975–991, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8247214/`.

**Lin:2011:CGA**

[LH11] Shang-Wei Lin and Pao-Ann Hsiung. Counterexample-guided assume-guarantee synthesis through learning.

*IEEE Transactions on Computers*, 60(5):734–750, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2012:PMC**

[LH12a] Chia-Wei Lee and Sun-Yuan Hsieh. Pancyclicity of matching composition networks under the conditional fault model. *IEEE Transactions on Computers*, 61 (2):278–183, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2012:MCP**

[LH12b] Shang-Wei Lin and Pao-Ann Hsiung. Model checking prioritized timed systems. *IEEE Transactions on Computers*, 61(6):843–856, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2016:HSC**

[LH16] Yuan-Cheng Lee and Chih-Wen Hsueh. Hardware/ software co-design of memory page translation for mobile virtualization. *IEEE Transactions on Computers*, 65(10):3070–3082, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lam:2014:SAD**

[LHC+14] Kam-Yiu Lam, Song Han, Deji Chen, Ming Xiong, Krithi Ramamritham, and Aloysius K. Mok. Schedulability analysis of deferrable scheduling algorithms for maintaining real-time data freshness. *IEEE Transactions on Computers*, 63 (4):979–994, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lien:2013:EDA**

[LHCL13] Chih-Yuan Lien, Chien-Chuan Huang, Pei-Yin Chen, and Yi-Fan Lin. An efficient denoising architecture for removal of impulse noise in images. *IEEE Transactions on Computers*, 62(4):631–643, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2014:DBS**

[LHH14a] Chun-Han Lin, Pi-Cheng Hsiu, and Cheng-Kang Hsieh. Dynamic backlight scaling optimization: A cloud-based energy-saving service for mobile streaming applications. *IEEE Transactions on Computers*, 63 (2):335–348, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2014:SWC**

[LHH14b] Ting-Yu Lin, Tai-Yi Huang, and Chia-Fu Hsu. Synergizing wireless communication technologies to improve Internet downloading experiences. *IEEE Transactions on Computers*, 63(11): 2851–2865, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2017:ACG**

[LHH17] Peng Liu, Qi Hu, and Xingcheng Hua. Adaptive coherence granularity for multi-socket systems. *IEEE Transactions on Computers*, 66(8):1302–1312, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7867795/`.

**Liang:2013:AEM**

[LHL13a] Jinghang Liang, Jie Han, and Fabrizio Lombardi. Analysis of error masking and restoring properties of sequential circuits. *IEEE Transactions on Computers*, 62(9):1694–1704, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liang:2013:NMR**

[LHL13b] Jinghang Liang, Jie Han, and Fabrizio Lombardi. New metrics for the reliability of approximate and probabilistic adders. *IEEE Transactions on Computers*, 62(9): 1760–1771, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:CAR**

[LHL+15a] Qingan Li, Yanxiang He, Jianhua Li, Liang Shi, Yiran Chen, and C. J. Xue. Compiler-assisted refresh minimization for volatile STT-RAM cache. *IEEE Transactions on Computers*, 64(8):2169–2181, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2015:AFE**

[LHL15b] Cong Liu, Jie Han, and F. Lombardi. An analytical framework for evaluating the error characteristics of approximate adders. *IEEE Transactions on Computers*, 64(5):1268–1281, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2015:IAR**

[LHPH15] Chia-Wei Lee, Chao-Wen Huang, Wen-Hao Pi, and Sun-Yuan Hsieh. An improved approximation ratio to the partial-terminal Steiner tree problem. *IEEE Transactions on Comput-*

*ers*, 64(1):274–279, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2015:SDF**

[LHTG15] Yanchao Lu, Bingsheng He, Xueyan Tang, and Minyi Guo. Synergy of dynamic frequency scaling and demotion on DRAM power management: Models and optimizations. *IEEE Transactions on Computers*, 64(8):2367–2381, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2013:WLS**

[LHY13] Ren Ping Liu, Mark Hedley, and Xun Yang. WLAN location service with TXOP. *IEEE Transactions on Computers*, 62(3):589–598, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2013:EMK**

[LHYZ13] Jing Liu, Qiong Huang, Bo Yang, and Yang Zhang. Efficient multicast key distribution using HOWP-based dynamic group access structures. *IEEE Transactions on Computers*, 62(8):1656–1672, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2012:TDC**

[Li12a] Jin-Fu Li. Testing and diagnosing comparison faults of TCAMs with asymmetric cells. *IEEE Transactions on Computers*, 61(11):1576–1587, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2012:SPC**

[Li12b] Keqin Li. Scheduling precedence constrained tasks with reduced processor energy on multiprocessor computers. *IEEE Transactions on Computers*, 61(12):1668–1681, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2011:BHI**

[Liu11] Ming T. Liu. A brief history of the IEEE transactions on computers. *IEEE Transactions on Computers*, 60(4):449, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Low:2013:MET**

[LJ13] Joshua Yung Lih Low and Ching Chuen Jong. A memory-efficient tables-and-additions method for accurate computation of elementary functions. *IEEE Transactions on Computers*, 62(5):858–872, May 2013.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Low:2015:UMB**

[LJ15] J. Y. L. Low and Ching Chuen Jong. Unified Mitchell-based approximation for efficient logarithmic conversion circuit. *IEEE Transactions on Computers*, 64 (6):1783–1797, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2018:TFE**

[LJ18] Nian-Ze Lee and Jie-Hong R. Jiang. Towards formal evaluation and verification of probabilistic design. *IEEE Transactions on Computers*, 67(8):1202–1216, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8302961/`.

**Li:2019:SSC**

[LJG+19] J. Li, S. Jiang, S. Gong, J. Wu, J. Yan, G. Yan, and X. Li. SqueezeFlow: A sparse CNN accelerator exploiting concise convolution rules. *IEEE Transactions on Computers*, 68(11): 1663–1677, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2013:GFS**

[LJL13] Chia-Yu Lu, Shang-Ming Jen, and Chi-Sung Laih. A general framework of side-channel atomicity for elliptic curve scalar multiplication. *IEEE Transactions on Computers*, 62 (3):428–438, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2018:GPE**

[LJVJ18] Jie Lu, Hongyang Jia, Naveen Verma, and Niraj K. Jha. Genetic programming for energy-efficient and energy-scalable approximate feature computation in embedded inference systems. *IEEE Transactions on Computers*, 67 (2):222–236, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8008802/`.

**Liu:2015:UFL**

[LJY+15] Wenping Liu, Hongbo Jiang, Yang Yang, Xiaofei Liao, Hongzhi Lin, and Zemeng Jin. A unified framework for line-like skeleton extraction in 2D/3D sensor networks. *IEEE Transactions on Computers*, 64(5):1323–1335, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lange:2010:AEM**

[LK10] H. Lange and A. Koch. Architectures and execution models for hardware/ software compilation and their system-level realization. *IEEE Transactions on Computers*, 59(10):1363–1377, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374369`.

**Lee:2014:IPC**

[LK14] Sungjin Lee and Jihong Kim. Improving performance and capacity of flash storage devices by exploiting heterogeneity of MLC flash memory. *IEEE Transactions on Computers*, 63(10): 2445–2458, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lakshminarayana:2015:BPP**

[LK15a] N. B. Lakshminarayana and H. Kim. Block-precise processors: Low-power processors with reduced operand store accesses and result broadcasts. *IEEE Transactions on Computers*, 64(11): 3102–3114, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2015:FDC**

[LK15b] Jongmin Lee and Soontae Kim. Filter data cache: An energy-efficient small L0 data cache architecture driven by miss cost reduction. *IEEE Transactions on Computers*, 64(7):1927–1939, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2016:ELA**

[LK16a] S. Lee and J. Kim. Effective lifetime-aware dynamic throttling for NAND flash-based SSDs. *IEEE Transactions on Computers*, 65 (4):1075–1089, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2016:RDR**

[LK16b] Yebin Lee and Soontae Kim. RAMS: DRAM rank-aware memory scheduling for energy saving. *IEEE Transactions on Computers*, 65(10): 3210–3216, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2018:SLA**

[LK18] Joo Hwan Lee and Hyesoon Kim. StaleLearn: Learning acceleration with asynchronous synchronization between model replicas on PIM. *IEEE Transactions on Computers*, 67

(6):861–873, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8168367/`.

**Lee:2016:EPF**

[LKBS16] E. Lee, H. Kang, H. Bahn, and K. G. Shin. Eliminating periodic flush overhead of file I/O with non-volatile buffer cache. *IEEE Transactions on Computers*, 65(4):1145–1157, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2016:RMB**

[LKH16] Junghoon Lee, Taehoon Kim, and Jaehyuk Huh. Reducing the memory bandwidth overheads of hardware security support for multi-core processors. *IEEE Transactions on Computers*, 65(11):3384–3397, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lai:2015:CHA**

[LKJ15] B.-C. C. Lai, Hsien-Kai Kuo, and Jing-Yang Jou. A cache hierarchy aware thread mapping methodology for GPGPUs. *IEEE Transactions on Computers*, 64(4):884–898, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2017:IEE**

[LKK+17] S. Lee, K. Kim, G. Koo, H. Jeon, M. Annavaram, and W. W. Ro. Improving energy efficiency of GPUs through data compression and compressed execution. *IEEE Transactions on Computers*, 66(5):834–847, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2019:IBR**

[LKK+19] H. Lee, M. Kim, H. Kim, H. Kim, and H. Lee. Integration and boost of a read-modify-write module in phase change memory system. *IEEE Transactions on Computers*, 68(12): 1772–1784, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2013:MPG**

[LKLK13] Duhee Lee, Jong-Chan Kim, Chang-Gun Lee, and Kanghee Kim. mRT-PLRU: A general framework for real-time multitask executions on NAND flash memory. *IEEE Transactions on Computers*, 62(4):758–771, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2015:ZCE**

[LKLM15] Youngjae Lee, Jin-Soo Kim, Sang-Won Lee, and Seungryoul Maeng. Zombie chasing: Efficient flash management considering dirty data in the buffer cache. *IEEE Transactions on Computers*, 64 (2):569–581, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2012:GFB**

[LKLT12] Chia-Hung Lin, Jian-Jhih Kuo, Bing-Hong Liu, and Ming-Jer Tsai. GPS-free, boundary-recognition-free, and reliable double-ruling-based information brokerage scheme in wireless sensor networks. *IEEE Transactions on Computers*, 61 (6):885–898, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lotfi-Kamran:2016:EHS**

[LKMSA16] Pejman Lotfi-Kamran, Mehdi Modarressi, and Hamid Sarbazi-Azad. An efficient hybrid-switched network-on-chip for chip multiprocessors. *IEEE Transactions on Computers*, 65(5):1656–1662, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2014:MAP**

[LKS+14] Junghoon Lee, Hanjoon Kim, Minjeong Shin, John Kim, and Jaehyuk Huh. Mutually aware prefetcher and on-chip network designs for multi-cores. *IEEE Transactions on Computers*, 63(9): 2316–2329, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2013:AAC**

[LKT13] Cheng-Kuan Lin, Tzu-Liang Kung, and Jimmy J. M. Tan. An algorithmic approach to conditional-fault local diagnosis of regular multiprocessor interconnected systems under the PMC model. *IEEE Transactions on Computers*, 62(3):439–451, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2012:ASP**

[LKYC12] Jae-Beom Lee, Myoung-Jin Kim, Sungroh Yoon, and Eui-Young Chung. Application-support particle filter for dynamic voltage scaling of multimedia applications. *IEEE Transactions on Computers*, 61(9): 1256–1269, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2011:FFR**

[LL11] Yawei Li and Zhiling Lan. FREM: a fast restart mechanism for general checkpoint/restart. *IEEE Transactions on Computers*, 60(5): 639–652, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:IBE**

[LLC+15] Jin Li, Jingwei Li, Xiaofeng Chen, Chunfu Jia, and Wenjing Lou. Identity-based encryption with outsourced revocation in cloud computing. *IEEE Transactions on Computers*, 64 (2):425–437, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lai:2016:UDH**

[LLC+16] Bo-Cheng Charles Lai, Chia-Ying Lee, Tsou-Han Chiu, Hsien-Kai Kuo, and Chun-Kai Chang. Unified designs for high performance LDPC decoding on GPGPU. *IEEE Transactions on Computers*, 65(12):3754–3765, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2013:APM**

[LLCC13] Cheng-Hung Lin, Chen-Hsiung Liu, Lung-Sheng Chien, and Shih-Chieh Chang. Accelerating pattern matching using a novel parallel algorithm on GPUs. *IEEE Transactions on Computers*, 62(10):1906–1916, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2013:IBM**

[LLCH13] Jonathan Lee, Shin-Jie Lee, Hsi-Min Chen, and Kuo-Hsun Hsu. Itinerary-based mobile agent as a basis for distributed OSGi services. *IEEE Transactions on Computers*, 62(10):1988–2000, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:RMM**

[LLD+16] Lei Liu, Yong Li, Chen Ding, Hao Yang, and Chengyong Wu. Rethinking memory management in modern operating system: Horizontal, vertical or random? *IEEE Transactions on Computers*, 65(6):1921–1935, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2019:EEA**

[LLD19] Peng Liu, Shunbin Li, and Qingyuan Ding. An energy-efficient accelerator based on hybrid CPU–FPGA devices for password recovery. *IEEE Transactions* on Computers, 68(2):170–181, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8453825/.

**Lu:2015:ASD**

[LLHC15] Shyue-Kung Lu, Tsu-Lin Li, M. Hashizume, and Jiann-Liang Chen. Address scrambling and data inversion techniques for yield enhancement of NROM-based ROMs. *IEEE Transactions on Computers*, 64(5):1230–1240, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2018:GEI**

[LLK18] Zhe Liu, Patrick Longa, and Çetin Kaya Koç. Guest Editors' introduction to the special issue on cryptographic engineering in a post-quantum world: State of the art advances. *IEEE Transactions on Computers*, 67(11):1532–1534, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8485531/.

**Liu:2019:SBC**

[LLKA19] Jian Liu, Wenting Li, Ghassan O. Karame, and N. Asokan. Scalable Byzantine consensus via hardware-assisted secret sharing. *IEEE Transactions on Computers*, 68(1):139–151, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8419336/.

**Lin:2011:HAB**

[LLL11] Po-Ching Lin, Ying-Dar Lin, and Yuan-Cheng Lai. A hybrid algorithm of backward hashing and automaton tracking for virus scanning. *IEEE Transactions on Computers*, 60(4):594–601, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:ECF**

[LLL15] Runhui Li, Jian Lin, and P. P. C. Lee. Enabling concurrent failure recovery for regenerating-coding-based storage systems: From theory to practice. *IEEE Transactions on Computers*, 64(7):1898–1911, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2016:ARD**

[LLL16] Y. Li, P. P. C. Lee, and J. C. S. Lui. Analysis of reliability dynamics of SSD RAID. *IEEE Transactions on Computers*, 65(4):1131–1144, April 2016. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Luo:2017:DNN**

[LLL+17] Tao Luo, Shaoli Liu, Ling Li, Yuqing Wang, Shijin Zhang, Tianshi Chen, Zhiwei Xu, Olivier Temam, and Yunji Chen. DaDianNao: A neural network supercomputer. *IEEE Transactions on Computers*, 66 (1):73–88, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2013:PAD**

[LLLJ13] Fangming Liu, Bo Li, Baochun Li, and Hai Jin. Peer-assisted on-demand streaming: Characterizing demands and optimizing supplies. *IEEE Transactions on Computers*, 62(2): 351–361, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lim:2014:ABF**

[LLLP14] Hyesook Lim, Kyuhee Lim, Nara Lee, and Kyong-Hye Park. On adding Bloom filters to longest prefix matching algorithms. *IEEE Transactions on Computers*, 63 (2):411–423, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2015:CPM**

[LLM+15] Xiulong Liu, Keqiu Li, Geyong Min, Yanming Shen, A. X. Liu, and Wenyu Qu. Completely pinpointing the missing RFID tags in a time-efficient way. *IEEE Transactions on Computers*, 64(1):87–96, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2013:QSA**

[LLOS13] Liang Lu, Weiqiang Liu, Maire O'Neill, and Earl E. Swartzlander. QCA systolic array design. *IEEE Transactions on Computers*, 62(3):548–560, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2014:LCS**

[LLQ+14] Peng Li, David J. Lilja, Weikang Qian, Marc D. Riedel, and Kia Bazargan. Logical computation on stochastic bit streams with linear finite-state machines. *IEEE Transactions on Computers*, 63(6):1474–1486, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:TFD**

[LLS+16] Joseph K. Liu, Kaitai Liang, Willy Susilo, Jianghua Liu, and Yang Xiang. Two-factor data security protection mechanism for cloud storage system. *IEEE Transactions on Computers*, 65 (6):1992–2004, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2011:IRT**

[LLW+11] Miao Liu, Duo Liu, Yi Wang, Meng Wang, and Zili Shao. On improving real-time interrupt latencies of hybrid operating systems with two-level hardware interrupts. *IEEE Transactions on Computers*, 60(7):978–991, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2017:BSR**

[LLW+17] Hsuan-Hung Liu, Bing-Yang Lin, Cheng-Wen Wu, Wan-Ting Chiang, Lee Mincent, Hung-Chih Lin, Ching-Nen Peng, and Min-Jer Wang. A built-off self-repair scheme for channel-based 3D memories. *IEEE Transactions on Computers*, 66 (8):1293–1301, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7850958/`.

**Liu:2018:SCM**

[LLW+18] Yidong Liu, Siting Liu, Yanzhi Wang, Fabrizio Lombardi, and Jie Han. A stochastic computational multi-layer perceptron with backward propagation. *IEEE Transactions on Computers*, 67(9):1273–1286, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8319953/`.

**Li:2017:TCA**

[LLX+17] Jianhua Li, Minming Li, Chun Jason Xue, Yiming Ouyang, and Fanfan Shen. Thread criticality assisted replication and migration for chip multiprocessor caches. *IEEE Transactions on Computers*, 66(10): 1747–1762, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7931700/`.

**Li:2016:SAD**

[LLXC16] Jingwei Li, Jin Li, Dongqing Xie, and Zhang Cai. Secure auditing and deduplicating data in cloud. *IEEE Transactions on Computers*, 65 (8):2386–2396, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:AMC**

[LLZ+17] Jun-Wei Li, Shi-Ning Li, Yu Zhang, Tao Gu, Yee Wei Law, Zhe Yang, Xingshe Zhou, and Marimuthu Palaniswami. An analytical model for coding-based reprogramming protocols in lossy wireless sensor networks. *IEEE Transactions on Computers*, 66(1):24–37, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lombardi:2013:RST**

[LMB13] Michele Lombardi, Michela Milano, and Luca Benini. Robust scheduling of task graphs under execution time uncertainty. *IEEE Transactions on Computers*, 62 (1):98–111, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2016:DMC**

[LMB+16] Yuqi Lin, Saif U. R. Malik, Kashif Bilal, Qiusong Yang, Yongji Wang, and Samee U. Khan. Designing and modeling of covert channels in operating systems. *IEEE Transactions on Computers*, 65(6):1706–1719, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2017:UMF**

[LMB17] S. Liu, G. Mingas, and C. S. Bouganis. An unbiased MCMC FPGA-based accelerator in the land of custom precision arithmetic. *IEEE Transactions on Computers*, 66(5):745–758, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Luque:2012:CAM**

[LMC+12] Carlos Luque, Miquel Moreto, Francisco J. Cazorla, Roberto Gioiosa, Alper Buyuktosunoglu, and Mateo Valero. CPU accounting for multicore processors. *IEEE Transactions on Computers*, 61 (2):251–264, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Le:2015:TFB**

[LMC+15] Yuan Le, Liran Ma, Wei Cheng, Xiuzhen Cheng, and Biao Chen. A time fairness-based MAC algorithm for throughput maximization in 802.11 networks. *IEEE Transactions on Computers*, 64(1):19–31, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2014:PDC**

[LMJ14] Yong Li, R. Melhem, and A. K. Jones. A practical data classification framework for scalable and high performance chip-multiprocessors. *IEEE Transactions on Computers*, 63(12):2905–2918, December 2014. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Lee:2011:PAM**

[LMNP11] Chang Joo Lee, Onur Mutlu, Veynu Narasiman, and Yale N. Patt. Prefetch-aware memory controllers. *IEEE Transactions on Computers*, 60(10):1406–1430, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=6008537.

**Luong:2013:GCP**

[LMT13] The Van Luong, Nouredine Melab, and El-Ghazali Talbi. GPU computing for parallel local search metaheuristic algorithms. *IEEE Transactions on Computers*, 62 (1):173–185, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:MFN**

[LMZQ17] Yin Li, Xingpo Ma, Yu Zhang, and Chuanda Qi. Mastrovito form of non-recursive Karatsuba multiplier for all trinomials. *IEEE Transactions on Computers*, 66(9): 1573–1584, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7870679/.

**Liu:2012:PED**

[LN12] Wei Liu and Alberto Nannarelli. Power efficient division and square root unit. *IEEE Transactions on Computers*, 61(8):1059–1070, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lao:2018:FPS**

[LNCX18] Bin Lao, Ge Nong, Wai Hong Chan, and Jing Yi Xie. Fast in-place suffix sorting on a multicore computer. *IEEE Transactions on Computers*, 67(12): 1737–1749, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8371211/.

**Liu:2019:OMM**

[LNL+19] W. Liu, J. Ni, Z. Liu, C. Liu, and M. O. Neill. Optimized modular multiplication for supersingular isogeny Diffie–Hellman. *IEEE Transactions on Computers*, 68 (8):1249–1255, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:LEC**

[LOC+16] C. W. Liu, S. H. Ou, K. C. Chang, T. C. Lin, and S. K. Chen. A low-error, cost-efficient design procedure for evaluating logarithms to be used in a logarithmic arithmetic processor. *IEEE Transactions on Computers*, 65(4):1158–1164, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:RSC**

[LOH17] Wenjie Li, Sharief M. A. Oteafy, and Hossam S. Hassanein. Rate-selective caching for adaptive streaming over information-centric networks. *IEEE Transactions on Computers*, 66(9): 1613–1628, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7887688/.

**Lombardi:2010:SJ**

[Lom10] Fabrizio Lombardi. State of the journal. *IEEE Transactions on Computers*, 59 (2):145–149, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5372165.

**Lombardi:2011:E**

[Lom11] Fabrizio Lombardi. Editorial. *IEEE Transactions on Computers*, 60(1):1, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Louri:2019:EIE**

[Lou19] Ahmed Louri. Editorial from the incoming Editor-in-Chief. *IEEE Transactions on Computers*, 68(1): 3, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8573058/.

**Li:2013:ONJ**

[LOX+13] Yu Li, Sai Tung On, Jianliang Xu, Byron Choi, and Haibo Hu. Optimizing nonindexed join processing in flash storage-based systems. *IEEE Transactions on Computers*, 62(7):1417–1431, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Le:2012:STB**

[LP12] Hoang Le and Viktor K. Prasanna. Scalable tree-based architectures for IPv4/v6 lookup using prefix partitioning. *IEEE Transactions on Computers*, 61 (7):1026–1039, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Le:2013:MEM**

[LP13a] Hoang Le and Viktor K. Prasanna. A memory-efficient and modular approach for large-scale string pattern matching. *IEEE Transactions on Computers*, 62(5):844–857, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liaskos:2013:GSR**

[LP13b] Christos K. Liaskos and Georgios I. Papadimitriou. Generalizing the square root rule for optimal periodic scheduling in push-based wireless environments. *IEEE Transactions on Computers*, 62(5):1044–1050, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Langhammer:2017:SPL**

[LP17] Martin Langhammer and Bogdan Pasca. Single precision logarithm and exponential architectures for hard floating-point enabled FPGAs. *IEEE Transactions on Computers*, 66(12): 2031–2043, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7927449/`.

**Liu:2014:DEC**

[LPCW14] Cong Liu, Yan Pan, Ai Chen, and Jie Wu. A DFA with extended character-set for fast deep packet inspection. *IEEE Transactions on Computers*, 63(8):1925–1937, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lukefahr:2016:EFG**

[LPD+16] A. Lukefahr, S. Padmanabha, R. Das, F. M. Sleiman, R. G. Dreslinski, T. F. Wenisch, and S. Mahlke. Exploring fine-grained heterogeneity with composite cores. *IEEE Transactions on Computers*, 65(2):535–547, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Luiz:2010:MSW**

[LPL10] S. O. D. Luiz, A. Perkusich, and A. M. N. Lima. Multisize sliding window in workload estimation for dynamic power management. *IEEE Transactions on Computers*, 59(12):1625–1639, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453349`.

**Liu:2012:NCU**

[LPL12] Jianguo Liu, Chao Pan, and Zhenbing Liu. Novel convolutions using first-order moments. *IEEE Transactions on Computers*, 61 (7):1050–1056, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lai:2013:SLR**

[LPL+13] Hanjiang Lai, Yan Pan, Cong Liu, Liang Lin, and Jie Wu. Sparse learning-to-rank via an efficient primal-dual algorithm. *IEEE Transactions on Computers*, 62 (6):1221–1233, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lima:2010:KBA**

[LPW10] J. B. Lima, D. Panario, and Qiang Wang. A Karatsuba-based algorithm for polynomial multiplication in Chebyshev form. *IEEE Transactions on Computers*, 59(6):835–841, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416688.

**Liu:2016:PPO**

[LQD+16] Ximeng Liu, Baodong Qin, Robert H. Deng, Rongxing Lu, and Jianfeng Ma. A privacy-preserving outsourced functional computation framework across large-scale multiple encrypted domains. *IEEE Transactions on Computers*, 65(12): 3567–3579, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2017:DAR**

[LQW+17] Weiqiang Liu, Liangyu Qian, Chenghua Wang, Honglan Jiang, Jie Han, and Fabrizio Lombardi. Design of approximate radix-4 Booth multipliers for error-tolerant computing. *IEEE Transactions on Computers*, 66 (8):1435–1441, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7862783/.

**Lentaris:2010:GPM**

[LR10] G. Lentaris and D. Reisis. A graphics parallel memory organization exploiting request correlations. *IEEE Transactions on Computers*, 59(6):762–775, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416691.

**Lai:2013:LLR**

[LR13] Shouwen Lai and Binoy Ravindran. Least-latency routing over time-dependent wireless sensor networks. *IEEE Transactions on Computers*, 62(5):969–983, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2016:PGA**

[LR16] S. Lee and W. W. Ro. Parallel GPU architecture simulation framework exploiting architectural-level parallelism with timing error prediction. *IEEE Transactions on Computers*, 65 (4):1253–1265, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2018:SST**

[LR18] Changmin Lee and Won Woo Ro. Simultaneous and speculative thread migration for improving energy efficiency of heterogeneous core architectures. *IEEE Transactions on Computers*, 67 (4):498–512, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8097407/.

**Lai:2010:HQB**

[LRC10] Shouwen Lai, B. Ravindran, and Hyeonjoong Cho. Heterogeneous quorum-based wake-up scheduling in wireless sensor networks. *IEEE Transactions on Computers*, 59(11):1562–1575, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396329.

**Lee:2016:EMF**

[LRP16] J. Lee, H. Roh, and S. Park. External Mergesort for flash-based solid state drives. *IEEE Transactions on Computers*, 65 (5):1518–1527, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2018:MFM**

[LRP+18] Jaewoo Lee, Saravanan Ramanathan, Kieu-My Phan, Arvind Easwaran, Insik Shin, and Insup Lee. MC-Fluid: Multi-core fluid-based mixed-criticality scheduling. *IEEE Transactions on Computers*, 67(4):469–483, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8059775/.

**Liu:2015:MDT**

[LRY+15] Chang Liu, R. Ranjan, Chi Yang, Xuyun Zhang, Lizhe Wang, and Jinjun Chen. MuR-DPA: Top-down levelled multi-replica Merkle hash tree based secure public auditing for dynamic big data storage on cloud. *IEEE Transactions on Computers*, 64(9):2609–2622, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2010:DHP**

[LS10a] Mingqiang Li and Jiwu Shu. DACO: a high-performance disk architecture designed specially for large-scale erasure-coded storage systems. *IEEE Transactions on Computers*, 59(10):1350–1362, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396331`. See comment [Tho12].

**Li:2010:PSD**

[LS10b] Mingqiang Li and Jiwu Shu. Preventing silent data corruptions from propagating during data reconstruction. *IEEE Transactions on Computers*, 59(12): 1611–1624, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416679`.

**Lu:2010:RPS**

[LS10c] Wencheng Lu and S. Sahni. Recursively partitioned static IP router tables. *IEEE Transactions on Computers*, 59(12):1683–1690, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5487500`.

**Li:2013:SES**

[LS13] Ze Li and Haiying Shen. SEDUM: Exploiting social networks in utility-based distributed routing for DTNs. *IEEE Transactions on Computers*, 62(1):83–97, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:FGC**

[LSA+17] Tuo Li, Muhammad Shafique, Jude Angelo Ambrose, Jörg Henkel, and Sri Parameswaran. Fine-grained checkpoint recovery for application-specific instruction-set processors. *IEEE Transactions on Computers*, 66(4):647–660, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2018:AAW**

[LSA18] Haeseung Lee, Muhammad Shafique, and Mohammad Abdullah Al Faruque. Aging-aware workload management on embedded GPU under process variation. *IEEE Transactions on Computers*, 67(7):920–933, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8247279/`.

**Lee:2010:PTA**

[LSC10] Jong Sung Lee, K. Skadron, and Sung Woo Chung. Predictive temperature-aware DVFS. *IEEE Transactions on Computers*, 59

(1):127–133, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255230.

**Lam:2011:AAT**

[LSC11] Siew Kei Lam, Thambipillai Srikanthan, and Christopher T. Clarke. Architecture-aware technique for mapping area-time efficient custom instructions onto FPGAs. *IEEE Transactions on Computers*, 60(5):680–692, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2015:HPL**

[LSG+15] Youyou Lu, Jiwu Shu, Jia Guo, Shuai Li, and O. Mutlu. High-performance and lightweight transaction support in flash-based SSDs. *IEEE Transactions on Computers*, 64(10):2819–2832, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2018:ACG**

[LSG+18] Qiao Li, Liang Shi, Congming Gao, Yejia Di, and Chun Jason Xue. Access characteristic guided read and write regulation on flash based storage systems. *IEEE Transactions on Computers*, 67(12): 1663–1676, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8362696/.

**Lu:2016:SSC**

[LSGZ16] Y. Lu, J. Shu, J. Guo, and P. Zhu. Supporting system consistency with differential transactions in flash-based SSDs. *IEEE Transactions on Computers*, 65 (2):627–639, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:EGD**

[LSHC15] Dan Li, Yunfei Shang, Wu He, and Congjie Chen. EXR: Greening data center network with software defined exclusive routing. *IEEE Transactions on Computers*, 64(9):2534–2544, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2013:BBA**

[LSK13] Sungjin Lee, Dongkun Shin, and Jihong Kim. BAGC: Buffer-aware garbage collection for flash-based storage systems. *IEEE Transactions on Computers*, 62(11): 2141–2154, November 2013. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Liu:2015:AEC**

[LSL15] F. Liu, P. Shu, and J. C. Lui. AppATP: An energy conserving adaptive mobile-cloud transmission protocol. *IEEE Transactions on Computers*, 64(11): 3051–3063, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2013:LSN**

[LSS13] Ze Li, Haiying Shen, and Karan Sapra. Leveraging social networks to combat collusion in reputation systems for peer-to-peer networks. *IEEE Transactions on Computers*, 62(9): 1745–1759, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2015:CSA**

[LSSE15] Jinkyu Lee, K. G. Shin, I. Shin, and A. Easwaran. Composition of schedulability analyses for real-time multiprocessor systems. *IEEE Transactions on Computers*, 64(4):941–954, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2015:ETP**

[LSW15] Guoxin Liu, Haiying Shen, and L. Ward. An efficient and trustworthy P2P and social network integrated file sharing system. *IEEE Transactions on Computers*, 64(1):54–70, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liang:2013:AAC**

[LSX13] Weifa Liang, Pascal Schweitzer, and Zichuan Xu. Approximation algorithms for capacitated minimum forest problems in wireless sensor networks with a mobile sink. *IEEE Transactions on Computers*, 62(10): 1932–1944, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Luo:2014:EES**

[LSXP14] Jianqiang Luo, Mochan Shrestha, Lihao Xu, and James S. Plank. Efficient encoding schedules for XOR-based erasure codes. *IEEE Transactions on Computers*, 63(9):2259–2272, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2015:POB**

[LSZ+15] Liang Liu, Yuning Song, Haiyang Zhang, Huadong Ma, and A. V. Vasilakos. Physarum optimization: A biology-inspired algorithm for the Steiner tree problem in networks. *IEEE Transactions on Computers*, 64(3):819–832, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Le:2014:IMX**

[LT14] Duc-Phong Le and Chik How Tan. Improved Miller's algorithm for computing pairings on Edwards curves. *IEEE Transactions on Computers*, 63(10):2626–2632, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liaskos:2015:PRU**

[LT15] C. Liaskos and A. Tsioliaridou. A promise of realizable, ultra-scalable communications at nano-scale: A multi-modal nano-machine architecture. *IEEE Transactions on Computers*, 64 (5):1282–1295, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Leu:2014:NPR**

[LTL14] Jenq-Shiou Leu, Nguyen Hai Tung, and Chun-Yao Liu. Non-parametric RSS prediction based energy saving scheme for moving smartphones. *IEEE Transactions on Computers*, 63(7):1793–1801, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2012:OHD**

[LTLC12] Qiang Liu, Tim Todman, Wayne Luk, and George A. Constantinides. Optimizing hardware design by composing utility-directed transformations. *IEEE Transactions on Computers*, 61(12): 1800–1812, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liaskos:2014:DCB**

[LTP+14] Christos K. Liaskos, Angeliki N. Tsioliaridou, Georgios I. Papadimitriou, Petros Nicopolitidis, and Andreas S. Pomportsis. On data compatibility and broadcast stream formation. *IEEE Transactions on Computers*, 63(9):2369–2375, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:SPC**

[LTVL15] Kenli Li, Xiaoyong Tang, B. Veeravalli, and Keqin Li. Scheduling precedence constrained stochastic tasks on heterogeneous cluster systems. *IEEE Transactions on Computers*, 64(1):191–204, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2012:PPG**

[LTW+12] Shaoshan Liu, Jie Tang, Ligang Wang, Xiao-Feng Li, and Jean-Luc Gaudiot. Packer: Parallel garbage collection based on virtual spaces. *IEEE Transactions on Computers*, 61(11): 1611–1623, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lora:2019:TAI**

[LVF19] M. Lora, S. Vinco, and F. Fummi. Translation, abstraction and integration for effective smart system design. *IEEE Transactions on Computers*, 68(10): 1525–1538, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2012:MRP**

[LvH12] Xin Li and Reinhard von Hanxleden. Multithreaded reactive programming — the Kiel Esterel processor. *IEEE Transactions on Computers*, 61(3):337–349, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2016:CSP**

[LVJ16] Jie Lu, Naveen Verma, and Niraj K. Jha. Compressed signal processing on Nyquist-sampled signals. *IEEE Transactions on Computers*, 65(11):3293–3303, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lerman:2018:SST**

[LVMS18] Liran Lerman, Nikita Veshchikov, Olivier Markowitch, and François-Xavier Standaert. Start simple and then refine: Bias-variance decomposition as a diagnosis tool for leakage profiling. *IEEE Transactions on Computers*, 67 (2):268–283, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7990260/`.

**Liu:2013:FDP**

[LW13] Cong Liu and Jie Wu. Fast deep packet inspection with a dual finite automata. *IEEE Transactions on Computers*, 62(2):310–321, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2015:DCN**

[LW15] D. Li and J. Wu. On data center network architectures for interconnecting dual-port servers. *IEEE Transactions on Computers*, 64(11): 3210–3222, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2017:FTF**

[LW17] Kibum Lee and S. Simon Wong. Fault-tolerant FPGA with column-based redundancy and power gating using RRAM. *IEEE Transactions on Computers*, 66(6):946–956, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7763756/`.

**Lu:2013:ODR**

[LWF13] Zaixin Lu, Weili Wu, and Bin Fu. Optimal data retrieval scheduling in the multichannel wireless broadcast environments. *IEEE Transactions on Computers*, 62(12):2427–2439, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:TSE**

[LWF+17] Zheng Li, Fang Wang, Dan Feng, Yu Hua, Jingning Liu, Wei Tong, Yu Chen, and Salah S. Harb. Time and space-efficient write parallelism in PCM by exploiting data patterns. *IEEE Transactions on Computers*, 66(9): 1629–1644, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7870591/`.

**Lu:2016:RAD**

[LWH+16] Y. Lu, D. Wu, B. He, X. Tang, J. Xu, and M. Guo. Rank-aware dynamic migrations and adaptive demotions for DRAM power management. *IEEE Transactions on Computers*, 65 (1):187–202, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2011:EXR**

[LWK11] Chin-Yung Lu, Shiou-An Wang, and Sy-Yen Kuo. An extended XQDD representation for multiple-valued quantum logic. *IEEE Transactions on Computers*, 60(10):1377–1389, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5928328`.

**Lee:2015:GCE**

[LWKA15] J. Lee, D. H. Woo, H. Kim, and M. Azimi. GREEN cache: Exploiting the disciplined memory model of OpenCL on GPUs. *IEEE Transactions on Computers*, 64(11):3167–3180, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2016:LPS**

[LWL+16] B. Lin, C. Wu, M. Lee,

H. Lin, C. Peng, and M. Wang. A local parallel search approach for memory failure pattern identification. *IEEE Transactions on Computers*, 65 (3):770–780, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2011:CDC**

[LWW11] Xiang-Yang Li, Yajun Wang, and Yu Wang. Complexity of data collection, aggregation, and selection for wireless sensor networks. *IEEE Transactions on Computers*, 60(3):386–399, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2015:CHS**

[LWY15] Ting-Yu Lin, Kun-Ru Wu, and Guang-Chuen Yin. Channel-hopping scheme and channel-diverse routing in static multi-radio multi-hop wireless networks. *IEEE Transactions on Computers*, 64(1):71–86, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liang:2018:FMP**

[LWZ18] Yun Liang, Shuo Wang, and Wei Zhang. FlexCL: A model of performance and power for OpenCL workloads on FPGAs. *IEEE Transactions on Computers*, 67(12): 1750–1764, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8365849/`.

**Levitin:2017:DCP**

[LXDV17] Gregory Levitin, Liudong Xing, Yuanshun Dai, and Vinod M. Vokkarane. Dynamic checkpointing policy in heterogeneous real-time standby systems. *IEEE Transactions on Computers*, 66(8):1449–1456, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7851034/`.

**Levitin:2015:MRC**

[LXJD15] G. Levitin, Liudong Xing, B. W. Johnson, and Yuanshun Dai. Mission reliability, cost and time for cold standby computing systems with periodic backup. *IEEE Transactions on Computers*, 64(4):1043–1057, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2012:FDM**

[LXK12] Xiaomei Liu, Li Xiao, and Andrew Kreling. A fully distributed method to detect and reduce cut vertices in large-scale overlay networks. *IEEE Transactions on Computers*, 61(7):969–

985, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2013:WED**

[LXL+13] Jianjun Li, Ming Xiong, Victor C. S. Lee, LihChyun Shu, and Guohui Li. Workload-efficient deadline and period assignment for maintaining temporal consistency under EDF. *IEEE Transactions on Computers*, 62 (6):1255–1268, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2014:RMD**

[LXL+14] Dan Li, Mingwei Xu, Ying Liu, Xia Xie, Yong Cui, Jingyi Wang, and Guihai Chen. Reliable multicast in data center networks. *IEEE Transactions on Computers*, 63(8):2011–2024, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2019:RND**

[LXW+19] Shuo Li, Nong Xiao, Peng Wang, Guangyu Sun, Xiaoyang Wang, Yiran Chen, Hai Helen Li, Jason Cong, and Tao Zhang. RC-NVM: Dual-addressing non-volatile memory architecture supporting both row and column memory accesses. *IEEE Transactions on Computers*, 68(2): 239–254, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8453833/`.

**Liu:2015:STE**

[LXZ+15] X. Liu, B. Xiao, S. Zhang, K. Bu, and A. Chan. STEP: A time-efficient tag searching protocol in large RFID systems. *IEEE Transactions on Computers*, 64(11): 3265–3277, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2016:DRN**

[LXZH16] Limei Lin, Li Xu, Shuming Zhou, and Sun-Yuan Hsieh. The $t/k$-diagnosability for regular networks. *IEEE Transactions on Computers*, 65(10):3157–3170, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lee:2011:EEQ**

[LY11] Sookyoung Lee and Mohamed F. Younis. EQAR: Effective QoS-aware relay node placement algorithm for connecting disjoint wireless sensor subnetworks. *IEEE Transactions on Computers*, 60(12):1772–1787, December 2011. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Lu:2017:MPF**

[LY17] Zhonghai Lu and Yuan Yao. Marginal performance: Formalizing and quantifying power over/under provisioning in NoC DVFS. *IEEE Transactions on Computers*, 66(11):1903–1917, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7947135/.

**Lu:2018:TVD**

[LY18] Zhonghai Lu and Yuan Yao. Thread voting DVFS for manycore NoCs. *IEEE Transactions on Computers*, 67(10):1506–1524, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8338086/.

**Lee:2015:DIJ**

[LYB15] Eunji Lee, Seung Hoon Yoo, and Hyokyung Bahn. Design and implementation of a journaling file system for phase-change memory. *IEEE Transactions on Computers*, 64(5):1349–1360, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2010:PAI**

[LYCT10] Chia-Hung Lin, Shiao-An Yuan, Shih-Wei Chiu, and Ming-Jer Tsai. ProgressFace: An algorithm to improve routing efficiency of GPSR-like routing protocols in wireless ad hoc networks. *IEEE Transactions on Computers*, 59(6):822–834, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416690.

**Lai:2011:TCI**

[LYH11] Chun-Hung Lai, Fu-Ching Yang, and Ing-Jer Huang. A trace-capable instruction cache for cost-efficient real-time program trace compression in SoC. *IEEE Transactions on Computers*, 60(12):1665–1677, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5601688.

**Liu:2018:TAT**

[LYJ+18] Weichen Liu, Lei Yang, Weiwen Jiang, Liang Feng, Nan Guan, Wei Zhang, and Nikil Dutt. Thermal-aware task mapping on dynamically reconfigurable network-on-chip based multiprocessor system-on-chip. *IEEE Transactions on Comput-*

*ers*, 67(12):1818–1834, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8373717/.

**Le:2019:ADF**

[LYK19] D. Le, S. L. Yeo, and K. Khoo. Algebraic differential fault analysis on SIMON block cipher. *IEEE Transactions on Computers*, 68(11): 1561–1572, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2019:PHB**

[LYL+19] W. Lu, G. Yan, J. Li, S. Gong, S. Jiang, J. Wu, and X. Li. Promoting the harmony between sparsity and regularity: A relaxed synchronous architecture for convolutional neural networks. *IEEE Transactions on Computers*, 68(6):867–881, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lasla:2015:EAB**

[LYOB15] N. Lasla, M. F. Younis, A. Ouadjaout, and N. Badache. An effective area-based localization algorithm for wireless networks. *IEEE Transactions on Computers*, 64(8):2103–2118, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lim:2010:PTI**

[LYS10] Hyesook Lim, Changhoon Yim, and E. E. Swartzlander. Priority tries for IP address lookup. *IEEE Transactions on Computers*, 59(6):784–794, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416681.

**Lin:2014:SMM**

[LYS14] Wen-Ching Lin, Jheng-Hao Ye, and Ming-Der Shieh. Scalable Montgomery modular multiplication architecture with low-latency and low-memory bandwidth requirement. *IEEE Transactions on Computers*, 63 (2):475–483, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2016:OPT**

[LYT+16] Fan Li, Zhiyuan Yin, Shaojie Tang, Yu Cheng, and Yu Wang. Optimization problems in throwbox-assisted delay tolerant networks: Which throwboxes to activate? How many active ones I need? *IEEE Transactions on Computers*, 65(5):1663–1670, May 2016.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2016:ECD**

[LYY16] W. Li, Y. Yang, and D. Yuan. Ensuring cloud data reliability with minimum replication by proactive replica checking. *IEEE Transactions on Computers*, 65(5):1494–1506, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2014:EMA**

[LZ14] Geng Lin and Wenxing Zhu. An efficient memetic algorithm for the max-bisection problem. *IEEE Transactions on Computers*, 63 (6):1365–1376, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lama:2015:CPP**

[LZ15] P. Lama and Xiaobo Zhou. Coordinated power and performance guarantee with fuzzy MIMO control in virtualized server clusters. *IEEE Transactions on Computers*, 64(1):97–111, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:DRA**

[LZA+16] Zhihong Liu, Qi Zhang, Reaz Ahmed, Raouf Boutaba, Yaping Liu, and Zhenghu Gong. Dynamic resource allocation for MapReduce with partitioning skew. *IEEE Transactions on Computers*, 65(11):3304–3317, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2013:DFT**

[LZS+13] Yong Lu, Youjian Zhao, Fuchun Sun, Hongbo Li, and Dianjun Wang. Dynamic fault-tolerant routing based on FSA for LEO satellite networks. *IEEE Transactions on Computers*, 62(10): 1945–1958, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lu:2015:PTR**

[LZW+15] Gang Lu, Jianfeng Zhan, Haining Wang, Lin Yuan, Yunwei Gao, Chuliang Weng, and Yong Qi. PowerTracer: Tracing requests in multi-tier services to reduce energy inefficiency. *IEEE Transactions on Computers*, 64 (5):1389–1401, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2013:SPD**

[LZYL13] Xinxin Liu, Han Zhao, Xin Yang, and Xiaolin Li. SinkTrail: a proactive data reporting protocol for wireless sensor networks. *IEEE*

*Transactions on Computers*, 62(1):151–162, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2016:TSF**

[LZZ16] Jinzhao Liu, Yuezhi Zhou, and Di Zhang. TranSim: A simulation framework for cache-enabled transparent computing systems. *IEEE Transactions on Computers*, 65(10):3171–3183, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Li:2017:DRM**

[LZZ17a] Xingquan Li, Ziran Zhu, and Wenxing Zhu. Discrete relaxation method for triple patterning lithography layout decomposition. *IEEE Transactions on Computers*, 66 (2):285–298, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Liu:2017:NVM**

[LZZ+17b] Duo Liu, Kan Zhong, Xiao Zhu, Yang Li, Lingbo Long, and Zili Shao. Non-volatile memory based page swapping for building high-performance mobile devices. *IEEE Transactions on Computers*, 66(11): 1918–1931, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7938390/`.

**Liu:2016:SOC**

[LZZV16] T. Liu, Y. Zhu, Q. Zhang, and A. Vasilakos. Stochastic optimal control for participatory sensing systems with heterogeneous requests. *IEEE Transactions on Computers*, 65(5):1619–1631, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Lin:2013:TMM**

[LZZZ13] Jiang Lin, Hongzhong Zheng, Zhichun Zhu, and Zhao Zhang. Thermal modeling and management of DRAM systems. *IEEE Transactions on Computers*, 62(10): 2069–2082, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Manoochehri:2014:ELC**

[MAD14] M. Manoochehri, M. Annavaram, and M. Dubois. Extremely low cost error protection with correctable parity protected cache. *IEEE Transactions on Computers*, 63(10):2431–2444, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Marchal:2017:HEU**

[MAG+17] Samuel Marchal, Giovanni Armano, Tommi Gröndahl, Kalle Saari, Nidhi Singh, and N. Asokan. Off-the-hook: An efficient and usable client-side phishing prevention application. *IEEE Transactions on Computers*, 66(10): 1717–1733, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7926371/.

**Minaeva:2018:TTC**

[MAHD18] Anna Minaeva, Benny Akesson, Zdeněk Hanzálek, and Dakshina Dasari. Time-triggered co-scheduling of computation and communication with jitter requirements. *IEEE Transactions on Computers*, 67 (1):115–129, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7967685/.

**Mann:2016:MAV**

[Man16] Zoltán Ádám Mann. Multicore-aware virtual machine placement in cloud data centers. *IEEE Transactions on Computers*, 65(11):3357–3369, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Marongiu:2012:OCE**

[MB12a] Andrea Marongiu and Luca Benini. An OpenMP compiler for efficient use of distributed scratchpad memory in MPSoCs. *IEEE Transactions on Computers*, 61 (2):222–236, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Martirosyan:2012:PTR**

[MB12b] Anahit Martirosyan and Azzedine Boukerche. Preserving temporal relationships of events for wireless sensor actor networks. *IEEE Transactions on Computers*, 61(8):1203–1216, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mingas:2016:PBM**

[MB16] G. Mingas and C. S. Bouganis. Population-based MCMC on multi-core CPUs, GPUs and FPGAs. *IEEE Transactions on Computers*, 65(4):1283–1296, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Melani:2017:SAC**

[MBB+17] Alessandra Melani, Marko Bertogna, Vincenzo Bonifaci, Alberto Marchetti-Spaccamela, and Giorgio Buttazzo. Schedulability analysis of conditional parallel task graphs in multicore systems. *IEEE Transactions on Computers*, 66 (2):339–353, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Morari:2013:SMI**

[MBC+13] A. Morari, C. Boneti, F. J. Cazorla, R. Gioiosa, Chen-Yong Cher, A. Buyuktosunoglu, P. Bose, and M. Valero. SMT malleability in IBM POWER5 and POWER6 processors. *IEEE Transactions on Computers*, 62(4):813–826, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Michelogiannakis:2011:EEB**

[MBD11] George Michelogiannakis, Daniel U. Becker, and William J. Dally. Evaluating elastic buffer and wormhole flow control. *IEEE Transactions on Computers*, 60(6):896–903, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Melani:2017:ERT**

[MBD+17] Alessandra Melani, Marko Bertogna, Robert I. Davis, Vincenzo Bonifaci, Alberto Marchetti-Spaccamela, and Giorgio Buttazzo. Exact response time analysis for fixed priority memory-processor co-scheduling. *IEEE Transactions on Computers*, 66 (4):631–646, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Migliore:2018:PPF**

[MBF18] Vincent Migliore, Guillaume Bonnoron, and Caroline Fontaine. Practical parameters for somewhat homomorphic encryption schemes on binary circuits. *IEEE Transactions on Computers*, 67(11):1550–1560, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8302942/`.

**Moreira:2010:BSR**

[MBGS10] O. Moreira, T. Basten, M. Geilen, and S. Stuijk. Buffer sizing for rate-optimal single-rate data-flow scheduling revisited. *IEEE Transactions on Computers*, 59(2):188–201, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5282495`.

**Malrait:2011:ECS**

[MBM11] Luc Malrait, Sara Bouchenak, and Nicolas Marchand. Experience with CONSER: a system for server control through fluid modeling. *IEEE Transactions on Computers*, 60(7):951–963, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Moyer:2012:SWC**

[MBS+12] Thomas Moyer, Kevin Butler, Joshua Schiffman, Patrick McDaniel, and Trent Jaeger. Scalable Web content attestation. *IEEE Transactions on Computers*, 61(5):686–699, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mireshghallah:2019:EEP**

[MBSSA19] F. Mireshghallah, M. Bakhshalipour, M. Sadrosadati, and H. Sarbazi-Azad. Energy-efficient permanent fault tolerance in hard real-time systems. *IEEE Transactions on Computers*, 68(10):1539–1545, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mukhopadhyay:2011:PEA**

[MC11] Debdeep Mukhopadhyay and Dipanwita Roy Chowdhury. A parallel efficient architecture for large cryptographically robust $n \times k (k \geq n/2)$ mappings. *IEEE Transactions on Computers*, 60 (3):375–385, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Malandrino:2012:CDC**

[MCC12] Francesco Malandrino, Claudio Casetti, and Carla-Fabiana Chiasserini. Content discovery and caching in mobile networks with infrastructure. *IEEE Transactions on Computers*, 61(10): 1507–1520, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maddah:2016:SST**

[MCM16] Rakan Maddah, Sangyeun Cho, and Rami Melhem. Symbol shifting: Tolerating more faults in PCM blocks. *IEEE Transactions on Computers*, 65(7):2270–2283, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Munoz-Coreas:2019:QCD**

[MCT19] E. Muñoz-Coreas and H. Thapliyal. Quantum circuit design of a $T$-count optimized integer multiplier. *IEEE Transactions on Computers*, 68(5):729–739, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mo:2018:PAL**

[MCXZ18] Yuchang Mo, Lirong Cui, Liudong Xing, and Zhao

Zhang. Performability analysis of large-scale multi-state computing systems. *IEEE Transactions on Computers*, 67(1):59–72, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7981361/.

**Michelogiannakis:2013:EBF**

[MD13] George Michelogiannakis and William J. Dally. Elastic buffer flow control for on-chip networks. *IEEE Transactions on Computers*, 62 (2):295–309, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Manoochehri:2016:AMA**

[MD16] Mehrtash Manoochehri and Michel Dubois. Accurate model for application failure due to transient faults in caches. *IEEE Transactions on Computers*, 65 (8):2397–2410, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Martins:2017:AIR**

[MEBS17] Paulo Martins, Julien Eynard, Jean-Claude Bajard, and Leonel Sousa. Arithmetical improvement of the round-off for cryptosystems in high-dimensional lattices. *IEEE Transactions on Computers*, 66(12): 2005–2018, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7891511/.

**Muck:2014:TUD**

[MF14] T. R. Muck and A. A. Frohlich. Toward unified design of hardware and software components using C++. *IEEE Transactions on Computers*, 63(11): 2880–2893, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mohammadi:2014:TMA**

[MFG14] Anwar Mohammadi, Nathan Fisher, and Daniel Grosu. Truthful mechanisms for allocating a single processor to sporadic tasks in competitive real-time environments. *IEEE Transactions on Computers*, 63 (8):2066–2079, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mashayekhy:2016:TMC**

[MFG16] Lena Mashayekhy, Nathan Fisher, and Daniel Grosu. Truthful mechanisms for competitive reward-based scheduling. *IEEE Transactions on Computers*, 65 (7):2299–2312, July 2016. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Mao:2017:RCP**

[MFT+17] Bomin Mao, Zubair Md. Fadlullah, Fengxiao Tang, Nei Kato, Osamu Akashi, Takeru Inoue, and Kimihiro Mizutani. Routing or computing? The paradigm shift towards intelligent computer network packet transmission based on deep learning. *IEEE Transactions on Computers*, 66(11):1946–1960, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7935536/.

**Metra:2011:GEI**

[MG11a] Cecilia Metra and Rajesh Galivanche. Guest Editors' introduction: Special section on concurrent on-line testing and error/fault resilience of digital systems. *IEEE Transactions on Computers*, 60(9):1217–1218, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5962402.

**Moller:2011:IDI**

[MG11b] Niels Möller and Torbjörn Granlund. Improved division by invariant integers. *IEEE Transactions on Computers*, 60(2):165–175, February 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mandal:2016:FRI**

[MG16] Kalikinkar Mandal and Guang Gong. Feedback reconstruction and implementations of pseudorandom number generators from composited De Bruijn sequences. *IEEE Transactions on Computers*, 65(9):2725–2738, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maene:2018:HBT**

[MGdC+18] Pieter Maene, Johannes Götzfried, Ruan de Clercq, Tilo Müller, Felix Freiling, and Ingrid Verbauwhede. Hardware-based trusted computing architectures for isolation and attestation. *IEEE Transactions on Computers*, 67(3):361–374, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7807249/.

**Manole:2014:PSP**

[MGW14] Sagi Manole, Amit Golander, and Shlomo Weiss. Protein sequence pattern matching: Leveraging application specific hardware accelerators. *IEEE Trans-*

actions on Computers, 63 (2):448–460, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Meloni:2015:EDB**

[MH15] N. Meloni and M. A. Hasan. Efficient double bases for scalar multiplication. *IEEE Transactions on Computers*, 64(8):2204–2212, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Musavi:2019:OBM**

[MH19] S. A. Musavi and M. R. Hashemi. An ontology-based method for HW/SW architecture reconstruction. *IEEE Transactions on Computers*, 68(7):1007–1018, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mazahir:2017:PEM**

[MH+17] Sana Mazahir, Osman Hasan, Rehan Hafiz, Muhammad Shafique, and Jörg Henkel. Probabilistic error modeling for approximate adders. *IEEE Transactions on Computers*, 66(3):515–530, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mazahir:2017:PEA**

[MHHS17] Sana Mazahir, Osman Hasan, Rehan Hafiz, and Muhammad Shafique. Probabilistic error analysis of approximate recursive multipliers. *IEEE Transactions on Computers*, 66(11):1982–1990, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7935435/.

**Mahmood:2015:ECR**

[MHK15] T. Mahmood, Seokin Hong, and Soontae Kim. Ensuring cache reliability and energy scaling at near-threshold voltage with Macho. *IEEE Transactions on Computers*, 64(6):1694–1706, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Momeni:2015:DAA**

[MHML15] A. Momeni, Jie Han, P. Montuschi, and F. Lombardi. Design and analysis of approximate compressors for multiplication. *IEEE Transactions on Computers*, 64 (4):984–994, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Matadamas-Hernandez:2014:PSP**

[MHRARG+14] J. Matadamas-Hernandez, G. Roman-Alonso, F. Rojas-Gonzalez, M. A. Castro-Garcia, Azzedine Boukerche, M. Aguilar-Cornejo, and S. Cordero-Sanchez. Parallel simulation of pore networks using multicore CPUs. *IEEE Transactions on Computers*, 63(6):1513–1525, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mokhov:2014:SPI**

[MIS+14] Andrey Mokhov, Alexei Iliasov, Danil Sokolov, Maxim Rykunov, Alex Yakovlev, and Alexander Romanovsky. Synthesis of processor instruction sets from high-level ISA specifications. *IEEE Transactions on Computers*, 63(6):1552–1566, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ma:2014:HRA**

[MJW+14] Sheng Ma, Natalie Enright Jerger, Zhiying Wang, Mingche Lai, and Libo Huang. Holistic routing algorithm design to support workload consolidation in NoCs. *IEEE Transactions on Computers*, 63 (3):529–542, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mao:2016:LDD**

[MJWT16] Bo Mao, Hong Jiang, Suzhen Wu, and Lei Tian. Leveraging data deduplication to improve the performance of primary storage systems in the cloud. *IEEE Transactions on Computers*, 65 (6):1775–1788, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Matsutani:2011:PRL**

[MKAY11] Hiroki Matsutani, Michihiro Koibuchi, Hideharu Amano, and Tsutomu Yoshinaga. Prediction router: a low-latency on-chip router architecture with multiple predictors. *IEEE Transactions on Computers*, 60(6):783–799, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maniatakos:2013:LCC**

[MKFM13] Michail Maniatakos, Prabhakar Kudva, Bruce M. Fleischer, and Yiorgos Makris. Low-cost concurrent error detection for floating-point unit (FPU) controllers. *IEEE Transactions on Computers*, 62(7):1376–1388, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Morris:2014:TDS**

[MKLW14] Randy W. Morris, Avinash Karanth Kodi, Ahmed Louri, and Ralph D. Whaley. Three-dimensional stacked nanophotonic network-on-chip architecture with minimal reconfiguration. *IEEE Transactions on Computers*, 63 (1):243–255, January 2014.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mohaqeqi:2014:ALA**

[MKM14] Morteza Mohaqeqi, Mehdi Kargahi, and Ali Movaghar. Analytical leakage-aware thermal modeling of a real-time system. *IEEE Transactions on Computers*, 63 (6):1378–1392, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mozaffari-Kermani:2010:CSI**

[MKRM10] M. Mozaffari-Kermani and A. Reyhani-Masoleh. Concurrent structure-independent fault detection schemes for the Advanced Encryption Standard. *IEEE Transactions on Computers*, 59(5): 608–622, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5406504.

**Mozaffari-Kermani:2011:LPH**

[MKRM11] Mehran Mozaffari-Kermani and Arash Reyhani-Masoleh. A low-power high-performance concurrent fault detection approach for the composite field S-box and inverse S-box. *IEEE Transactions on Computers*, 60(9): 1327–1340, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5962403.

**Mozaffari-Kermani:2012:EHP**

[MKRM12] Mehran Mozaffari-Kermani and Arash Reyhani-Masoleh. Efficient and high-performance parallel hardware architectures for the AES-GCM. *IEEE Transactions on Computers*, 61(8):1165–1178, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maniatakos:2011:ILI**

[MKT+11] Michail Maniatakos, Naghmeh Karimi, Chandra Tirumurti, Abhijit Jas, and Yiorgos Makris. Instruction-level impact analysis of low-level faults in a modern microprocessor controller. *IEEE Transactions on Computers*, 60(9):1260–1273, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432157.

**Meng:2013:EMS**

[ML13] Shicong Meng and Ling Liu. Enhanced monitoring-as-a-service for effective cloud management. *IEEE Transactions on Computers*, 62(9): 1705–1720, September 2013. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Mun:2016:NAE**

[ML16] J. H. Mun and H. Lim. New approach for efficient IP address lookup using a Bloom filter in trie-based algorithms. *IEEE Transactions on Computers*, 65 (5):1558–1565, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Manumachu:2018:BOO**

[ML18] Ravindranath Reddy Manumachu and Alexey Lastovetsky. Bi-objective optimization of data-parallel applications on homogeneous multicore clusters for performance and energy. *IEEE Transactions on Computers*, 67 (2):160–177, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8013836/`.

**Mancillas-Lopez:2010:RHI**

[MLCH10] C. Mancillas-Lopez, D. Chakraborty, and F. Rodriguez Henriquez. Reconfigurable hardware implementations of tweakable enciphering schemes. *IEEE Transactions on Computers*, 59(11):1547–1561, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432161`.

**Min:2014:DIL**

[MLE14] Changwoo Min, Sang-Won Lee, and Young Ik Eom. Design and implementation of a log-structured file system for flash-based solid state drives. *IEEE Transactions on Computers*, 63(9): 2215–2227, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Moreno:2012:RMM**

[MLH12] Julio Villalba Moreno, Tomas Lang, and Javier Hormigo. Radix-2 multioperand and multiformat streaming online addition. *IEEE Transactions on Computers*, 61 (6):790–803, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mei:2015:PMS**

[MLOL15] J. Mei, K. Li, A. Ouyang, and K. Li. A profit maximization scheme with guaranteed quality of service in cloud computing. *IEEE Transactions on Computers*, 64(11):3064–3078, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maragos:2019:FMP**

[MLS19] K. Maragos, G. Lentaris, and D. Soudris. In-the-field mitigation of process variability for improved FPGA performance. *IEEE Transactions on Computers*, 68 (7):1049–1063, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ma:2012:HPB**

[MLW12] Kun Ma, Han Liang, and Kaijie Wu. Homomorphic property-based concurrent error detection of RSA: a countermeasure to fault attack. *IEEE Transactions on Computers*, 61(7):1040–1049, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mali:2016:TTB**

[MM16] Goutam Mali and Sudip Misra. TRAST: Trust-based distributed topology management for wireless multimedia sensor networks. *IEEE Transactions on Computers*, 65(6):1978–1991, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mazumdar:2017:CRS**

[MM17] Bodhisatwa Mazumdar and Debdeep Mukhopadhyay. Construction of rotation symmetric S-boxes with high nonlinearity and improved DPA resistivity. *IEEE Transactions on Computers*, 66(1):59–72, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Milutinovic:2019:IRS**

[MMAC19] S. Milutinovic, E. Mezzetti, J. Abella, and F. J. Cazorla. Increasing the reliability of software timing analysis for cache-based processors. *IEEE Transactions on Computers*, 68(6):836–851, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mostefaoui:2014:LRA**

[MMB14] A. Mostefaoui, M. Melkemi, and A. Boukerche. Localized routing approach to bypass holes in wireless sensor networks. *IEEE Transactions on Computers*, 63(12): 3053–3065, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maddah:2015:RTM**

[MMC15] R. Maddah, R. Melhem, and Sangyeun Cho. RDIS: Tolerating many stuck-at faults in resistive memory. *IEEE Transactions on Computers*, 64(3):847–861, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2016:SJb**

[MMC+16] Paolo Montuschi, Edward J. McCluskey, Samarjit Chakraborty, Jason Cong, Ramón M. Rodríguez-Dagnino, Fred Douglis, Lieven Eeckhout, Gernot Heiser, Sushil Jajodia, Ruby B. Lee, Dinesh Manocha, Tomás F. Pena, Isabelle Puaut, Hanan Samet, and Donatella Sciuto. State of the journal. *IEEE Transactions on Computers*, 65 (7):2014–2018, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Manna:2018:TAA**

[MMCS18] Kanchan Manna, Priyajit Mukherjee, Santanu Chattopadhyay, and Indranil Sengupta. Thermal-aware application mapping strategy for network-on-chip based system design. *IEEE Transactions on Computers*, 67 (4):528–542, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8096996/`.

**Misra:2014:AAC**

[MMH14] S. Misra, N. E. Majd, and Hong Huang. Approximation algorithms for constrained relay node placement in energy harvesting wireless sensor networks. *IEEE Transactions on Computers*, 63(12):2933–2947, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Moradi:2013:OAR**

[MMP13] Amir Moradi, Oliver Mischke, and Christof Paar. One attack to rule them all: Collision timing attack versus 42 AES ASIC cores. *IEEE Transactions on Computers*, 62(9):1786–1798, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maniatakos:2015:RVA**

[MMTM15] M. Maniatakos, M. Michael, C. Tirumurti, and Y. Makris. Revisiting vulnerability analysis in modern microprocessors. *IEEE Transactions on Computers*, 64(9): 2664–2674, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Macdonald:2014:PMF**

[MNFA14] Kevin Macdonald, Christopher Nitta, Matthew Farrens, and Venkatesh Akella. PDG_GEN: A methodology for fast and accurate simulation of on-chip networks. *IEEE Transactions on Computers*, 63(3):650–663, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mashayekhy:2016:OMR**

[MNGV16] L. Mashayekhy, M. M. Nejad, D. Grosu, and A. V. Vasilakos. An online mechanism for resource allocation and pricing in clouds. *IEEE Transactions on Computers*, 65(4):1172–1184, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mladenov:2011:IER**

[MNK11] Todor Mladenov, Saeid Nooshabadi, and Keseon Kim. Implementation and evaluation of Raptor codes on embedded systems. *IEEE Transactions on Computers*, 60(12):1678–1691, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5601702.

**Metra:2012:NDT**

[MOMT12] Cecilia Metra, Martin Omana, T. M. Mak, and Simon Tam. New design for testability approach for clock fault testing. *IEEE Transactions on Computers*, 61(4):448–457, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2015:ENE**

[Mon15a] P. Montuschi. Editorial from the new Editor in Chief. *IEEE Transactions on Computers*, 64(1):2, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2015:SJ**

[Mon15b] P. Montuschi. State of the journal. *IEEE Transactions on Computers*, 64 (6):1506–1508, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2016:SJa**

[Mon16] P. Montuschi. State of the journal. *IEEE Transactions on Computers*, 65(1): 1–4, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2017:SJ**

[Mon17] Paolo Montuschi. State of the journal. *IEEE Transactions on Computers*, 66(1): 1–2, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2018:SJ**

[Mon18] Paolo Montuschi. State of the journal. *IEEE Transactions on Computers*, 67(1): 1, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Montuschi:2019:TYS**

[Mon19] Paolo Montuschi. Thank-you state of the journal editorial by the outgoing Editor-in-Chief. *IEEE Transactions on Computers*, 68(1): 1–2, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8573061/`.

**Milano:2014:GEI**

[MOS14] Michela Milano, Barry O'Sullivan, and Martin Sachenbacher. Guest Editors' introduction: Special section on computational sustainability: Where computer science meets sustainable development. *IEEE Transactions on Computers*, 63(1):88–89, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mastroleon:2012:PDA**

[MOYB12] Lykomidis Mastroleon, Daniel C. O'Neill, Benjamin Yolken, and Nicholas Bambos. Power and delay aware management of packet switches. *IEEE Transactions on Computers*, 61(12):1789–1799, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Matula:2015:MDE**

[MPZ15] D. W. Matula, M. T. Panu, and J. Y. Zhang. Multiplicative division employing independent factors. *IEEE Transactions on Computers*, 64(7):2012–2019, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Magron:2019:CRE**

[MRD19] V. Magron, A. Rocca, and T. Dang. Certified roundoff error bounds using Bernstein expansions and sparse Krivine–Stengle representations. *IEEE Transactions on Computers*, 68(7):953–966, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Migliore:2018:HSC**

[MRL+18] Vincent Migliore, Maria Méndez Real, Vianney Lapotre, Arnaud Tisserand, Caroline Fontaine, and Guy Gogniat. Hardware/software co-design of an accelerator for FV homomorphic encryption scheme using Karatsuba algorithm. *IEEE Transactions on Computers*, 67 (3):335–347, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7797469/`.

**Menzel:2015:CHD**

[MRW+15] M. Menzel, R. Ranjan, Lizhe Wang, S. U. Khan, and

Jinjun Chen. CloudGenius: A hybrid decision support method for automating the migration of Web application clusters to public clouds. *IEEE Transactions on Computers*, 64 (5):1336–1348, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mishra:2012:PPE**

[MS12] Tania Banerjee Mishra and Sartaj Sahni. PETCAM — a power efficient TCAM architecture for forwarding tables. *IEEE Transactions on Computers*, 61(1):3–17, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**McCartney:2015:SMT**

[MS15] W. P. McCartney and N. Sridhar. Stackless multithreading for embedded systems. *IEEE Transactions on Computers*, 64(10):2940–2952, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Martinez:2012:DTR**

[MSC12] Carlos Alvarez Martinez, Jesus Corbal San Adrian, and Mateo Valero Cortes. Dynamic tolerance region computing for multimedia. *IEEE Transactions on Computers*, 61(5):650–665, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ma:2014:DBV**

[MSG14] Zhiqiang Ma, Zhonghua Sheng, and Lin Gu. DVM: A big virtual machine for cloud computing. *IEEE Transactions on Computers*, 63(9): 2245–2258, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mardiris:2015:ADA**

[MSK15] V. A. Mardiris, G. C. Sirakoulis, and I. G. Karafyllidis. Automated design architecture for 1-D cellular automata using quantum cellular automata. *IEEE Transactions on Computers*, 64(9): 2476–2489, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mosenia:2017:CCA**

[MSKRJ17] A. Mosenia, S. Sur-Kolay, A. Raghunathan, and N. K. Jha. CABA: Continuous authentication based on BioAura. *IEEE Transactions on Computers*, 66(5): 759–772, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Majumder:2012:NBH**

[MSPK12] Turbo Majumder, Souradip Sarkar, Partha Pratim Pande, and Ananth Kalyanaraman. NoC-based hardware accelerator for breakpoint phylogeny. *IEEE Transactions on Computers*, 61(6):857–869, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maitra:2017:DFA**

[MSS17] Subhamoy Maitra, Akhilesh Siddhanti, and Santanu Sarkar. A differential fault attack on Plantlet. *IEEE Transactions on Computers*, 66(10):1804–1808, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7917296/`.

**Maitra:2018:TAA**

[MSS+18] Subhamoy Maitra, Nishant Sinha, Akhilesh Siddhanti, Ravi Anand, and Sugata Gangopadhyay. A TMDTO attack against Lizard. *IEEE Transactions on Computers*, 67(5):733–739, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8107499/`.

**Moser:2010:APM**

[MTBB10] C. Moser, L. Thiele, D. Brunelli, and L. Benini. Adaptive power management for environmentally powered systems. *IEEE Transactions on Computers*, 59(4):478–491, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5291691`.

**Mao:2019:AMC**

[MTFK19] B. Mao, F. Tang, Z. M. Fadlullah, and N. Kato. An absorbing Markov chain based model to solve computation and communication tradeoff in GPU-accelerated MDRUs for safety confirmation in disaster scenarios. *IEEE Transactions on Computers*, 68(9):1256–1268, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maniatakos:2012:GSV**

[MTGM12] Michail Maniatakos, Chandrasekharan (Chandra) Tirumurti, Rajesh Galivanche, and Yiorgos Makris. Global signal vulnerability (GSV) analysis for selective state element hardening in modern microprocessors. *IEEE Transactions on Computers*, 61(10):1361–1370, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Milenkovic:2011:CPR**

[MUMB11] Aleksandar Milenković, Vladimir Uzelac, Milena Milenković, and Martin Burtscher. Caches and predictors for real-time, unobtrusive, and cost-effective program tracing in embedded systems. *IEEE Transactions on Computers*, 60(7):992–1005, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mangassarian:2010:RQE**

[MVB10] H. Mangassarian, A. Veneris, and M. Benedetti. Robust QBF encodings for sequential circuits with applications to verification, debug, and test. *IEEE Transactions on Computers*, 59(7):981–994, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5441288`.

**Mao:2010:HSS**

[MW10] Shufu Mao and T. Wolf. Hardware support for secure processing in embedded systems. *IEEE Transactions on Computers*, 59(6):847–854, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5406503`.

**Muller:2013:NXN**

[MW13] Dirk Muller and Matthias Werner. A note on "New Strategies for Assigning Real-Time Tasks to Multiprocessor Systems". *IEEE Transactions on Computers*, 62(9):1904–1905, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ma:2015:LOS**

[MWLJ15] Sheng Ma, Zhiying Wang, Zonglin Liu, and N. E. Jerger. Leaving one slot empty: Flit bubble flow control for torus cache-coherent NoCs. *IEEE Transactions on Computers*, 64(3):763–777, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ma:2014:DDP**

[MWW14] Kai Ma, Xiaorui Wang, and Yefu Wang. DPPC: Dynamic power partitioning and control for improved chip multiprocessor performance. *IEEE Transactions on Computers*, 63(7):1736–1750, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Maneesilp:2013:RSA**

[MWWT13] Jullawadee Maneesilp, Chong Wang, Hongyi Wu, and Nian-Feng Tzeng. RFID support for accurate 3D localization. *IEEE Transactions on Computers*, 62

(7):1447–1459, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Meng:2016:SRA**

[MWY+16] T. Meng, F. Wu, Z. Yang, G. Chen, and A. V. Vasilakos. Spatial reusability-aware routing in multi-hop wireless networks. *IEEE Transactions on Computers*, 65(1):244–255, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Mao:2017:EEG**

[MWZ+17] Mengjie Mao, Wujie Wen, Yaojun Zhang, Yiran Chen, and Hai Li. An energy-efficient GPGPU register file architecture using racetrack memory. *IEEE Transactions on Computers*, 66(9): 1478–1490, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7891951/`.

**Mokhov:2010:CPO**

[MY10] A. Mokhov and A. Yakovlev. Conditional partial order graphs: Model, synthesis, and application. *IEEE Transactions on Computers*, 59(11):1480–1493, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432155`.

**Ma:2016:AFE**

[MYHL16] J. Ma, G. Yan, Y. Han, and X. Li. An analytical framework for estimating scale-out and scale-up power efficiency of heterogeneous manycores. *IEEE Transactions on Computers*, 65 (2):367–381, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Min:2011:EDT**

[MYW11] Jaehong Min, Daeyoung Yoon, and Youjip Won. Efficient deduplication techniques for modern backup operation. *IEEE Transactions on Computers*, 60 (6):824–840, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nannarelli:2016:PPS**

[Nan16] Alberto Nannarelli. Performance/power space exploration for Binary64 division units. *IEEE Transactions on Computers*, 65 (5):1671–1677, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nannarelli:2019:TFP**

[Nan19] A. Nannarelli. Tunable floating-point adder. *IEEE*

*Transactions on Computers*, 68(10):1553–1560, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nain:2017:SPE**

[NBZP17] Ajay Kumar Nain, Jagadish Bandaru, Mohammed Abdullah Zubair, and Rajalakshmi Pachamuthu. A secure phase-encrypted IEEE 802.15.4 transceiver design. *IEEE Transactions on Computers*, 66(8):1421–1427, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7862216/.

**Nisar:2011:GPC**

[NC11] Muhammad M. Nisar and Abhijit Chatterjee. Guided probabilistic checksums for error control in low-power digital filters. *IEEE Transactions on Computers*, 60(9):1313–1326, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5674023.

**Napoli:2017:SRC**

[NCD+17] Ettore Napoli, Gerardo Castellano, Davide De Caro, Darjn Esposito, Nicola Petra, and Antonio G. M. Strollo. A SISO register circuit tailored for input data with low transition probability. *IEEE Transactions on Computers*, 66(1):45–51, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Narasimhan:2013:HTD**

[NDC+13] Seetharam Narasimhan, Dongdong Du, Rajat Subhra Chakraborty, Somnath Paul, Francis G. Wolff, Christos A. Papachristou, Kaushik Roy, and Swarup Bhunia. Hardware Trojan detection by multiple-parameter side-channel analysis. *IEEE Transactions on Computers*, 62(11):2183–2195, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ngo:2017:CSS**

[NDG+17] Xuan Thuy Ngo, Jean-Luc Danger, Sylvain Guilley, Tarik Graba, Yves Mathieu, Zakaria Najm, and Shivam Bhasin. Cryptographically secure shield for security IPs protection. *IEEE Transactions on Computers*, 66(2):354–360, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Naithani:2018:OSE**

[NEE18] Ajeya Naithani, Stijn Eyerman, and Lieven Eeckhout. Optimizing soft error reliability through scheduling on heterogeneous multicore processors. *IEEE Transactions on Computers*, 67(6):830–846, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8126846/`.

**Niyato:2010:MMH**

[NH10] D. Niyato and E. Hossain. A microeconomic model for hierarchical bandwidth sharing in dynamic spectrum access networks. *IEEE Transactions on Computers*, 59(7):865–877, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453338`.

**Namba:2011:TSR**

[NI11] Kazuteru Namba and Hideo Ito. Test sets for robust path delay fault testing on two-rail logic circuits. *IEEE Transactions on Computers*, 60(10):1459–1470, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5661767`.

**Nam:2011:OOO**

[NKEM11] Eyee Hyun Nam, Bryan Suk Joon Kim, Hyeonsang Eom, and Sang Lyul Min. Ozone (O3): An out-of-order flash memory controller architecture. *IEEE Transactions on Computers*, 60(5):653–666, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nicolici:2014:NAa**

[NL14] Nicola Nicolici and Zahra Lak. A novel algorithmic approach to aid post-silicon delay measurement and clock tuning. *IEEE Transactions on Computers*, 63(5):1074–1084, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Namba:2015:NBO**

[NL15a] K. Namba and F. Lombardi. Non-binary orthogonal latin square codes for a multilevel phase charge memory (PCM). *IEEE Transactions on Computers*, 64(7):2092–2097, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Namba:2015:PDT**

[NL15b] K. Namba and F. Lombardi. Parallel decodable two-level unequal burst error correcting codes. *IEEE Transactions on Computers*, 64(10): 2902–2911, October 2015. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Namba:2016:PDM**

[NL16a] Kazuteru Namba and Fabrizio Lombardi. Parallel decodable multi-level unequal burst error correcting codes for memories of approximate systems. *IEEE Transactions on Computers*, 65(12): 3794–3801, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Namba:2016:SMS**

[NL16b] Kazuteru Namba and Fabrizio Lombardi. Single multiscale-symbol error correction codes for multiscale storage systems. *IEEE Transactions on Computers*, 65(6):2005–2009, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nikolov:2016:OLC**

[NL16c] D. Nikolov and E. Larsson. Optimizing the level of confidence for multiple jobs. *IEEE Transactions on Computers*, 65(4):1239–1252, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Namba:2018:SAE**

[NL18] Kazuteru Namba and Fabrizio Lombardi. A single and adjacent error correction code for fast decoding of critical bits. *IEEE Transactions on Computers*, 67(10): 1525–1531, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8329222/`.

**Namba:2019:CWL**

[NL19] Kazuteru Namba and Fabrizio Lombardi. Coding for write latency reduction in a Multi-Level Cell (MLC) Phase Change Memory (PCM). *IEEE Transactions on Computers*, 68 (2):301–306, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8454732/`.

**Nam:2014:GEE**

[NLP+14] Min-Young Nam, Jaemyoun Lee, Kyung-Joon Park, Lui Sha, and Kyungtae Kang. Guaranteeing the end-to-end latency of an IMA system with an increasing workload. *IEEE Transactions on Computers*, 63(6):1460–1473, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Najafi:2017:PCE**

[NLRB17] M. Hassan Najafi, David J. Lilja, Marc D. Riedel, and

Kia Bazargan. Polysynchronous clocking: Exploiting the skew tolerance of stochastic circuits. *IEEE Transactions on Computers*, 66(10):1734–1746, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7911306/.

**Neophytou:2010:TSG**

[NM10] S. N. Neophytou and M. K. Michael. Test set generation with a large number of unspecified bits using static and dynamic techniques. *IEEE Transactions on Computers*, 59(3):301–316, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5342413.

**Negre:2015:NPA**

[NR15] C. Negre and J.-M. Robert. New parallel approaches for scalar multiplication in elliptic curve over fields of small characteristic. *IEEE Transactions on Computers*, 64(10):2875–2890, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nguyen:2015:FRT**

[NRG15] Thi Huyen Chau Nguyen, P. Richard, and E. Grolleau. An FPTAS for response time analysis of fixed priority real-time tasks with resource augmentation. *IEEE Transactions on Computers*, 64(7):1805–1818, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nopper:2013:SMC**

[NS13] Tobias Nopper and Christoph Scholl. Symbolic model checking for incomplete designs with flexible modeling of unknowns. *IEEE Transactions on Computers*, 62(6):1234–1254, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nannarelli:2014:GEI**

[NST14] Alberto Nannarelli, Peter-Michael Seidel, and Ping Tak Peter Tang. Guest Editors' introduction: Special section on computer arithmetic. *IEEE Transactions on Computers*, 63(8):1852–1853, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Neto:2014:PUP**

[NTR14] Joao Carlos Neto, Alexandre Ferreira Tenca, and Wilson Vicente Ruggiero. A parallel and uniform $k$-partition method for Montgomery multiplication. *IEEE Transactions on Computers*, 63(9):2122–2133, September 2014.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nieto:2016:PRS**

[NVB16] Alejandro Nieto, David L. Vilariño, and Victor M. Brea. PRECISION: A reconfigurable SIMD/MIMD coprocessor for computer vision systems-on-chip. *IEEE Transactions on Computers*, 65(8):2548–2561, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Namin:2011:WLF**

[NWA11] Ashkan Hosseinzadeh Namin, Huapeng Wu, and Majid Ahmadi. A word-level finite field multiplier using normal basis. *IEEE Transactions on Computers*, 60(6):890–895, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Namin:2012:HSA**

[NWA12] Ashkan Hosseinzadeh Namin, Huapeng Wu, and Majid Ahmadi. High-speed architectures for multiplication using reordered normal basis. *IEEE Transactions on Computers*, 61 (2):164–172, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nunez-Yanez:2015:AVS**

[NY15] J. L. Nunez-Yanez. Adaptive voltage scaling with in-situ detectors in commercial FPGAs. *IEEE Transactions on Computers*, 64 (1):45–53, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nunez-Yanez:2019:EPN**

[NY19] J. Nunez-Yanez. Energy proportional neural network inference with adaptive voltage and frequency scaling. *IEEE Transactions on Computers*, 68(5):676–687, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nunez-Yanez:2016:EOC**

[NYHB16] J. Luis Nunez-Yanez, M. Hosseinabady, and A. Beldachi. Energy optimization in commercial FPGAs with voltage, frequency and logic scaling. *IEEE Transactions on Computers*, 65(5):1484–1493, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Neishaburi:2014:NMT**

[NZ14] M. H. Neishaburi and Zeljko Zilic. On a new mechanism of trigger generation for post-silicon debugging. *IEEE Transactions on Computers*, 63(9):

2330–2342, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nechma:2015:PSM**

[NZ15] T. Nechma and M. Zwolinski. Parallel sparse matrix solution for circuit simulation on FPGAs. *IEEE Transactions on Computers*, 64(4):1090–1103, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Nong:2011:TEA**

[NZC11] Ge Nong, Sen Zhang, and Wai Hong Chan. Two efficient algorithms for linear time suffix array construction. *IEEE Transactions on Computers*, 60(10): 1471–1484, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5582081`.

**Nie:2014:LCB**

[NZLK14] Weiran Nie, Sen Zhou, Kwei-Jay Lin, and Soo Dong Kim. An on-line capacity-based admission control for real-time service processes. *IEEE Transactions on Computers*, 63(9):2134–2145, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Oh:2017:DBE**

[OCK17] Hyunggoy Oh, Inhyuk Choi, and Sungho Kang. DRAM-based error detection method to reduce the post-silicon debug time for multiple identical cores. *IEEE Transactions on Computers*, 66(9): 1504–1517, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7872459/`.

**Ozturk:2017:CAH**

[ÖDSS17] Erdinç Öztürk, Yarkın Doröz, Erkay Savaş, and Berk Sunar. A custom accelerator for homomorphic encryption applications. *IEEE Transactions on Computers*, 66(1):3–16, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**On:2014:FBC**

[OGH+14] Sai Tung On, Shen Gao, Bingsheng He, Ming Wu, Qiong Luo, and Jianliang Xu. FD-Buffer: A cost-based adaptive buffer replacement algorithm for flash memory devices. *IEEE Transactions on Computers*, 63(9): 2288–2301, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ozsoy:2014:ESS**

[OGPK14] Meltem Ozsoy, Nael Abu Ghazaleh, Dmitry Ponomarev, and Mehmet Kayaalp. Efficiently securing systems from code reuse attacks. *IEEE Transactions on Computers*, 63(5):1144–1156, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Oh:2017:CED**

[OHCK17] Hyunggoy Oh, Taewoo Han, Inhyuk Choi, and Sungho Kang. An on-chip error detection method to reduce the post-silicon debug time. *IEEE Transactions on Computers*, 66 (1):38–44, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ozturk:2013:CDE**

[OKC13] Ozcan Ozturk, Mahmut Kandemir, and Guangyu Chen. Compiler-directed energy reduction using dynamic voltage scaling and voltage islands for embedded systems. *IEEE Transactions on Computers*, 62 (2):268–278, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ozsoy:2016:HBM**

[OKD+16] Meltem Ozsoy, Khaled N. Khasawneh, Caleb Donovick, Iakov Gorelik, Nael Abu-Ghazaleh, and Dmitry Ponomarev. Hardware-based malware detection using low-level architectural features. *IEEE Transactions on Computers*, 65(11):3332–3344, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Oh:2019:ACP**

[OKY+19] Yunho Oh, Keunsoo Kim, Myung Kuk Yoon, Jong Hyun Park, Yongjun Park, Murali Annavaram, and Won Woo Ro. Adaptive cooperation of prefetching and warp scheduling on GPUs. *IEEE Transactions on Computers*, 68(4):609–616, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8515055/`.

**Onizawa:2014:HTC**

[OMFH14] Naoya Onizawa, Atsushi Matsumoto, Tomoyoshi Funazaki, and Takahiro Hanyu. High-throughput compact delay-insensitive asynchronous NoC router. *IEEE Transactions on Computers*, 63 (3):637–649, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ozturk:2017:CHA**

[OOD+17] Ozcan Ozturk, Umut Orhan,

Wei Ding, Praveen Yedlapalli, and Mahmut Taylan Kandemir. Cache hierarchy-aware query mapping on emerging multicore architectures. *IEEE Transactions on Computers*, 66(3):403–415, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Otoom:2015:MCM**

[OP15] M. Otoom and J. M. Paul. Multiprocessor capacity metric and analysis. *IEEE Transactions on Computers*, 64(11):3181–3196, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ozsoy:2014:SLC**

[OPAGS14] Meltem Ozsoy, Dmitry Ponomarev, Nael Abu-Ghazaleh, and Tameesh Suri. SIFT: Low-complexity energy-efficient information flow tracking on SMT processors. *IEEE Transactions on Computers*, 63(2):484–496, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Omana:2017:NAP**

[OPV+17] Martin Omana, Marco Padovani, Kreshnik Veliu, Cecilia Metra, Juergen Alt, and Rajesh Galivanche. New approaches for power binning of high performance microprocessors. *IEEE Transactions on Computers*, 66(7):1159–1171, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://www.computer.org/csdl/trans/tc/2017/07/07822988-abs.html.

**Oshin:2015:EER**

[OPZ15] T. O. Oshin, S. Poslad, and Zelun Zhang. Energy-efficient real-time human mobility state classification using Smartphones. *IEEE Transactions on Computers*, 64(6):1680–1693, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Omana:2013:LCN**

[ORBM13] Martin Omana, Daniele Rossi, Nicolo Bosio, and Cecilia Metra. Low cost NBTI degradation detection and masking approaches. *IEEE Transactions on Computers*, 62(3):496–509, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Omana:2010:HPR**

[ORM10] M. Omaña, D. Rossi, and C. Metra. High-performance robust latches. *IEEE Transactions on Computers*, 59(11):1455–1465, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396333.

**Oseledets:2011:ITK**

[Ose11] Ivan Oseledets. Improved $n$-term Karatsuba-like formulas in GF(2). *IEEE Transactions on Computers*, 60 (8):1212–1216, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Or:2016:MBH**

[OWP16] N. L. Or, X. Wang, and D. Pao. MEMORY-based hardware architectures to detect ClamAV virus signatures with restricted regular expression features. *IEEE Transactions on Computers*, 65(4):1225–1238, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Oh:2018:WSD**

[OYP+18] Yunho Oh, Myung Kuk Yoon, Jong Hyun Park, Yongjun Park, and Won Woo Ro. WASP: Selective data prefetching with monitoring runtime warp progress on GPUs. *IEEE Transactions on Computers*, 67(9): 1366–1373, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8309426/.

**Paterna:2012:VAT**

[PAC+12] Francesco Paterna, Andrea Acquaviva, Alberto Caprara, Francesco Papariello, Giuseppe Desoli, and Luca Benini. Variability-aware task allocation for energy-efficient quality of service provisioning in embedded streaming multimedia applications. *IEEE Transactions on Computers*, 61(7):939–953, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Panda:2016:SSP**

[Pan16] Biswabandan Panda. SPAC: A synergistic prefetcher aggressiveness controller for multi-core systems. *IEEE Transactions on Computers*, 65(12):3740–3753, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Patel:2013:NGN**

[PAP13] Kimish Patel, Murali Annavaram, and Massoud Pedram. NFRA: Generalized network flow-based resource allocation for hosting centers. *IEEE Transactions on Computers*, 62(9): 1772–1785, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Piso:2011:VLG**

[PB11] Daniel Piso and Javier D. Bruguera. Variable latency Goldschmidt algorithm based on a new rounding method and a remainder estimate. *IEEE Transactions on Computers*, 60(11): 1535–1546, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669291`.

**Parikh:2016:RCD**

[PB16] Ritesh Parikh and Valeria Bertacco. Resource conscious diagnosis and reconfiguration for NoC permanent faults. *IEEE Transactions on Computers*, 65 (7):2241–2256, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Poursafaei:2017:NNA**

[PBE17] Farimah R. Poursafaei, Mostafa Bazzaz, and Alireza Ejlali. NPAM: NVM-aware page allocation for multi-core embedded systems. *IEEE Transactions on Computers*, 66(10): 1703–1716, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7926310/`.

**Park:2016:BIL**

[PBL16] Jaesung Park, Heejung Byun, and Jung-Ryun Lee. Bio-inspired load-balancing framework for loosely coupled heterogeneous server systems. *IEEE Transactions on Computers*, 65(11): 3280–3292, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pontarelli:2013:TAD**

[PBT13] Salvatore Pontarelli, Giuseppe Bianchi, and Simone Teofili. Traffic-aware design of a high-speed FPGA network intrusion detection system. *IEEE Transactions on Computers*, 62(11): 2322–2334, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Panda:2011:CCS**

[PBV11] Preeti Ranjan Panda, M. Balakrishnan, and Anant Vishnoi. Compressing cache state for postsilicon processor debug. *IEEE Transactions on Computers*, 60(4):484–497, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pellizzoni:2010:IPP**

[PC10] R. Pellizzoni and M. Caccamo. Impact of peripheral-processor interference on

WCET analysis of real-time embedded systems. *IEEE Transactions on Computers*, 59(3):400–415, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5282496`.

**Paul:2016:TSO**

[PC16] G. Paul and A. Chattopadhyay. Three snakes in one hole: The first systematic hardware accelerator design for SOSEMANUK with optional serpent and SNOW 2.0 modes. *IEEE Transactions on Computers*, 65(2):640–653, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Park:2014:CXP**

[PCHS14] Sun-Mi Park, Ku-Young Chang, Dowon Hong, and Changho Seo. Comments on "On the Polynomial Multiplication in Chebyshev Form". *IEEE Transactions on Computers*, 63(12):3162–3163, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [ACO12].

**Park:2016:CMS**

[PCHS16] S. Park, K. Chang, D. Hong, and C. Seo. Comments on "Multiway Splitting Method for Toeplitz Matrix Vector Product". *IEEE Transactions on Computers*, 65(1):332–333, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [HN13].

**Park:2017:NBR**

[PCHS17] Sun-Mi Park, Ku-Young Chang, Dowon Hong, and Changho Seo. New block recombination for subquadratic space complexity polynomial multiplication based on overlap-free approach. *IEEE Transactions on Computers*, 66(8):1396–1406, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7857684/`.

**Park:2018:SSC**

[PCHS18] Sun-Mi Park, Ku-Young Chang, Dowon Hong, and Changho Seo. Subquadratic space complexity multiplier using even type GNB based on efficient Toeplitz matrix-vector product. *IEEE Transactions on Computers*, 67(12):1794–1805, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8359428/`.

**Park:2015:IIB**

[PCLN15] H. Park, J. Choi, D. Lee, and S. H. Noh. iBuddy: Inverse buddy for enhancing memory allocation/deallocation performance on multi-core systems. *IEEE Transactions on Computers*, 64 (3):720–732, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Paul:2011:RDE**

[PCZB11] Somnath Paul, Fang Cai, Xinmiao Zhang, and Swarup Bhunia. Reliability-driven ECC allocation for multiple bit error resilience in processor cache. *IEEE Transactions on Computers*, 60 (1):20–34, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pourshaghaghi:2013:FCV**

[PdG13] Hamid Reza Pourshaghaghi and Jose Pineda de Gyvez. Fuzzy-controlled voltage scaling based on supply current tracking. *IEEE Transactions on Computers*, 62(12): 2397–2410, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Patranabis:2019:SCS**

[PDJ+19] S. Patranabis, N. Datta, D. Jap, J. Breier, S. Bhasin, and D. Mukhopadhyay. SCADFA: Combined SCA + DFA attacks on block ciphers with practical validations. *IEEE Transactions on Computers*, 68(10): 1498–1510, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pan:2013:UQE**

[PDXZ13] Yangyang Pan, Guiqiang Dong, Ningde Xie, and Tong Zhang. Using quasi-EZ-NAND flash memory to build large-capacity solid-state drives in computing systems. *IEEE Transactions on Computers*, 62 (5):1051–1057, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pravadelli:2014:TQS**

[PFGB14] Graziano Pravadelli, Franco Fummi, Valerio Guarnieri, and Nicola Bombieri. Testbench qualification of SystemC TLM protocols through mutation analysis. *IEEE Transactions on Computers*, 63(5):1248–1261, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pedram:2014:AAF**

[PGvdG14] Ardavan Pedram, Andreas Gerstlauer, and Robert A. van de Geijn. Algorithm, architecture, and floating-point unit codesign of a matrix factorization accelerator. *IEEE Transactions on Computers*, 63(8): 1854–1867, August 2014.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pippenger:2011:FAS**

[Pip11] Nicholas Pippenger. On-the-fly algorithms and sequential machines. *IEEE Transactions on Computers*, 60(9): 1372–1375, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5710885.

**Pagani:2017:TSP**

[PKC+17] Santiago Pagani, Heba Khdr, Jian-Jia Chen, Muhammad Shafique, Minming Li, and Jörg Henkel. Thermal safe power (TSP): Efficient power budgeting for heterogeneous manycore systems in dark silicon. *IEEE Transactions on Computers*, 66(1):147–162, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Park:2019:GGC**

[PKY19] SeongJae Park, Minchan Kim, and Heon Y. Yeom. GCMA: Guaranteed contiguous memory allocator. *IEEE Transactions on Computers*, 68(3):390–401, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8456561/.

**Palangappa:2016:WCS**

[PLM16] P. M. Palangappa, J. Li, and K. Mohanram. WOM-code solutions for low latency and high endurance in phase change memory. *IEEE Transactions on Computers*, 65(4):1025–1040, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Papagianni:2013:OAV**

[PLP+13] Chrysa Papagianni, Aris Leivadeas, Symeon Papavassiliou, Vasilis Maglaris, Cristina Cervello-Pastor, and Alvaro Monje. On the optimal allocation of virtual resources in cloud computing networks. *IEEE Transactions on Computers*, 62 (6):1060–1071, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Peng:2014:TSD**

[PLZW14] Wei Peng, Feng Li, Xukai Zou, and Jie Wu. A two-stage deanonymization attack against anonymized social networks. *IEEE Transactions on Computers*, 63 (2):290–303, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pricopi:2014:TSA**

[PM14] M. Pricopi and T. Mitra. Task scheduling on adaptive multi-core. *IEEE Transactions on Computers*, 63(10): 2590–2603, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Possa:2014:MRF**

[PMH+14] P. R. Possa, S. A. Mahmoudi, N. Harb, C. Valderrama, and P. Manneback. A multi-resolution FPGA-based architecture for real-time edge and corner detection. *IEEE Transactions on Computers*, 63(10): 2376–2388, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Papavramidou:2016:TAE**

[PN16] Panagiota Papavramidou and Michael Nicolaidis. Test algorithms for ECC-based memory repair in ultimate CMOS and post-CMOS. *IEEE Transactions on Computers*, 65(7):2284–2298, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pattabiraman:2013:SSP**

[PNKI13] Karthik Pattabiraman, Nithin M. Nakka, Zbigniew T. Kalbarczyk, and Ravishankar K. Iyer. SymPLFIED: Symbolic program-level fault injection and error detection framework. *IEEE Transactions on Computers*, 62(11): 2292–2307, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pontarelli:2013:EDC**

[PO13] Salvatore Pontarelli and Marco Ottavi. Error detection and correction in content addressable memories by using Bloom filters. *IEEE Transactions on Computers*, 62(6):1111–1126, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2012:CFT**

[Pom12a] Irith Pomeranz. Concatenation of functional test subsequences for improved fault coverage and reduced test length. *IEEE Transactions on Computers*, 61(6):899–904, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2012:FIU**

[Pom12b] Irith Pomeranz. Fast identification of undetectable transition faults under functional broadside tests. *IEEE Transactions on Computers*, 61(6):905–910, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2012:CCT**

[Pom12c] Irith Pomeranz. On the computation of common test data for broadside and skewed-load tests. *IEEE Transactions on Computers*, 61(4):578–583, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2012:SAS**

[Pom12d] Irith Pomeranz. On the switching activity and static test compaction of multicycle scan-based tests. *IEEE Transactions on Computers*, 61(8):1179–1188, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2013:ASA**

[Pom13a] Irith Pomeranz. An adjacent switching activity metric under functional broadside tests. *IEEE Transactions on Computers*, 62 (2):404–410, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2013:STP**

[Pom13b] Irith Pomeranz. Signal-transition patterns of functional broadside tests. *IEEE Transactions on Computers*, 62(12):2544–2549, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2014:SLB**

[Pom14] Irith Pomeranz. Sharing logic for built-in generation of functional broadside tests. *IEEE Transactions on Computers*, 63 (4):1048–1054, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2015:PFB**

[Pom15a] I. Pomeranz. Piecewise-functional broadside tests based on reachable states. *IEEE Transactions on Computers*, 64(8):2415–2420, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2015:TVO**

[Pom15b] I. Pomeranz. Test vector omission for fault coverage improvement of functional test sequences. *IEEE Transactions on Computers*, 64(11):3317–3321, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2015:TDS**

[Pom15c] I. Pomeranz. Two-dimensional static test compaction for functional test sequences. *IEEE Transactions on Computers*, 64(10):3009–3015, October 2015. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Pomeranz:2016:BGP**

[Pom16a] Irith Pomeranz. *LFSR*-based generation of partially-functional broadside tests. *IEEE Transactions on Computers*, 65(8):2659–2664, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2016:IAD**

[Pom16b] Irith Pomeranz. Improving the accuracy of defect diagnosis with multiple sets of candidate faults. *IEEE Transactions on Computers*, 65(7):2332–2338, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pathak:2010:EET**

[PP10] A. Pathak and V. K. Prasanna. Energy-efficient task mapping for data-driven sensor network macroprogramming. *IEEE Transactions on Computers*, 59(7):955–968, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5313800`.

**Park:2011:HPS**

[PP11] Youngwoo Park and Kyu Ho Park. High-performance scalable flash file system using virtual metadata storage with phase-change RAM. *IEEE Transactions on Computers*, 60(3):321–334, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Park:2014:ETM**

[PP14] Heemin Park and Moonju Park. An efficient test method for rate monotonic schedulability. *IEEE Transactions on Computers*, 63(5):1309–1315, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Parfieniuk:2016:SIC**

[PP16] Marek Parfieniuk and Sang Yoon Park. Sparse-iteration 4D CORDIC algorithms for multiplying quaternions. *IEEE Transactions on Computers*, 65(9):2859–2871, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Potharaju:2014:WSE**

[PPB+14] Rahul Potharaju, Dan Pei, Fabian E. Bustamante, Yan Chen, David R. Choffnes, Yao Zhao, and Kai Chen. Where the sidewalk ends: Extending the Internet AS graph using traceroutes from P2P users. *IEEE Transactions on Computers*, 63(4):1021–1036, April 2014.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Park:2012:PAN**

[PPKW12] Seung-Ho Park, Jung-Wook Park, Shin-Dug Kim, and Charles C. Weems. A pattern adaptive NAND flash memory storage structure. *IEEE Transactions on Computers*, 61(1):134–138, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Psarras:2017:DCM**

[PPND17] Anastasios Psarras, Michalis Paschou, Chrysostomos Nicopoulos, and Giorgos Dimitrakopoulos. A dual-clock multiple-queue shared buffer. *IEEE Transactions on Computers*, 66(10):1809–1815, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7930511/`.

**Pham:2013:LOF**

[PPP13] Hung-Manh Pham, Sebastien Pillement, and Stanisław J. Piestrak. Low-overhead fault-tolerance technique for a dynamically reconfigurable softcore processor. *IEEE Transactions on Computers*, 62(6):1179–1192, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pomeranz:2010:EDS**

[PR10] I. Pomeranz and S. M. Reddy. Equivalence, dominance, and similarity relations between fault pairs and a fault pair collapsing process for fault diagnosis. *IEEE Transactions on Computers*, 59(2):150–158, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184814`.

**Prasad:2014:MDA**

[PR14] Abhinandan S. Prasad and Shrisha Rao. A mechanism design approach to resource procurement in cloud computing. *IEEE Transactions on Computers*, 63(1):17–30, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pontarelli:2013:LCC**

[PRBM13] Salvatore Pontarelli, Pedro Reviriego, Chris J. Bleakley, and Juan Antonio Maestro. Low complexity concurrent error detection for complex multiplication. *IEEE Transactions on Computers*, 62(9):1899–1903, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Perez-Resa:2019:SSE**

[PRGBSAC19] A. Pérez-Resa, M. Garcia-Bosque, C. Sánchez-Azqueta, and S. Celma. Self-synchronized encryption for physical layer in 10Gbps optical links. *IEEE Transactions on Computers*, 68 (6):899–911, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pontarelli:2016:PDP**

[PRM16] S. Pontarelli, P. Reviriego, and J. A. Maestro. Parallel *d*-pipeline: A cuckoo hashing implementation for increased throughput. *IEEE Transactions on Computers*, 65 (1):326–331, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Poudel:2019:MTU**

[PRM19] Prawar Poudel, Biswajit Ray, and Aleksandar Milenkovic. Microcontroller TRNGs using perturbed states of NOR flash memory cells. *IEEE Transactions on Computers*, 68 (2):307–313, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8443106/`.

**Pontarelli:2015:LDS**

[PROM15] S. Pontarelli, P. Reviriego, M. Ottavi, and J. A. Maestro. Low delay single symbol error correction codes based on Reed Solomon codes. *IEEE Transactions on Computers*, 64(5):1497–1501, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pudi:2017:MLF**

[PSL17] Vikramkumar Pudi, K. Sridharan, and Fabrizio Lombardi. Majority logic formulations for parallel adder designs at reduced delay and circuit complexity. *IEEE Transactions on Computers*, 66(10):1824–1830, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7909019/`.

**Patranabis:2017:PSK**

[PSM17] S. Patranabis, Y. Shrivastava, and D. Mukhopadhyay. Provably secure key-aggregate cryptosystems with broadcast aggregate keys for online data sharing on the cloud. *IEEE Transactions on Computers*, 66(5):891–904, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Psarras:2016:SNC**

[PSND16] Anastasios Psarras, Ioannis Seitanidis, Chrysostomos Nicopoulos, and Giorgos Dimitrakopoulos. ShortPath: A network-on-chip router with fine-grained pipeline bypassing. *IEEE Transactions on Computers*, 65(10):3136–3147, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pauwels:2012:CFG**

[PTD+12] Karl Pauwels, Matteo Tomasi, Javier Diaz, Eduardo Ros, and Marc M. Van Hulle. A comparison of FPGA and GPU for real-time phase-based optical flow, stereo, and local image features. *IEEE Transactions on Computers*, 61(7):999–1012, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pedram:2012:CTH**

[PvdGG12] Ardavan Pedram, Robert A. van de Geijn, and Andreas Gerstlauer. Codesign tradeoffs for high-performance, low-power linear algebra architectures. *IEEE Transactions on Computers*, 61(12):1724–1736, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Park:2014:MXM**

[PVKA14] Dongkook Park, Aniruddha Vaidya, Akhilesh Kumar, and Mani Azimi. MoDe-X: Microarchitecture of a layout-aware modular decoupled crossbar for on-chip interconnects. *IEEE Transactions on Computers*, 63(3):622–636, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Papadopoulos:2016:SMO**

[PWTS16] Lazaros Papadopoulos, Ivan Walulya, Philippas Tsigas, and Dimitrios Soudris. A systematic methodology for optimization of applications utilizing concurrent data structures. *IEEE Transactions on Computers*, 65(7):2019–2031, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Pao:2011:SSE**

[PWW+11] Derek Pao, Xing Wang, Xiaoran Wang, Cong Cao, and Yuesheng Zhu. String searching engine for virus scanning. *IEEE Transactions on Computers*, 60(11):1596–1609, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669261`.

**Pang:2019:PHS**

[PZZQ19] Ning Pang, Jifu Zhang, Chaowei Zhang, and Xiao Qin. Parallel hierarchical subspace clustering of categorical data. *IEEE Transactions on Computers*, 68(4):542–555, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8520776/`.

**Quislant:2013:LSL**

[QGPZ13] Ricardo Quislant, Eladio Gutierrez, Oscar Plata, and Emilio L. Zapata. LS-Sig: Locality-sensitive signatures for transactional memory. *IEEE Transactions on Computers*, 62(2):322–335, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qin:2010:CAL**

[QJM+10] Xiao Qin, Hong Jiang, A. Manzanares, Xiaojun Ruan, and Shu Yin. Communication-aware load balancing for parallel applications on clusters. *IEEE Transactions on Computers*, 59(1):42–52, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184810`.

**Qiu:2016:WMA**

[QLH+16] Keni Qiu, Qingan Li, Jingtong Hu, Weigong Zhang, and Chun Jason Xue. Write mode aware loop tiling for high performance low power volatile PCM in embedded systems. *IEEE Transactions on Computers*, 65(7):2313–2324, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qian:2011:AFT**

[QLR+11] Weikang Qian, Xin Li, Marc D. Riedel, Kia Bazargan, and David J. Lilja. An architecture for fault-tolerant computation with stochastic logic. *IEEE Transactions on Computers*, 60(1):93–105, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qiu:2015:PCM**

[QML+15] M. Qiu, Z. Ming, J. Li, K. Gai, and Z. Zong. Phase-change memory optimization for green cloud with genetic algorithm. *IEEE Transactions on Computers*, 64(12):3528–3540, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Quinones:2010:LRW**

[QPG10] E. Quiñones, J. Parcerisa, and A. González. Leveraging register windows to reduce physical registers to the bare minimum. *IEEE Transactions on Computers*, 59(12):1598–1610, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-

9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453344.

**Qian:2017:PPS**

[QQW+17] Jianwei Qian, Fudong Qiu, Fan Wu, Na Ruan, Guihai Chen, and Shaojie Tang. Privacy-preserving selective aggregation of online user behavior data. *IEEE Transactions on Computers*, 66 (2):326–338, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qiu:2016:LLM**

[QSYS16] Chenxi Qiu, Haiying Shen, Lei Yu, and Sohraab Soltani. Low-latency multi-flow cooperative broadcast in fading wireless networks. *IEEE Transactions on Computers*, 65(6):1802–1815, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qiu:2013:PNT**

[QWB+13] Qinru Qiu, Qing Wu, Morgan Bishop, Robinson E. Pino, and Richard W. Linderman. A parallel neuromorphic text recognition system and its implementation on a heterogeneous high-performance computing cluster. *IEEE Transactions on Computers*, 62(5):886–899, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qian:2015:MGL**

[QZC15] J. Qian, Q. Zhu, and H. Chen. Multi-granularity locality-sensitive Bloom filter. *IEEE Transactions on Computers*, 64(12):3500–3514, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Qi:2016:SPR**

[QZL+16] Saiyu Qi, Yuanqing Zheng, Mo Li, Li Lu, and Yunhao Liu. Secure and private RFID-enabled third-party supply chain systems. *IEEE Transactions on Computers*, 65(11):3413–3426, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rahimi:2014:AAG**

[RBG14] Abbas Rahimi, Luca Benini, and Rajesh K. Gupta. Application-adaptive guardbanding to mitigate static and dynamic variability. *IEEE Transactions on Computers*, 63(9):2160–2173, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Roy:2019:CMF**

[RBG+19] Debapriya Basu Roy, Shivam Bhasin, Sylvain Guilley, Annelie Heuser, Sikhar Patranabis, and Debdeep Mukhopadhyay. CC meets FIPS: A hybrid test methodology for first order side channel analysis. *IEEE Transactions on Computers*, 68(3):347–361, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8490742/`.

**Randolph:2015:SCO**

[RBIQ15] A. Randolph, H. Boucheneb, A. Imine, and A. Quintero. On synthesizing a consistent operational transformation approach. *IEEE Transactions on Computers*, 64 (4):1074–1089, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ratanaworabhan:2012:ERD**

[RBK+12] Paruj Ratanaworabhan, Martin Burtscher, Darko Kirovski, Benjamin Zorn, Rahul Nagpal, and Karthik Pattabiraman. Efficient runtime detection and toleration of asymmetric races. *IEEE Transactions on Computers*, 61(4):548–562, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Reviriego:2011:ODL**

[RBMO11] Pedro Reviriego, Chris Bleakley, Juan Antonio Maestro, and Anne O'Donnell. Offset DMR: a low overhead soft error detection and correction technique for transform-based convolution. *IEEE Transactions on Computers*, 60(10): 1511–1516, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=6008538`.

**Ripoll:2013:PSM**

[RBR13] Ismael Ripoll and Rafael Ballester-Ripoll. Period selection for minimal hyperperiod in periodic task systems. *IEEE Transactions on Computers*, 62(9): 1813–1822, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rezende:2015:RSU**

[RBRL15] C. Rezende, A. Boukerche, H. S. Ramos, and A. A. F. Loureiro. A reactive and scalable unicast solution for video streaming over VANETs. *IEEE Transactions on Computers*, 64 (3):614–626, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Richter:2014:OTP**

[RC14] Michael Richter and Krishnendu Chakrabarty. Optimization of test pin-count, test scheduling, and test access for NoC-based multicore SoCs. *IEEE Transactions on Computers*, 63 (3):691–702, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rahman:2014:CCO**

[RCC14] Musfiq Rahman, Bruce R. Childers, and Sangyeun Cho. COMeT+: Continuous online memory testing with multi-threading extension. *IEEE Transactions on Computers*, 63(7):1668–1681, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ros:2012:EMC**

[RCFP+12] Alberto Ros, Blas Cuesta, Ricardo Fernandez-Pascual, Maria E. Gomez, Manuel E. Acacio, Antonio Robles, Jose M. Garcia, and Jose Duato. Extending Magny-Cours cache coherence. *IEEE Transactions on Computers*, 61(5):593–606, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rehman:2016:CLS**

[RCK+16] S. Rehman, K. Chen, F. Kriebel, A. Toma, M. Shafique, J. Chen, and J. Henkel. Cross-layer software dependability on unreliable hardware. *IEEE Transactions on Computers*, 65(1):80–94, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Radojkovic:2016:TAM**

[RCM+16] P. Radojkovic, P. M. Carpenter, M. Moreto, V. Cakarevic, J. Verdu, A. Pajuelo, F. J. Cazorla, M. Nemirovsky, and M. Valero. Thread assignment in multicore/multithreaded processors: A statistical approach. *IEEE Transactions on Computers*, 65(1):256–269, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Reda:2011:ITT**

[RCN11] Sherief Reda, Ryan J. Cochran, and Abdullah Nazma Nowroz. Improved thermal tracking for processors using hard and soft sensor allocation techniques. *IEEE Transactions on Computers*, 60(6):841–851, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rottenstreich:2013:EWC**

[RCRK13] Ori Rottenstreich, Rami Cohen, Danny Raz, and Isaac Keslassy. Exact worst case TCAM rule expansion. *IEEE Transactions on Computers*, 62(6):1127–1140, June 2013. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Roohi:2018:NCN**

[RD18] Arman Roohi and Ronald F. DeMara. NV-clustering: Normally-off computing using non-volatile datapaths. *IEEE Transactions on Computers*, 67(7):949–959, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8263396/.

**Refaei:2010:ARM**

[RDEN10] M. T. Refaei, L. A. DaSilva, M. Eltoweissy, and T. Nadeem. Adaptation of reputation management systems to dynamic network conditions in ad hoc networks. *IEEE Transactions on Computers*, 59(5):707–719, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5406505.

**Redinbo:2011:SWS**

[Red11] G. Robert Redinbo. Systematic wavelet subcodes for data protection. *IEEE Transactions on Computers*, 60(6):904–909, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Redinbo:2014:CDC**

[Red14] G. Robert Redinbo. Correcting DFT codes with a modified Berlekamp–Massey algorithm and Kalman recursive syndrome extension. *IEEE Transactions on Computers*, 63(1):196–203, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Redinbo:2018:DCD**

[Red18] G. Robert Redinbo. Designing checksums for detecting errors in fast unitary transforms. *IEEE Transactions on Computers*, 67(4):566–572, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8039530/.

**Redinbo:2019:TPD**

[Red19] G. R. Redinbo. Tensor product DFT codes vs standard DFT codes. *IEEE Transactions on Computers*, 68(11):1678–1688, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rattanatamrong:2014:DSR**

[RF14] Prapaporn Rattanatamrong and Jose A. B. Fortes. Dynamic scheduling of real-time mixture-of-experts systems on limited resources. *IEEE*

*Transactions on Computers*, 63(7):1751–1764, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rashid:2015:MAP**

[RGK15] M. M. Rashid, I. Gondal, and J. Kamruzzaman. Mining associated patterns from wireless sensor networks. *IEEE Transactions on Computers*, 64(7):1998–2011, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ramamritham:2014:SAD**

[RHC+14] Krithi Ramamritham, Song Han, Deji Chen, Ming Xiong, Kam-Yiu Lam, and Aloysius K. Mok. Schedulability analysis of deferrable scheduling algorithms for maintaining real-time data freshness. *IEEE Transactions on Computers*, 63(4):979–994, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Roy:2018:HFB**

[RJV+18] Sujoy Sinha Roy, Kimmo Järvinen, Jo Vliegen, Frederik Vercauteren, and Ingrid Verbauwhede. HEPCloud: An FPGA-based multicore processor for FV somewhat homomorphic function evaluation. *IEEE Transactions on Computers*, 67(11):1637–1650, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8318681/`.

**Rodchenko:2018:TIE**

[RKN+18] Andrey Rodchenko, Christos Kotselidis, Andy Nisbet, Antoniu Pop, and Mikel Luján. Type information elimination from objects on architectures with tagged pointers support. *IEEE Transactions on Computers*, 67(1):130–143, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7962268/`.

**Rajendran:2015:ITV**

[RKR15] J. Rajendran, R. Karri, and G. S. Rose. Improving tolerance to variations in memristor-based applications using parallel memristors. *IEEE Transactions on Computers*, 64(3):733–746, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rodriguez:2019:AMP**

[RKT19] Gabriel Rodríguez, Mahmut T. Kandemir, and Juan Touriño. Affine modeling of program traces. *IEEE Transactions on Computers*, 68(2):294–300, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8408540/.

**Ren:2016:FAL**

[RKZ16] P. Ren, M. A. Kinsy, and N. Zheng. Fault-aware load-balancing routing for 2D-mesh and torus on-chip network topologies. *IEEE Transactions on Computers*, 65(3):873–887, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ramanujam:2013:RTO**

[RL13] Rohit Sunkam Ramanujam and Bill Lin. Randomized throughput-optimal oblivious routing for torus networks. *IEEE Transactions on Computers*, 62(3):561–574, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Reviriego:2019:TBO**

[RLRL19] P. Reviriego, S. Liu, O. Rottenstreich, and F. Lombardi. Two bit overlap: A class of double error correction one step majority logic decodable codes. *IEEE Transactions on Computers*, 68(5):798–803, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ryu:2018:CLP**

[RLSK18] Junhee Ryu, Dongeun Lee, Kang G. Shin, and Kyungtae Kang. ClusterFetch: A lightweight prefetcher for intensive disk reads. *IEEE Transactions on Computers*, 67(2):284–290, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8025580/.

**Ren:2015:DCM**

[RLX15] Xiaojiang Ren, Weifa Liang, and Wenzheng Xu. Data collection maximization in renewable sensor networks via time-slot scheduling. *IEEE Transactions on Computers*, 64(7):1870–1883, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rebeiro:2015:MAA**

[RM15a] C. Rebeiro and D. Mukhopadhyay. Micro-architectural analysis of time-driven cache attacks: Quest for the ideal implementation. *IEEE Transactions on Computers*, 64(3):778–790, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Reyhani-Masoleh:2015:CLL**

[RM15b] A. Reyhani-Masoleh. Comments on "Low-Latency Digit-Serial Systolic Double

Basis Multiplier over Using Subquadratic Toeplitz Matrix-Vector Product Approach". *IEEE Transactions on Computers*, 64 (4):1215–1216, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [CLL+14].

**Rodriguez:2015:ECP**

[RM15c] A. Rodriguez and F. Moreno. Evolutionary computing and particle filtering: A hardware-based motion estimation system. *IEEE Transactions on Computers*, 64(11): 3140–3152, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rafiev:2012:MRR**

[RMB+12] Ashur Rafiev, Andrey Mokhov, Frank P. Burns, Julian P. Murphy, Albert Koelmans, and Alex Yakovlev. Mixed radix Reed–Muller expansions. *IEEE Transactions on Computers*, 61(8):1189–1202, August 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rahmati:2013:CAP**

[RMB+13] Dara Rahmati, Srinivasan Murali, Luca Benini, Federico Angiolini, Giovanni De Micheli, and Hamid Sarbazi-Azad. Computing accurate performance bounds for best effort networks-on-chip. *IEEE Transactions on Computers*, 62(3):452–467, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rajapaksha:2015:VCA**

[RMC+15] N. Rajapaksha, A. Madanayake, R. J. Cintra, J. Adikari, and V. S. Dimitrov. VLSI computational architectures for the arithmetic cosine transform. *IEEE Transactions on Computers*, 64(9): 2708–2715, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Reyhani-Masoleh:2019:NMI**

[RMERM19] A. Reyhani-Masoleh, H. El-Razouk, and A. Monfared. New multiplicative inverse architectures using Gaussian normal basis. *IEEE Transactions on Computers*, 68(7):991–1006, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rajendran:2012:EEM**

[RMKR12] Jeyavijayan Rajendran, Harika Manem, Ramesh Karri, and Garrett S. Rose. An energy-efficient memristive threshold logic circuit. *IEEE Transactions on Computers*, 61(4):474–487, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ruj:2013:PTK**

[RNS13] Sushmita Ruj, Amiya Nayak, and Ivan Stojmenovic. Pairwise and triple key distribution in wireless sensor networks with applications. *IEEE Transactions on Computers*, 62(11): 2224–2237, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ratan:2011:SRQ**

[RO11] Rahul Ratan and A. Yavuz Oruc. Self-routing quantum sparse crossbar packet concentrators. *IEEE Transactions on Computers*, 60(10): 1390–1405, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5487508.

**Rodriguez-Olivares:2018:FBD**

[ROGHNB+18] N. A. Rodríguez-Olivares, A. Gómez-Hernández, L. Nava-Balanzar, H. Jiménez-Hernández, and J. A. Soto-Cajiga. FPGA-based data storage system on NAND flash memory in RAID 6 architecture for in-line pipeline inspection gauges. *IEEE Transactions on Computers*, 67 (7):1046–1053, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8315494/.

**Rafferty:2017:ELI**

[ROH17] Ciara Rafferty, Máire O'Neill, and Neil Hanley. Evaluation of large integer multiplication methods on hardware. *IEEE Transactions on Computers*, 66(8):1369–1382, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7869256/.

**Raghavendra:2016:PPB**

[RPM16] K. Raghavendra, Biswabandan Panda, and Madhu Mutyam. PBC: Prefetched blocks compaction. *IEEE Transactions on Computers*, 65(8):2534–2547, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Romano:2014:DEP**

[RQ14] Paolo Romano and Francesco Quaglia. Design and evaluation of a parallel invocation protocol for transactional applications over the Web. *IEEE Transactions on Computers*, 63 (2):317–334, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Roy:2011:SRP**

[RRK11] Soumyaroop Roy, Nagarajan Ranganathan, and Srinivas Katkoori. State-retentive power gating of register files in multicore processors featuring multithreaded in-order cores. *IEEE Transactions on Computers*, 60(11): 1547–1560, November 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669257.

**Ren:2016:DFC**

[RRS+16] P. Ren, X. Ren, S. Sane, M. A. Kinsy, and N. Zheng. A deadlock-free and connectivity-guaranteed methodology for achieving fault-tolerance in on-chip networks. *IEEE Transactions on Computers*, 65(2):353–366, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rodrigues:2010:ACU**

[RS10] T. K. Rodrigues and E. E. Swartzlander. Adaptive CORDIC: Using parallel angle recoding to accelerate rotations. *IEEE Transactions on Computers*, 59 (4):522–531, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374379.

**Ruhrup:2013:OCO**

[RS13] Stefan Ruhrup and Ivan Stojmenovic. Optimizing communication overhead while reducing path length in beaconless georouting with guaranteed delivery for wireless sensor networks. *IEEE Transactions on Computers*, 62(12):2440–2453, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rawat:2017:VIS**

[RS17] Hemendra Rawat and Patrick Schaumont. Vector instruction set extensions for efficient computation of Keccak. *IEEE Transactions on Computers*, 66(10):1778–1789, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7918507/.

**Roy:2019:EHP**

[RSG+19] I. Roy, A. Srivastava, M. Grimm, M. Nourian, M. Becchi, and S. Aluru. Evaluating high performance pattern matching on the automata processor. *IEEE Transactions on Computers*, 68(8):1201–1212, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Raha:2017:QCA**

[RSJR17] Arnab Raha, Soubhagya Sutar, Hrishikesh Jayakumar, and Vijay Raghunathan. Quality configurable approximate DRAM. *IEEE Transactions on Computers*, 66 (7):1172–1187, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07784741-abs.html`.

**Rodrigues:2018:CAS**

[RSN+18] Tiago Gama Rodrigues, Katsuya Suto, Hiroki Nishiyama, Nei Kato, and Katsuhiro Temma. Cloudlets activation scheme for scalable mobile edge computing with transmission power control and virtual machine migration. *IEEE Transactions on Computers*, 67(9): 1287–1300, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8322166/`.

**Rodrigues:2017:HMM**

[RSNK17] T. G. Rodrigues, K. Suto, H. Nishiyama, and N. Kato. Hybrid method for minimizing service delay in edge cloud computing through VM migration and transmission power control. *IEEE Transactions on Computers*, 66(5):810–819, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Reorda:2017:EDS**

[RSU17] Matteo Sonza Reorda, Luca Sterpone, and Anees Ullah. An error-detection and self-repairing method for dynamically and partially reconfigurable systems. *IEEE Transactions on Computers*, 66 (6):1022–1033, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7563888/`.

**Revol:2014:NRP**

[RT14] Nathalie Revol and Philippe Theveny. Numerical reproducibility and parallel computations: Issues for interval algorithms. *IEEE Transactions on Computers*, 63 (8):1915–1924, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ren:2018:LHA**

[RTL+18] Shiru Ren, Le Tan, Chunqi Li, Zhen Xiao, and Weijia Song. Leveraging hardware-assisted virtualization for deterministic replay on commodity multi-core processors. *IEEE Transactions on Computers*, 67(1):45–58, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-

9956 (electronic). URL http://ieeexplore.ieee.org/document/7982675/.

**Ramos:2019:APM**

[RTRM19] A. Ramos, R. G. Toral, P. Reviriego, and J. A. Maestro. An ALU protection methodology for soft processors on SRAM-based FPGAs. *IEEE Transactions on Computers*, 68(9): 1404–1410, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ramos:2018:EPR**

[RURM18] Alexis Ramos, Anees Ullah, Pedro Reviriego, and Juan Antonio Maestro. Efficient protection of the register file in soft-processors implemented on Xilinx FPGAs. *IEEE Transactions on Computers*, 67(2):299–304, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8008792/.

**Russinoff:2013:CFV**

[Rus13] David M. Russinoff. Computation and formal verification of SRT quotient and square root digit selection tables. *IEEE Transactions on Computers*, 62(5):900–913, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Radonjic:2013:ICC**

[RV13] Aleksandar Radonjic and Vladimir Vujicic. Integer codes correcting burst errors within a byte. *IEEE Transactions on Computers*, 62 (2):411–415, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ranieri:2015:NOT**

[RVC+15] J. Ranieri, A. Vincenzi, A. Chebira, D. Atienza, and M. Vetterli. Near-optimal thermal monitoring framework for many-core systems-on-chip. *IEEE Transactions on Computers*, 64(11): 3197–3209, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rahulamathavan:2016:UCA**

[RVH+16] Yogachandran Rahulamathavan, Suresh Veluru, Jinguang Han, Fei Li, Muttukrishnan Rajarajan, and Rongxing Lu. User collusion avoidance scheme for privacy-preserving decentralized key-policy attribute-based encryption. *IEEE Transactions on Computers*, 65(9):2939–2946, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Rahmani:2014:HPF**

[RVL+14] Amir-Mohammad Rahmani, Kameswar Rao Vaddina, Khalid Latif, Pasi Liljeberg, Juha Plosila, and Hannu Tenhunen. High-performance and fault-tolerant 3D NoC-bus hybrid architecture using ARB–NET-based adaptive monitoring platform. *IEEE Transactions on Computers*, 63 (3):734–747, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ramanathan:2018:SWC**

[RWC18] Nadesh Ramanathan, John Wickerson, and George A. Constantinides. Scheduling weakly consistent C concurrency for reconfigurable hardware. *IEEE Transactions on Computers*, 67 (7):992–1006, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8241825/`.

**Ren:2014:QAD**

[RWZZ14] Jinglei Ren, Yongwei Wu, Meiqi Zhu, and Weimin Zheng. Quatrain: Accelerating data aggregation between multiple layers. *IEEE Transactions on Computers*, 63(5):1207–1219, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ros:2015:ASC**

[RXC+15] A. Ros, P. Xekalakis, M. Cintra, M. E. Acacio, and J. M. Garcia. Adaptive selection of cache indexing bits for removing conflict misses. *IEEE Transactions on Computers*, 64(6):1534–1547, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ren:2019:PLL**

[RZPX19] Shiru Ren, Yunqi Zhang, Lichen Pan, and Zhen Xiao. Phantasy: Low-latency virtualization-based fault tolerance via asynchronous prefetching. *IEEE Transactions on Computers*, 68 (2):225–238, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8438984/`.

**Rajendran:2015:FAB**

[RZZ+15] J. Rajendran, Huan Zhang, Chi Zhang, G. S. Rose, Youngok Pino, O. Sinanoglu, and R. Karri. Fault analysis-based logic encryption. *IEEE Transactions on Computers*, 64(2):410–424, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sanchez:2011:ISR**

[SAR+11] Pedro Sanchez, Diego Alonso, Francisca Rosique, Barbara

Alvarez, and Juan A. Pastor. Introducing safety requirements traceability support in model-driven development of robotic applications. *IEEE Transactions on Computers*, 60 (8):1059–1071, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sarikaya:2010:UPM**

[SB10] R. Sarikaya and A. Buyuktosunoglu. A unified prediction method for predicting program behavior. *IEEE Transactions on Computers*, 59 (2):272–282, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184824`.

**S:2016:RSE**

[SB16] Anjan Kumar Pudi N S and Maryam Shojaei Baghini. Robust soft error tolerant CMOS latch configurations. *IEEE Transactions on Computers*, 65(9): 2820–2834, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Samanta:2018:CCB**

[SBB18] Jagannath Samanta, Jaydeb Bhaumik, and Soma Barman. Compact CA-based single byte error correcting codec. *IEEE Transactions on Computers*, 67 (2):291–298, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8010467/`.

**Samarah:2011:TAR**

[SBH11] Samer Samarah, Azzedine Boukerche, and Alexander Shema Habyalimana. Target association rules: a new behavioral patterns for point of coverage wireless sensor networks. *IEEE Transactions on Computers*, 60(6):879–889, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Srinivasagopalan:2012:OST**

[SBI12] Srivathsan Srinivasagopalan, Costas Busch, and S. S. Iyengar. An oblivious spanning tree for single-sink buy-at-bulk in low doubling-dimension graphs. *IEEE Transactions on Computers*, 61(5):700–712, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sarkar:2015:DFA**

[SBM15] S. Sarkar, S. Banik, and S. Maitra. Differential fault attack against grain family with very few faults and minimal assumptions. *IEEE Transactions on Computers*, 64(6):1647–1657, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Swierczynski:2018:BFI**

[SBMP18] Pawel Swierczynski, Georg T. Becker, Amir Moradi, and Christof Paar. Bitstream fault injections (BiFI)-automated fault attacks against SRAM-based FPGAs. *IEEE Transactions on Computers*, 67(3):348–360, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7809042/.

**Saponara:2014:DNI**

[SBP+14] Sergio Saponara, Tony Bacchillone, Esa Petri, Luca Fanucci, Riccardo Locatelli, and Marcello Coppola. Design of an NoC interface macrocell with hardware support of advanced networking functionalities. *IEEE Transactions on Computers*, 63(3):609–621, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sauer:2016:PSB**

[SBP16] Matthias Sauer, Bernd Becker, and Ilia Polian. PHAETON: A SAT-based framework for timing-aware path sensitization. *IEEE Transactions on Computers*, 65(6):1869–1881, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sun:2016:AOD**

[SBW+16] Zhenyu Sun, Xiuyuan Bi, Wenqing Wu, Sungjoo Yoo, and Hai (Helen) Li. Array organization and data management exploration in racetrack memory. *IEEE Transactions on Computers*, 65(4):1041–1054, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shamshiri:2011:MYC**

[SC11] Saeed Shamshiri and Kwang-Ting (Tim) Cheng. Modeling yield, cost, and quality of a spare-enhanced multicore chip. *IEEE Transactions on Computers*, 60(9): 1246–1259, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5710882.

**Saravanan:2018:PCE**

[SC18] Karthikeyan P. Saravanan and Paul M. Carpenter. PerfBound: Conserving energy with bounded overheads in on/off-based HPC interconnects. *IEEE Transactions on Computers*, 67(7): 960–974, July 2018. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8248748/`.

**Shin:2016:QDQ**

[SCJ+16a] Wongyu Shin, Jungwhan Choi, Jaemin Jang, Jinwoong Suh, Yongkee Kwon, Youngsuk Moon, Hongsik Kim, and Lee-Sup Kim. Q-DRAM: Quick-access DRAM with decoupled restoring from row-activation. *IEEE Transactions on Computers*, 65(7):2213–2227, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shin:2016:DLO**

[SCJ+16b] Wongyu Shin, Jungwhan Choi, Jaemin Jang, Jinwoong Suh, Youngsuk Moon, Yongkee Kwon, and Lee-Sup Kim. DRAM-latency optimization inspired by relationship between row-access time and refresh timing. *IEEE Transactions on Computers*, 65(10):3027–3040, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sengupta:2010:GTF**

[SCK10] S. Sengupta, M. Chatterjee, and K. A. Kwiat. A game theoretic framework for power control in wireless sensor networks. *IEEE Transactions on Computers*, 59(2):231–242, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5089316`.

**Sune:2010:CPA**

[SCNS10] V. Sune, J. A. Carrasco, H. Nabli, and B. Sericola. Comment on "Performability Analysis: a New Algorithm". *IEEE Transactions on Computers*, 59(1):137–138, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184816`.

**Shi:2012:VGA**

[SCSL12] Lin Shi, Hao Chen, Jianhua Sun, and Kenli Li. vCUDA: GPU-accelerated high-performance computing in virtual machines. *IEEE Transactions on Computers*, 61(6):804–816, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sha:2016:NDM**

[SCZ+16] Edwin H.-M. Sha, Xianzhang Chen, Qingfeng Zhuge, Liang Shi, and Weiwen Jiang. A new design of in-memory file system based on file virtual address framework. *IEEE Transactions on Computers*, 65(10):

2959–2972, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Singh:2013:AGA**

[SD13] Rajwinder Singh and Mayank Dave. Antecedence graph approach to checkpointing for fault tolerance in mobile agent systems. *IEEE Transactions on Computers*, 62 (2):247–258, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shirley:2014:CMC**

[SD14] C. G. Shirley and W. R. Daasch. Copula models of correlation: A DRAM case study. *IEEE Transactions on Computers*, 63(10): 2389–2401, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Streit:2018:PQK**

[SD18] Silvan Streit and Fabrizio De Santis. Post-quantum key exchange on ARMv8-A: a new hope for NEON made simple. *IEEE Transactions on Computers*, 67(11): 1651–1662, November 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8107588/`.

**Schley:2017:MLD**

[SDE+17] G. Schley, A. Dalirsani, M. Eggenberger, N. Hatami, H. J. Wunderlich, and M. Radetzki. Multi-layer diagnosis for fault-tolerant networks-on-chip. *IEEE Transactions on Computers*, 66(5):848–861, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sun:2019:APT**

[SDL+19] Chang-Ai Sun, Hepeng Dai, Huai Liu, Tsong Yueh Chen, and Kai-Yuan Cai. Adaptive partition testing. *IEEE Transactions on Computers*, 68(2):157–169, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8440117/`.

**Sethia:2012:CPE**

[SDMM12] Ankit Sethia, Ganesh Dasika, Trevor Mudge, and Scott Mahlke. A customized processor for energy efficient scientific computing. *IEEE Transactions on Computers*, 61(12):1711–1723, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Strollo:2011:EFH**

[SDP11] Antonio Giuseppe Maria Strollo, Davide De Caro, and

Nicola Petra. Elementary functions hardware implementation using constrained piecewise-polynomial approximations. *IEEE Transactions on Computers*, 60 (3):418–432, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Savino:2012:SRE**

[SDP+12] Alessandro Savino, Stefano Di Carlo, Gianfranco Politano, Alfredo Benso, Alberto Bosio, and Giorgio Di Natale. Statistical reliability estimation of microprocessor-based systems. *IEEE Transactions on Computers*, 61 (11):1521–1534, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Saqib:2015:PDT**

[SDP+15] F. Saqib, A. Dutta, J. Plusquellic, P. Ortiz, and M. S. Pattichis. Pipelined decision tree classification accelerator implementation in FPGA (DT-CAIF). *IEEE Transactions on Computers*, 64 (1):280–285, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sundaresan:2015:ZKG**

[SDZ15] S. Sundaresan, R. Doss, and Wanlei Zhou. Zero knowledge grouping proof protocol for RFID EPC C1G2 tags. *IEEE Transactions on Computers*, 64(10):2994–3008, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Savas:2014:SMQ**

[SEY14] Erkay Savas, Serdar Suer Erdem, and Kazim Yumbul. On selection of modulus of quadratic codes for the protection of cryptographic operations against fault attacks. *IEEE Transactions on Computers*, 63 (5):1182–1196, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Spinellis:2017:EUP**

[SF17] Diomidis Spinellis and Marios Fragkoulis. Extending Unix pipelines to DAGs. *IEEE Transactions on Computers*, 66(9):1547–1561, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7903579/`; `https://github.com/dspinellis/dgsh`.

**Shen:2012:EAA**

[SG12] Hong Shen and Longkun Guo. Efficient 2-approximation algorithms for computing 2-connected Steiner minimal networks. *IEEE Transactions on Computers*, 61(7):

954–968, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2013:EAA**

[SG13] Hong Shen and Longkun Guo. An eight-approximation algorithm for computing rooted three-vertex connected minimum Steiner networks. *IEEE Transactions on Computers*, 62(9): 1684–1693, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2012:LPC**

[SH12] Haiying Shen and Kai Hwang. Locality-preserving clustering and discovery of resources in wide-area distributed computational grids. *IEEE Transactions on Computers*, 61(4):458–473, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Schneider:2015:SDP**

[SHGW15] S. Schneider, M. Hirzel, B. Gedik, and Kun-Lung Wu. Safe data parallelism for general streaming. *IEEE Transactions on Computers*, 64(2):504–517, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Suzuki:2016:DSD**

[SHH+16] Jun Suzuki, Yoichi Hidaka, Junichi Higuchi, Yuki Hayashi, Masaki Kan, and Takashi Yoshikawa. Disaggregation and sharing of I/O devices in cloud data centers. *IEEE Transactions on Computers*, 65(10): 3013–3026, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sarikaya:2013:RAB**

[SIB13] Ruhi Sarikaya, Canturk Isci, and Alper Buyuktosunoglu. Runtime application behavior prediction using a statistical metric model. *IEEE Transactions on Computers*, 62(3):575–588, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shafique:2016:SPM**

[SIVH16] Muhammad Shafique, Anton Ivanov, Benjamin Vogel, and Jörg Henkel. Scalable power management for on-chip systems with malleable applications. *IEEE Transactions on Computers*, 65(11): 3398–3412, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shin:2017:BGL**

[SJC+17a] Wongyu Shin, Jaemin Jang, Jungwhan Choi, Jinwoong

Suh, and Lee-Sup Kim. Bank-group level parallelism. *IEEE Transactions on Computers*, 66(8): 1428–1434, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7845573/.

**Shin:2017:RLP**

[SJC+17b] Wongyu Shin, Jaemin Jang, Jungwhan Choi, Jinwoong Suh, Yongkee Kwon, Youngsuk Moon, and Lee-Sup Kim. Rank-level parallelism in DRAM. *IEEE Transactions on Computers*, 66 (7):1274–1280, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://www.computer.org/csdl/trans/tc/2017/07/07820157-abs.html.

**Sha:2018:TDE**

[SJD+18] Edwin Hsing-Mean Sha, Weiwen Jiang, Hailiang Dong, Zhulin Ma, Runyu Zhang, Xianzhang Chen, and Qingfeng Zhuge. Towards the design of efficient and consistent index structure with minimal write activities for non-volatile memory. *IEEE Transactions on Computers*, 67(3):432–448, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8046077/.

**Stojkovic:2010:IAC**

[SJS10] S. Stojkovic, D. Jankovic, and R. S. Stankovic. An improved algorithm for the construction of decision diagrams by rearranging and partitioning the input cube set. *IEEE Transactions on Computers*, 59(8):1105–1119, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396330.

**Shen:2014:DIM**

[SJS+14] Yunfu Shen, Yi Jin, Rong Shen, Junjie Peng, and Sheng Luo. Design and implementation of modified signed-digit adder. *IEEE Transactions on Computers*, 63(5):1134–1143, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sem-Jacobsen:2011:DFT**

[SJSLD11] Frank Olaf Sem-Jacobsen, Tor Skeie, Olav Lysne, and Jose Duato. Dynamic fault tolerance in fat trees. *IEEE Transactions on Computers*, 60(4):508–525, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sen:2019:SSA**

[SJVR19] S. Sen, S. Jain, S. Venkataramani, and A. Raghunathan. SparCE: Sparsity aware general-purpose core extensions to accelerate deep neural networks. *IEEE Transactions on Computers*, 68(6):912–925, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sinanoglu:2010:ISA**

[SKA10] O. Sinanoglu, M. H. Karaata, and B. AlBdaiwi. An inherently stabilizing algorithm for node-to-node routing over all shortest node-disjoint paths in hypercube networks. *IEEE Transactions on Computers*, 59(7):995–999, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5445078`.

**Seo:2014:IWC**

[SKC+14] Bumjoon Seo, Sooyong Kang, Jongmoo Choi, Jaehyuk Cha, Youjip Won, and Sungroh Yoon. IO workload characterization revisited: A data-mining approach. *IEEE Transactions on Computers*, 63(12): 3026–3038, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Salamat:2016:RRA**

[SKEB16] Ronak Salamat, Misagh Khayambashi, Masoumeh Ebrahimi, and Nader Bagherzadeh. A resilient routing algorithm with formal reliability analysis for partially connected 3D-NoCs. *IEEE Transactions on Computers*, 65(11): 3265–3279, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Salamat:2018:LAN**

[SKEB18] Ronak Salamat, Misagh Khayambashi, Masoumeh Ebrahimi, and Nader Bagherzadeh. LEAD: An adaptive 3D-NoC routing algorithm with queuing-theory based analytical verification. *IEEE Transactions on Computers*, 67(8):1153–1166, August 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8283712/`.

**Shafique:2016:CAL**

[SKH16] Muhammad Shafique, Muhammad Usman Karim Khan, and Jörg Henkel. Content-aware low-power configurable aging mitigation for SRAM memories. *IEEE Transactions on Computers*, 65(12):3617–3630, December 2016. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Soltiz:2013:MBN**

[SKM+13] Michael Soltiz, Dhireesha Kudithipudi, Cory Merkel, Garrett S. Rose, and Robinson E. Pino. Memristor-based neural logic blocks for nonlinearly separable functions. *IEEE Transactions on Computers*, 62(8):1597–1606, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shim:2014:SWC**

[SKM14] Hyotaek Shim, Jin-Soo Kim, and Seungryoul Maeng. System-wide cooperative optimization for NAND flash-based mobile systems. *IEEE Transactions on Computers*, 63(8):2052–2065, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sadiq:2015:SCO**

[SKPC15] U. Sadiq, M. Kumar, A. Passarella, and M. Conti. Service composition in opportunistic networks: A load and mobility aware solution. *IEEE Transactions on Computers*, 64(8):2308–2322, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sarkar:2010:NCH**

[SKPK10] S. Sarkar, G. R. Kulkarni, P. P. Pande, and A. Kalyanaraman. Network-on-chip hardware accelerators for biological sequence alignment. *IEEE Transactions on Computers*, 59(1):29–41, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5255227.

**Suzuki:2016:GGV**

[SKYK16] Yusuke Suzuki, Shinpei Kato, Hiroshi Yamada, and Kenji Kono. GPUvm: GPU virtualization at the hypervisor. *IEEE Transactions on Computers*, 65(9):2752–2766, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sakai:2013:DBE**

[SKZS13] Kazuya Sakai, Wei-Shinn Ku, Roger Zimmermann, and Min-Te Sun. Dynamic bit encoding for privacy protection against correlation attacks in RFID backward channel. *IEEE Transactions on Computers*, 62(1):112–123, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shieh:2010:WBM**

[SL10] Ming-Der Shieh and Wen-

Ching Lin. Word-based Montgomery modular multiplication algorithm for low-latency scalable architectures. *IEEE Transactions on Computers*, 59(8): 1145–1151, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5441286.

**Shen:2013:LCM**

[SL13] Haiying Shen and Guoxin Liu. A lightweight and cooperative multifactor considered file replication method in structured P2P systems. *IEEE Transactions on Computers*, 62(11):2115–2130, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2014:LSN**

[SL14a] Haiying Shen and Ze Li. Leveraging social networks for effective spam filtering. *IEEE Transactions on Computers*, 63(11):2743–2759, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shin:2014:PJE**

[SL14b] Kang G. Shin and Jinkyu Lee. Preempt a job or not in EDF scheduling of uniprocessor systems. *IEEE Transactions on Computers*, 63(5):1197–1206, May 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2015:SIB**

[SLC15a] Haiying Shen, Guoxin Liu, and H. Chandler. Swarm intelligence based file replication and consistency maintenance in structured P2P file sharing systems. *IEEE Transactions on Computers*, 64(10):2953–2967, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2015:SSA**

[SLC+15b] Haiying Shen, Jinwei Liu, Kang Chen, Jianwei Liu, and S. Moyer. SCPS: A social-aware distributed cyber-physical human-centric search engine. *IEEE Transactions on Computers*, 64(2):518–532, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2015:CEF**

[SLL15] Haiying Shen, Yuhua Lin, and Ting Li. Combining efficiency, fidelity, and flexibility in resource information services. *IEEE Transactions on Computers*, 64 (2):353–367, February 2015. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Shen:2015:KSO**

[SLLG15] Haiying Shen, Ze Li, Jinwei Liu, and J. E. Grant. Knowledge sharing in the online social network of Yahoo! answers and its implications. *IEEE Transactions on Computers*, 64 (6):1715–1728, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Schaumont:2018:SSS**

[SLPB18] Patrick Schaumont, Ruby Lee, Ronald Perez, and Guido Bertoni. Special section on secure computer architectures. *IEEE Transactions on Computers*, 67 (3):305–306, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shi:2012:LBM**

[SLS+12] Lei Shi, Bin Liu, Changhua Sun, Zhengyu Yin, Laxmi N. Bhuyan, and H. Jonathan Chao. Load-balancing multipath switching system with flow slice. *IEEE Transactions on Computers*, 61 (3):350–365, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shang:2015:NPE**

[SLZX15] Y. Shang, D. Li, J. Zhu, and M. Xu. On the network power effectiveness of data center architectures. *IEEE Transactions on Computers*, 64(11):3237–3248, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Salkhordeh:2019:AMP**

[SMA19] R. Salkhordeh, O. Mutlu, and H. Asadi. An analytical model for performance and lifetime estimation of hybrid DRAM–NVM main memories. *IEEE Transactions on Computers*, 68 (8):1114–1130, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sanchez-Macian:2019:EIT**

[SMAR+19] Alfonso Sánchez-Macián, Luis Alberto Aranda, Pedro Reviriego, Vahdaneh Kiani, and Juan Antonio Maestro. Enhancing instruction TLB resilience to soft errors. *IEEE Transactions on Computers*, 68 (2):214–224, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8488602/`.

**Sarkar:2015:PAI**

[SMB+15] S. Sarkar, S. Misra, B. Bandyopadhyay, C. Chakraborty, and M. S. Obaidat. Performance analysis of IEEE 802.15.6 MAC protocol under non-ideal channel conditions and saturated traffic regime. *IEEE Transactions on Computers*, 64(10): 2912–2925, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sahoo:2018:MBA**

[SMCN18] Durga Prasad Sahoo, Debdeep Mukhopadhyay, Rajat Subhra Chakraborty, and Phuong Ha Nguyen. A multiplexer-based arbiter PUF composition with enhanced reliability and security. *IEEE Transactions on Computers*, 67 (3):403–417, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8025790/.

**Shreejith:2017:VHP**

[SME+17] Shanker Shreejith, Philipp Mundhenk, Andreas Ettner, Suhaib A. Fahmy, Sebastian Steinhorst, Martin Lukasiewycz, and Samarjit Chakraborty. VEGa: A high performance vehicular Ethernet gateway on hybrid FPGA. *IEEE Transactions on Computers*, 66(10): 1790–1803, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7917319/.

**Stefan:2014:DTN**

[SMG14] Radu Andrei Stefan, Anca Molnos, and Kees Goossens. dAElite: A TDM NoC supporting QoS, multicast, and fast connection set-up. *IEEE Transactions on Computers*, 63(3):583–594, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Seyedzadeh:2016:IBF**

[SMK+16] Seyed Mohammad Seyedzadeh, Rakan Maddah, Donald Kline, Alex K. Jones, and Rami Melhem. Improving bit flip reduction for biased and random data. *IEEE Transactions on Computers*, 65(11):3345–3356, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shamim:2017:WIF**

[SMN+17] Md Shahriar Shamim, Naseef Mansoor, Rounak Singh Narde, Vignesh Kothandapani, Amlan Ganguly, and Jayanti Venkataraman. A wireless interconnection framework for seamless inter and intra-chip communication in multichip systems. *IEEE*

*Transactions on Computers*, 66(3):389–402, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Saha:2016:UBE**

[SMP16] B. K. Saha, S. Misra, and S. Pal. Utility-based exploration for performance enhancement in opportunistic mobile networks. *IEEE Transactions on Computers*, 65(4):1310–1322, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sanchez-Macian:2017:CMK**

[SMRM17] Alfonso Sánchez-Macián, Pedro Reviriego, and Juan Antonio Maestro. Combined modular key and data error protection for content-addressable memories. *IEEE Transactions on Computers*, 66(6):1085–1090, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7763836/`.

**Sanchez-Macian:2017:SET**

[SMRML17] Alfonso Sánchez-Macián, Pedro Reviriego, Juan Antonio Maestro, and Shanshan Liu. Single event transient tolerant Bloom filter implementations. *IEEE Transactions on Computers*, 66(10): 1831–1836, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7921607/`.

**Sarood:2012:XCL**

[SMTK12] Osman Sarood, Phil Miller, Ehsan Totoni, and Laxmikant V. Kale. 'cood' load balancing for high performance computing data centers. *IEEE Transactions on Computers*, 61(12):1752–1764, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shi:2016:GCR**

[SN16] Xiaobing Shi and Nicola Nicolici. Generating cyclic-random sequences in a constrained space for in-system validation. *IEEE Transactions on Computers*, 65(12): 3676–3686, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Skitsas:2016:DEO**

[SNM16] M. A. Skitsas, C. A. Nicopoulos, and M. K. Michael. DaemonGuard: Enabling O/S-orchestrated fine-grained software-based selective-testing in multi-/many-core microprocessors. *IEEE Transactions on Computers*, 65(5):1453–1466, May 2016. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Seong:2010:HBM**

[SNY+10] Yoon Jae Seong, Eyee Hyun Nam, Jin Hyuk Yoon, Hongseok Kim, Jin yong Choi, Sookwan Lee, Young Hyun Bae, Jaejin Lee, Yookun Cho, and Sang Lyul Min. Hydra: a block-mapped parallel flash memory solid-state disk architecture. *IEEE Transactions on Computers*, 59(7):905–921, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5432160.

**Shukla:2010:MQS**

[SO10] M. K. Shukla and A. Y. Oruc. Multicasting in quantum switching networks. *IEEE Transactions on Computers*, 59(6):735–747, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416694.

**Salvador:2013:SRE**

[SOM+13] Ruben Salvador, Andres Otero, Javier Mora, Eduardo de la Torre, Teresa Riesgo, and Lukas Sekanina. Self-reconfigurable evolvable hardware system for adaptive image processing. *IEEE Transactions on Computers*, 62(8):1481–1493, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sousa:2015:RSE**

[Sou15] L. Sousa. $2^n$ RNS scalers for extended 4-moduli sets. *IEEE Transactions on Computers*, 64(12):3322–3334, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Simao:2010:CCT**

[SP10] Adenilso Simao and Alexandre Petrenko. Checking completeness of tests for finite state machines. *IEEE Transactions on Computers*, 59(8):1023–1032, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396326.

**Sideris:2012:CEP**

[SP12] Isidoros Sideris and Kiamal Pekmestzi. Cost effective protection techniques for TCAM memory arrays. *IEEE Transactions on Computers*, 61(12):1778–1788, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sakellariou:2016:ASL**

[SP16] Panagiotis Sakellariou and Vassilis Paliouras. Application-specific low-power multipliers. *IEEE Transactions on Computers*, 65(10):2973–2985, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Santos:2016:DDD**

[SPC+16] I. L. Santos, L. Pirmez, L. R. Carmo, P. F. Pires, F. C. Delicato, S. U. Khan, and A. Y. Zomaya. A decentralized damage detection system for wireless sensor and actuator networks. *IEEE Transactions on Computers*, 65(5):1363–1376, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shin:2018:EEP**

[SPC+18] Ho Hyun Shin, Young Min Park, Duheon Choi, Byoung Jin Kim, Dae-Hyung Cho, and Eui-Young Chung. EXTREME: Exploiting page table for reducing refresh power of 3D-stacked DRAM memory. *IEEE Transactions on Computers*, 67 (1):32–44, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7968377/.

**Sterpone:2013:NFT**

[SPH13] Luca Sterpone, Mario Pormann, and Jens Hagemeyer. A novel fault tolerant and runtime reconfigurable platform for satellite payload processing. *IEEE Transactions on Computers*, 62 (8):1508–1525, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shih:2015:RFN**

[SPTC15] Y. Shih, A. Pang, M. Tsai, and C. Chai. A rewarding framework for network resource sharing in co-channel hybrid access femtocell networks. *IEEE Transactions on Computers*, 64(11): 3079–3090, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sun:2015:REA**

[SQJ+15] Hui Sun, Xiao Qin, Hong Jiang, Jianzhong Huang, and Changsheng Xie. RB-Explorer: An accurate and practical approach to write amplification measurement for SSDs. *IEEE Transactions on Computers*, 64 (4):1133–1148, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shukla:2014:LLH**

[SR14] R. Shukla and K. C. Ray. Low latency hybrid

CORDIC algorithm. *IEEE Transactions on Computers*, 63(12):3066–3078, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Salehin:2015:SMP**

[SRCbL+15] K. M. Salehin, R. Rojas-Cessa, Chuan bi Lin, Ziqian Dong, and T. Kijkanjanarat. Scheme to measure packet processing time of a remote host through estimation of end-link capacity. *IEEE Transactions on Computers*, 64(1):205–218, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sanyal:2010:ETL**

[SRCK10] A. Sanyal, A. Rastogi, Wei Chen, and S. Kundu. An efficient technique for leakage current estimation in nanoscaled CMOS circuits incorporating self-loading effects. *IEEE Transactions on Computers*, 59(7):922–932, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5441289`.

**Sudan:2012:TMI**

[SRHC12] Kshitij Sudan, Karthick Rajamani, Wei Huang, and John B. Carter. Tiered memory: An iso-power memory architecture to address the memory power wall. *IEEE Transactions on Computers*, 61(12):1697–1710, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Srinivasan:2010:ARC**

[Sri10] Sudarshan K. Srinivasan. Automatic refinement checking of pipelines with out-of-order execution. *IEEE Transactions on Computers*, 59(8):1138–1144, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396327`.

**Shafique:2017:AGP**

[SRK+17] Muhammad Shafique, Semeen Rehman, Florian Kriebel, Muhammad Usman Karim Khan, Bruno Zatt, Arun Subramaniyan, Bruno Boessio Vizzotto, and Jörg Henkel. Application-guided power-efficient fault tolerance for H.264 context adaptive variable length coding. *IEEE Transactions on Computers*, 66(4):560–574, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shabani:2016:LAS**

[SRR+16] Hesam Shabani, Arman Roohi, Akram Reza, Midia Reshadi, Nader Bagherzadeh, and Ronald F. DeMara. Loss-aware switch design and non-blocking detection algorithm for intra-chip scale photonic interconnection networks. *IEEE Transactions on Computers*, 65 (6):1789–1801, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Swartzlander:2012:FIF**

[SS12] Earl E. Swartzlander and Hani H. M. Saleh. FFT implementation with fused floating-point operations. *IEEE Transactions on Computers*, 61(2):284–288, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669293.

**Schuiki:2019:SNM**

[SSGB19] Fabian Schuiki, Michael Schaffner, Frank K. Gürkaynak, and Luca Benini. A scalable near-memory architecture for training deep neural networks on large in-memory datasets. *IEEE Transactions on Computers*, 68(4):484–497, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8502059/.

**Seol:2018:ERF**

[SSJ+18] Hoseok Seol, Wongyu Shin, Jaemin Jang, Jungwhan Choi, Hakseung Lee, and Lee-Sup Kim. Elaborate refresh: A fine granularity retention management for deep submicron DRAMs. *IEEE Transactions on Computers*, 67(10): 1403–1415, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8326524/.

**Sakai:2016:NCS**

[SSKL16] K. Sakai, M. Sun, W. Ku, and T. H. Lai. A novel coding scheme for secure communications in distributed RFID systems. *IEEE Transactions on Computers*, 65 (2):409–421, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sang:2012:ERD**

[SST12] Yingpeng Sang, Hong Shen, and Hui Tian. Effective reconstruction of data perturbed by random projections. *IEEE Transactions on Computers*, 61(1):101–117, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shang:2012:TET**

[SSW12] Pengju Shang, Saba Sehrish, and Jun Wang. TRAID: Exploiting temporal redundancy and spatial redundancy to boost transaction processing systems performance. *IEEE Transactions on Computers*, 61(4):517–529, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shukla:2011:GEI**

[ST11a] Sandeep K. Shukla and Jean-Pierre Talpin. Guest Editors' introduction: Special section on science of design for safety critical systems. *IEEE Transactions on Computers*, 60(8):1057–1058, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Song:2011:TAF**

[ST11b] Haoyu Song and Jonathan S. Turner. Toward advocacy-free evaluation of packet classification algorithms. *IEEE Transactions on Computers*, 60(5):723–733, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Skoufis:2012:OFD**

[ST12] Michael N. Skoufis and Spyros Tragoudas. An on-line failure detection method for data buses using multithreshold receiving logic. *IEEE Transactions on Computers*, 61(2):187–198, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sfikas:2016:TNC**

[ST16] Yiorgos Sfikas and Yiorgos Tsiatouhas. Testing neighbouring cell leakage and transition induced faults in DRAMs. *IEEE Transactions on Computers*, 65(7):2339–2345, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shu:2017:CST**

[ST17] Wei Shu and Nian-Feng Tzeng. Compressed sharer tracking and relinquishment coherence for superior directory efficiency of chip multiprocessors. *IEEE Transactions on Computers*, 66(11): 1975–1981, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7911232/`.

**Sasaki:2018:TBD**

[ST18a] Yu Sasaki and Yosuke Todo. Tight bounds of differentially and linearly active S-boxes and division property of Lilliput. *IEEE Transactions on Computers*, 67(5):717–732, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8126222/.

**Shu:2018:NNU**

[ST18b] Wei Shu and Nian-Feng Tzeng. NUDA: Non-uniform directory architecture for scalable chip multiprocessors. *IEEE Transactions on Computers*, 67(5):740–747, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8107506/.

**Stewart:2014:IND**

[Ste14] I. A. Stewart. Interconnection networks of degree three obtained by pruning two-dimensional tori. *IEEE Transactions on Computers*, 63(10):2473–2486, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shahverdi:2017:LSC**

[STE17] Aria Shahverdi, Mostafa Taha, and Thomas Eisenbarth. Lightweight side channel resistance: Threshold implementations of Simon. *IEEE Transactions on Computers*, 66(4):661–671, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Soteriou:2016:HAT**

[STK16] V. Soteriou, T. Theocharides, and E. Kakoulli. A holistic approach towards intelligent hotspot prevention in network-on-chip-based multicores. *IEEE Transactions on Computers*, 65(3):819–833, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sheikh:2015:CFD**

[STR15] R. Sheikh, J. Tuck, and E. Rotenberg. Control-flow decoupling: An approach for timely, non-speculative branching. *IEEE Transactions on Computers*, 64(8):2182–2203, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sabarimuthu:2018:AMR**

[SV18] Jasmine Madonna Sabarimuthu and T. G. Venkatesh. Analytical miss rate calculation of L2 cache from the RD profile of L1 cache. *IEEE Transactions on Computers*, 67(1):9–15, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7970187/.

**Sridhar:2014:ICT**

[SVAB14] A. Sridhar, A. Vincenzi, D. Atienza, and T. Brunschwiler. 3D-ICE: A compact thermal model for early-stage design of liquid-cooled ICs. *IEEE Transactions on Computers*, 63(10):2576–2589, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Savino:2018:RCL**

[SVD18] Alessandro Savino, Alessandro Vallero, and Stefano Di Carlo. ReDO: Cross-layer multi-objective design-exploration framework for efficient soft error resilient systems. *IEEE Transactions on Computers*, 67(10):1462–1477, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8323226/`.

**Shi:2019:LWW**

[SWF+19] Y. Shi, W. Wei, H. Fan, M. H. Au, and X. Luo. A light-weight white-box encryption scheme for securing distributed embedded devices. *IEEE Transactions on Computers*, 68(10):1411–1427, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shoufan:2010:NCA**

[SWM+10] A. Shoufan, T. Wink, H. G. Molter, S. A. Huss, and E. Kohnert. A novel crypto-processor architecture for the McEliece public-key cryptosystem. *IEEE Transactions on Computers*, 59(11):1533–1546, November 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5477413`.

**Sun:2011:NAP**

[SWWC11] Hung-Min Sun, Hsun Wang, King-Hang Wang, and Chien-Ming Chen. A native APIs protection mechanism in the kernel mode against malicious code. *IEEE Transactions on Computers*, 60(6):813–823, June 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shang:2011:NPI**

[SWZG11] Pengju Shang, Jun Wang, Huijun Zhu, and Peng Gu. A new placement-ideal layout for multiway replication storage system. *IEEE Transactions on Computers*, 60(8):1142–1156, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sun:2015:ERE**

[SWZG15] Y. Sun, T. Wu, G. Zhao,

and M. Guizani. Efficient rule engine for smart building systems. *IEEE Transactions on Computers*, 64 (6):1658–1669, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2012:LCG**

[SX12] Haiying Shen and Cheng-Zhong Xu. Leveraging a compound graph-based DHT for multi-attribute range queries with performance analysis. *IEEE Transactions on Computers*, 61(4):433–447, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Song:2014:ARP**

[SXCL14] Weijia Song, Zhen Xiao, Qi Chen, and Haipeng Luo. Adaptive resource provisioning for the cloud using online bin packing. *IEEE Transactions on Computers*, 63(11): 2647–2660, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Saifullah:2015:EEC**

[SXLC15] A. Saifullah, You Xu, Chenyang Lu, and Yixin Chen. End-to-end communication delay analysis in industrial wireless networks. *IEEE Transactions on Computers*, 64(5):1361–1374, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shafik:2018:RPC**

[SYD18] Rishad Shafik, Alex Yakovlev, and Shidhartha Das. Real-power computing. *IEEE Transactions on Computers*, 67(10):1445–1461, October 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8330023/`.

**Son:2017:OOF**

[SYH17] Yongseok Son, Heon Young Yeom, and Hyuck Han. Optimizing I/O operations in file systems for fast storage devices. *IEEE Transactions on Computers*, 66 (6):1071–1084, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7769226/`.

**Suei:2014:EAF**

[SYK14] Pei-Lun Suei, Mi-Yen Yeh, and Tei-Wei Kuo. Endurance-aware flash-cache management for storage servers. *IEEE Transactions on Computers*, 63(10):2416–2430, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Sui:2014:DAH**

[SZDL14] Yan Sui, Xukai Zou, Eliza Y. Du, and Feng Li. Design and analysis of a highly user-friendly, secure, privacy-preserving, and revocable authentication method. *IEEE Transactions on Computers*, 63(4):902–916, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Shen:2018:ESM**

[SZG+18] Jiajie Shen, Kai Zhang, Jiazhen Gu, Yangfan Zhou, and Xin Wang. Efficient scheduling for multi-block updates in erasure coding based storage systems. *IEEE Transactions on Computers*, 67(4):573–581, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8094270/.

**Sun:2016:SCB**

[SZL+16] Guangyu Sun, Chao Zhang, Peng Li, Tao Wang, and Yiran Chen. Statistical cache bypassing for non-volatile memory. *IEEE Transactions on Computers*, 65(11):3427–3440, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Si:2014:GDR**

[SZS14] Weisheng Si, Albert Y. Zomaya, and Selvadurai Selvakennedy. A geometric deployment and routing scheme for directional wireless mesh networks. *IEEE Transactions on Computers*, 63(6):1323–1335, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Su:2016:SDP**

[SZW+16] Maomeng Su, Lei Zhang, Yongwei Wu, Kang Chen, and Keqin Li. Systematic data placement optimization in multi-cloud storage for complex requirements. *IEEE Transactions on Computers*, 65(6):1964–1977, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Song:2019:EEA**

[SZZ+19] Weining Song, Yang Zhou, Mengying Zhao, Lei Ju, Chun Jason Xue, and Zhiping Jia. EMC: Energy-aware morphable cache design for non-volatile processors. *IEEE Transactions on Computers*, 68(4):498–509, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8519610/.

**Tarihi:2016:HNV**

[TAH+16] Mojtaba Tarihi, Hossein Asadi, Alireza Haghdoost, Mohammad Arjomand, and

Hamid Sarbazi-Azad. A hybrid non-volatile cache design for solid-state drives using comprehensive I/O characterization. *IEEE Transactions on Computers*, 65 (6):1678–1691, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsoumanis:2016:PEM**

[TAM+16] K. Tsoumanis, N. Axelos, N. Moschopoulos, G. Zervakis, and K. Pekmestzi. Pre-encoded multipliers based on non-redundant radix-4 signed-digit encoding. *IEEE Transactions on Computers*, 65(2):670–676, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2015:HSB**

[TB15] Yi Tang and N. W. Bergmann. A hardware scheduler based on task queues for FPGA-based embedded real-time systems. *IEEE Transactions on Computers*, 64 (5):1254–1267, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsai:2017:ASR**

[TBC+17] Wei-Yu Tsai, Davis R. Barch, Andrew S. Cassidy, Michael V. DeBole, Alexander Andreopoulos, Bryan L. Jackson, Myron D. Flickner, John V. Arthur, Dharmendra S. Modha, John Sampson, and Vijaykrishnan Narayanan. Always-on speech recognition using TrueNorth, a reconfigurable, neurosynaptic processor. *IEEE Transactions on Computers*, 66 (6):996–1007, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7750640/.

**Tian:2014:SDO**

[TC14] Xiaohua Tian and Yu Cheng. A scalable destination-oriented multicast protocol with incremental deployability. *IEEE Transactions on Computers*, 63(4):793–806, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tay:2016:NIM**

[TC16] T. Fatt Tay and C. Chang. A non-iterative multiple residue digit error detection and correction algorithm in RRNS. *IEEE Transactions on Computers*, 65 (2):396–408, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsai:2018:STC**

[TCHL18] Ting-Hao Tsai, Ya-Shu Chen, Xue-Xin He, and Cheng-Yu Li. STEM:

A thermal-constrained real-time scheduling for 3D heterogeneous-ISA multicore processors. *IEEE Transactions on Computers*, 67 (6):874–889, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8214221/`.

**Tran:2018:SIS**

[TCK+18] Kim-Anh Tran, Trevor E. Carlson, Konstantinos Koukos, Magnus Själander, Vasileios Spiliopoulos, Stefanos Kaxiras, and Alexandra Jimborean. Static instruction scheduling for high performance on limited hardware. *IEEE Transactions on Computers*, 67(4):513–527, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8094900/`.

**Tsao:2015:EVB**

[TCYH15] C. Tsao, Y. Chang, M. Yang, and P. Huang. Efficient victim block selection for flash storage devices. *IEEE Transactions on Computers*, 64(12):3444–3460, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsai:2016:TSE**

[TFCY16] T. H. Tsai, L. F. Fan, Y. S. Chen, and T. S. Yao. Triple speed: Energy-aware real-time task synchronization in homogeneous multicore systems. *IEEE Transactions on Computers*, 65 (4):1297–1309, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsen:2011:HDB**

[TGNSC11] Charles Tsen, Sonia Gonzalez-Navarro, Michael J. Schulte, and Katherine Compton. Hardware designs for binary integer decimal-based rounding. *IEEE Transactions on Computers*, 60(5): 614–627, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tavli:2011:EER**

[TH11] Bulent Tavli and Wendi B. Heinzelman. Energy-efficient real-time multicast routing in mobile ad hoc networks. *IEEE Transactions on Computers*, 60(5):707–722, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ttofis:2013:EDH**

[THGT13] C. Ttofis, S. Hadjitheophanous, A. S. Georghiades, and T. Theocharides. Edge-directed hardware architecture for real-time disparity map computation. *IEEE*

*Transactions on Computers*, 62(4):690–704, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tran:2014:QBS**

[THM+14] Hai Anh Tran, S. Hoceini, A. Mellouk, J. Perez, and S. Zeadally. QoE-based server selection for content distribution networks. *IEEE Transactions on Computers*, 63(11):2803–2815, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Thomasian:2012:CDH**

[Tho12] Alexander Thomasian. Comment on “DACO: A High Performance Disk Architecture”. *IEEE Transactions on Computers*, 61(4):588–590, April 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [LS10a].

**Thornton:2015:SIU**

[Tho15] M. A. Thornton. Simulation and implication using a transfer function model for switching logic. *IEEE Transactions on Computers*, 64(12):3580–3590, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Todo:2018:CAN**

[TIHM18] Yosuke Todo, Takanori Isobe, Yonglin Hao, and Willi Meier. Cube attacks on non-blackbox polynomials based on division property. *IEEE Transactions on Computers*, 67(12):1720–1736, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8357953/`.

**Tan:2015:DDS**

[TJH+15] Zhiyuan Tan, A. Jamdagni, Xiangjian He, P. Nanda, Ren Ping Liu, and Jiankun Hu. Detection of denial-of-service attacks based on computer vision techniques. *IEEE Transactions on Computers*, 64(9):2519–2533, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2017:NNI**

[TJX+17] Guoming Tang, Weixiang Jiang, Zhifeng Xu, Fangming Liu, and Kui Wu. NIPD: Non-intrusive power disaggregation in legacy datacenters. *IEEE Transactions on Computers*, 66(2):312–325, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tziritas:2014:SGA**

[TKL+14] N. Tziritas, S. U. Khan, T. Loukopoulos, S. Lalis, Cheng-Zhong Xu, and P. Lampsas. Single and group agent migration: Algorithms, bounds, and optimality issues. *IEEE Transactions on Computers*, 63(12): 3143–3161, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ttofis:2016:LCR**

[TKT16] Christos Ttofis, Christos Kyrkou, and Theocharis Theocharides. A low-cost real-time embedded stereo vision system for accurate disparity estimation based on guided image filtering. *IEEE Transactions on Computers*, 65(9): 2678–2693, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Trefzer:2017:HSE**

[TLB+17] Martin A. Trefzer, David M. R. Lawson, Simon J. Bale, James A. Walker, and Andy M. Tyrrell. Hierarchical strategies for efficient fault recovery on the reconfigurable PAnDA device. *IEEE Transactions on Computers*, 66(6):930–945, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7756359/.

**Turakhia:2017:TPE**

[TLGM17] Yatish Turakhia, Guangshuo Liu, Siddharth Garg, and Diana Marculescu. Thread progress equalization: Dynamically adaptive power-constrained performance optimization of multi-threaded applications. *IEEE Transactions on Computers*, 66 (4):731–744, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2016:ASS**

[TLH+16] Shaohua Tang, Xiaoyu Li, Xinyi Huang, Yang Xiang, and Lingling Xu. Achieving simple, secure and efficient hierarchical access control in cloud computing. *IEEE Transactions on Computers*, 65(7):2325–2331, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsai:2012:SRT**

[TLL12] Wei-Lin Tsai, Wei-Chih Liu, and James Chien-Mo Li. Structural reduction techniques for logic-chain bridging fault diagnosis. *IEEE Transactions on Computers*, 61(7):928–938, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2013:AXP**

[TLL+13] Jie Tang, Shaoshan Liu, Chen Liu, Zhimin Gu, and Jean-Luc Gaudiot. Acceleration of XML parsing through prefetching. *IEEE Transactions on Computers*, 62(8):1616–1628, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2017:RTM**

[TLP17] Minghua Tang, Xiaola Lin, and Maurizio Palesi. The repetitive turn model for adaptive routing. *IEEE Transactions on Computers*, 66(1):138–146, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2018:SRA**

[TLP18] Minghua Tang, Jing Lin, and Maurizio Palesi. The suboptimal routing algorithm for 2D mesh network. *IEEE Transactions on Computers*, 67(5):704–716, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8118167/`.

**Tang:2011:NSD**

[TLZV11] Xiaoyong Tang, Kenli Li, Zeng Zeng, and Bharadwaj Veeravalli. A novel security-driven scheduling algorithm for precedence-constrained tasks in heterogeneous distributed systems. *IEEE Transactions on Computers*, 60(7):1017–1029, July 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsoutsos:2018:EDM**

[TM18] Nektarios Georgios Tsoutsos and Michail Maniatakos. Efficient detection for malicious and random errors in additive encrypted computation. *IEEE Transactions on Computers*, 67(1):16–31, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7967774/`.

**Take:2014:NIC**

[TMS+14] Yasuhiro Take, Hiroki Matsutani, Daisuke Sasaki, Michihiro Koibuchi, Tadahiro Kuroda, and Hideharu Amano. 3D NoC with inductive-coupling links for building-block SiPs. *IEEE Transactions on Computers*, 63(3):748–763, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Turcu:2016:OND**

[TPR16] Alexandru Turcu, Roberto Palmieri, and Binoy Ravindran. On open nesting in distributed transactional memory. *IEEE Transac-*

*tions on Computers*, 65 (6):1856–1868, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tomes:2018:TAP**

[TRA18] Erica Tomes, Everett Neil Rush, and Nihat Altiparmak. Towards adaptive parallel storage systems. *IEEE Transactions on Computers*, 67(12):1840–1848, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8359084/.

**Tumer:2011:IDS**

[TS11] Irem Y. Tumer and Carol S. Smidts. Integrated design-stage failure analysis of software-driven hardware systems. *IEEE Transactions on Computers*, 60(8): 1072–1084, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tsai:2013:QPD**

[Tsa13] Chang-Hsiung Tsai. A quick pessimistic diagnosis algorithm for hypercube-like multiprocessor systems under the PMC model. *IEEE Transactions on Computers*, 62(2):259–267, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Testa:2019:MMB**

[TSA+19] E. Testa, M. Soeken, L. G. Amarù, W. Haaswijk, and G. De Micheli. Mapping monotone Boolean functions into majority. *IEEE Transactions on Computers*, 68(5): 791–797, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tse:2012:OBB**

[Tse12] Savio S. H. Tse. Online bounds on balancing two independent criteria with replication and reallocation. *IEEE Transactions on Computers*, 61(11): 1601–1610, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Teymoori:2016:FFC**

[TSK16] Peyman Teymoori, Khosrow Sohraby, and Kiseon Kim. Fair flow control and fairness evaluation in computer networks and systems. *IEEE Transactions on Computers*, 65(7):2090–2103, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2010:LHO**

[TW10] Xiangyu Tang and Seongmoon Wang. A low hardware overhead self-diagnosis technique using Reed–Solomon codes for self-repairing chips. *IEEE Transactions on Computers*, 59(10):1309–1319, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374371`.

**Tian:2011:EMI**

[TWTT11] Xin Tian, Lin Wu, Yi-Hua Tan, and Jin-Wen Tian. Efficient multi-input/multi-output VLSI architecture for two-dimensional lifting-based discrete wavelet transform. *IEEE Transactions on Computers*, 60(8):1207–1211, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Trost:2016:OPC**

[TX16] William R. Trost and Guangwu Xu. On the optimal pre-computation of window NAF for Koblitz curves. *IEEE Transactions on Computers*, 65(9):2918–2924, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2011:STG**

[TXL11] Yong Tang, Bin Xiao, and Xicheng Lu. Signature tree generation for polymorphic worms. *IEEE Transactions on Computers*, 60(4):565–579, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Tang:2010:IST**

[TYWC10] Shyue-Ming Tang, Jinn-Shyong Yang, Yue-Li Wang, and Jou-Ming Chang. Independent spanning trees on multidimensional torus networks. *IEEE Transactions on Computers*, 59(1):93–102, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5184805`.

**Tang:2016:NOO**

[TYY+16] B. Tang, S. Yang, B. Ye, S. Guo, and S. Lu. Near-optimal one-sided scheduling for coded segmented network coding. *IEEE Transactions on Computers*, 65(3):929–939, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Teng:2014:SLS**

[TZL+14] Jin Teng, Boying Zhang, Xinfeng Li, Xiaole Bai, and Dong Xuan. E-Shadow: Lubricating social interaction using mobile phones. *IEEE Transactions on Computers*, 63(6):1422–1433, June 2014.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ugurdag:2017:HDS**

[UdDG+17] H. Fatih Ugurdag, Florent de Dinechin, Y. Serhan Gener, Sezer Gören, and Laurent-Stéphane Didier. Hardware division by small integer constants. *IEEE Transactions on Computers*, 66(12):2097–2110, December 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7933010/`.

**Ueno:2017:FAV**

[UHSA17] Rei Ueno, Naofumi Homma, Yukihiro Sugawara, and Takafumi Aoki. Formal approach for verifying Galois field arithmetic circuits of higher degrees. *IEEE Transactions on Computers*, 66 (3):431–442, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Uchigaito:2018:EDA**

[UMN18] Hiroshi Uchigaito, Seiji Miura, and Takumi Nito. Efficient data-allocation scheme for eliminating garbage collection during analysis of big graphs stored in NAND flash memory. *IEEE Transactions on Computers*, 67(5): 646–657, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8118115/`.

**Ueno:2019:TBP**

[USH19] R. Ueno, M. Suzuki, and N. Homma. Tackling biased PUFs through biased masking: a debiasing method for efficient fuzzy extractor. *IEEE Transactions on Computers*, 68(7):1091–1104, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ubal:2013:HBG**

[USP+13] Rafael Ubal, Julio Sahuquillo, Salvador Petit, Pedro Lopez, and Jose Duato. Hardware-based generation of independent subtraces of instructions in clustered processors. *IEEE Transactions on Computers*, 62(5):944–955, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Upasani:2016:CAW**

[UVG16] G. Upasani, X. Vera, and A. Gonzalez. A case for acoustic wave detectors for soft-errors. *IEEE Transactions on Computers*, 65(1): 5–18, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Unnikrishnan:2013:RDP**

[UVL+13] Deepak Unnikrishnan, Ramakrishna Vadlamani, Yong Liao, Jeremie Crenne, Lixin Gao, and Russell Tessier. Reconfigurable data planes for scalable network virtualization. *IEEE Transactions on Computers*, 62(12): 2476–2488, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vemu:2011:CCF**

[VA11] Ramtilak Vemu and Jacob Abraham. CEDA: Control-flow error detection using assertions. *IEEE Transactions on Computers*, 60(9): 1233–1245, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5871589.

**Vazquez:2014:FRM**

[VAB14] Alvaro Vazquez, Elisardo Antelo, and Javier D. Bruguera. Fast radix-10 multiplication using redundant BCD codes. *IEEE Transactions on Computers*, 63 (8):1902–1914, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Venkatachalam:2019:DAA**

[VALK19] S. Venkatachalam, E. Adams, H. J. Lee, and S. Ko. Design and analysis of area and power efficient approximate Booth multipliers. *IEEE Transactions on Computers*, 68(11):1697–1703, November 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vazquez:2010:IDH**

[VAM10] A. Vazquez, E. Antelo, and P. Montuschi. Improved design of high-performance parallel decimal multipliers. *IEEE Transactions on Computers*, 59(5):679–693, May 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5313798.

**Villegas:2018:LHT**

[VAN+18] Alejandro Villegas, Rafael Asenjo, Angeles Navarro, Oscar Plata, and David Kaeli. Lightweight hardware transactional memory for GPU scratchpad memory. *IEEE Transactions on Computers*, 67(6): 816–829, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8119530/.

**Vazquez:2013:IAA**

[VB13] Alvaro Vazquez and Javier D. Bruguera. Iterative algorithm and architecture for exponential, logarithm, powering, and root extraction. *IEEE Transactions on Computers*, 62(9): 1721–1731, September 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Villas:2013:DLR**

[VBR+13] L. A. Villas, A. Boukerche, H. S. Ramos, H. A. B. F. de Oliveira, R. B. de Araujo, and A. A. F. Loureiro. DRINA: A lightweight and reliable routing approach for in-network aggregation in wireless sensor networks. *IEEE Transactions on Computers*, 62(4):676–689, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Valente:2010:HTD**

[VC10] P. Valente and F. Checconi. High throughput disk scheduling with fair bandwidth distribution. *IEEE Transactions on Computers*, 59(9):1172–1186, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467047.

**Veronese:2013:EBF**

[VCB+13] Giuliana Santos Veronese, Miguel Correia, Alysson Neves Bessani, Lau Cheuk Lung, and Paulo Verissimo. Efficient Byzantine fault-tolerance. *IEEE Transactions on Computers*, 62 (1):16–30, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vujic:2012:DFD**

[VCG+12] Nikola Vujic, Felipe Cabarcas, Marc Gonzalez, Alex Ramirez, Xavier Martorell, and Eduard Ayguade. DMA++: On the fly data realignment for on-chip memories. *IEEE Transactions on Computers*, 61(2):237–250, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Valero:2019:AAG**

[VCSG+19] Alejandro Valero, Francisco Candel, Darío Suárez-Gracia, Salvador Petit, and Julio Sahuquillo. An aging-aware GPU register file design based on data redundancy. *IEEE Transactions on Computers*, 68 (1):4–20, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8395355/.

**Vergos:2012:MAD**

[VD12] Haridimos T. Vergos and Giorgos Dimitrakopoulos. On modulo $2^n + 1$ adder design. *IEEE Transactions* on Computers, 61(2):173–186, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**vandenBraak:2016:CXH**

[vdBGLGL+16] Gert-Jan van den Braak, Juan Gómez-Luna, José María González-Linares, Henk Corporaal, and Nicolás Guil. Configurable XOR hash functions for banked scratchpad memories in GPUs. *IEEE Transactions on Computers*, 65(7):2045–2058, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**VanWambeke:2013:AMA**

[VECD13] Nicolas Van Wambeke, Ernesto Exposito, Christophe Chassot, and Michel Diaz. ATP: A microprotocol approach to autonomic communication. *IEEE Transactions on Computers*, 62(11):2131–2140, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**VandenSteen:2016:APP**

[VED+16] Sam Van den Steen, Stijn Eyerman, Sander De Pestel, Moncef Mechri, Trevor E. Carlson, David Black-Schaffer, Erik Hagersten, and Lieven Eeckhout. Analytical processor performance and power modeling using micro-architecture independent characteristics. *IEEE Transactions on Computers*, 65(12):3537–3551, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vinco:2016:CMV**

[VGF16] Sara Vinco, Valerio Guarnieri, and Franco Fummi. Code manipulation for virtual platform integration. *IEEE Transactions on Computers*, 65(9):2694–2708, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Volkova:2019:THI**

[VIDH19] Anastasia Volkova, Matei Istoan, Florent De Dinechin, and Thibault Hilaire. Towards hardware IIR filters computing just right: Direct form I case study. *IEEE Transactions on Computers*, 68(4):597–608, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8521688/`.

**Voyiatzis:2015:GSP**

[VK15] I. K. Voyiatzis and D. J. Kavvadias. On the generation of SIC pairs in optimal time. *IEEE Transactions on Computers*, 64(10):2891–2901, October 2015. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Venkatesan:2016:CDD**

[VKS+16] R. Venkatesan, V. J. Kozhikkottu, M. Sharad, C. Augustine, A. Raychowdhury, K. Roy, and A. Raghunathan. Cache design with domain wall memory. *IEEE Transactions on Computers*, 65 (4):1010–1024, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vogel:2019:ESV**

[VMB19] Pirmin Vogel, Andrea Marongiu, and Luca Benini. Exploring shared virtual memory for FPGA accelerators with a configurable IOMMU. *IEEE Transactions on Computers*, 68(4):510–525, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8519631/`.

**Villalba-Moreno:2018:URH**

[VMHGN18] Julio Villalba-Moreno, Javier Hormigo, and Sonia González-Navarro. Unbiased rounding for HUB floating-point addition. *IEEE Transactions on Computers*, 67(9): 1359–1365, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8300633/`.

**Veeravalli:2014:GEI**

[VP14] Bharadwaj Veeravalli and Manish Parashar. Guest Editors' introduction: Special issue on cloud of clouds. *IEEE Transactions on Computers*, 63(1):1–2, January 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Valero:2012:DPE**

[VPS+12] Alejandro Valero, Salvador Petit, Julio Sahuquillo, Pedro Lopez, and Jose Duato. Design, performance, and energy consumption of eDRAM/SRAM macrocells for L1 data caches. *IEEE Transactions on Computers*, 61(9):1231–1242, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vallero:2019:SES**

[VSC+19] A. Vallero, A. Savino, A. Chatzidimitriou, M. Kaliorakis, M. Kooli, M. Riera, M. Anglada, G. Di Natale, A. Bosio, R. Canal, A. Gonzalez, D. Gizopoulos, R. Mariani, and S. Di Carlo. SyRA: Early system reliability analysis for cross-layer soft errors resilience in memory arrays of microprocessor systems. *IEEE Transactions on Computers*, 68(5):765–783, May 2019. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Viegas:2017:TEE**

[VSF+17] Eduardo Viegas, Altair O. Santin, André França, Ricardo Jasinski, Volnei A. Pedroni, and Luiz S. Oliveira. Towards an energy-efficient anomaly-based intrusion detection engine for embedded systems. *IEEE Transactions on Computers*, 66 (1):163–177, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Valero:2015:DHS**

[VSLD15] A. Valero, J. Sahuquillo, P. Lopez, and J. Duato. Design of hybrid second-level caches. *IEEE Transactions on Computers*, 64 (7):1884–1897, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Valadimas:2016:TET**

[VTA16] S. Valadimas, Y. Tsiatouhas, and A. Arapoyanni. Timing error tolerance in small core designs for SoC applications. *IEEE Transactions on Computers*, 65 (2):654–663, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Venkatesan:2016:SCA**

[VTW16] Vimalraj Venkatesan, Y. C. Tay, and Qingsong Wei. Sizing cleancache allocation for virtual machines' transcendent memory. *IEEE Transactions on Computers*, 65 (6):1949–1963, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Vazquez:2012:RFP**

[VVMAZ12] Alvaro Vazquez, Julio Villalba-Moreno, Elisardo Antelo, and Emilio L. Zapata. Redundant floating-point decimal CORDIC algorithm. *IEEE Transactions on Computers*, 61(11):1551–1562, November 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Verbeek:2017:DVC**

[VYEB17] Freek Verbeek, Pooria M. Yaghini, Ashkan Eghbal, and Nader Bagherzadeh. Deadlock verification of cache coherence protocols and communication fabrics. *IEEE Transactions on Computers*, 66(2):272–284, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Verbeek:2018:CAV**

[VYEB18] Freek Verbeek, Pooria M. Yaghini, Ashkan Eghbal, and

Nader Bagherzadeh. A compositional approach for verifying protocols running on on-chip networks. *IEEE Transactions on Computers*, 67(7):905–919, July 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8239635/`.

**Whitham:2010:TPO**

[WA10] J. Whitham and N. Audsley. Time-predictable out-of-order execution for hard real-time systems. *IEEE Transactions on Computers*, 59(9):1210–1223, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467051`.

**Wu:2017:AAD**

[WAK+17] Di Wu, Dmitri I. Arkhipov, Minyoung Kim, Carolyn L. Talcott, Amelia C. Regan, Julie A. McCann, and Nalini Venkatasubramanian. ADDSEN: Adaptive data processing and dissemination for drone swarms in urban sensing. *IEEE Transactions on Computers*, 66(2):183–198, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Walther:2019:VNR**

[Wal19] Christoph Walther. Verified Newton–Raphson iteration for multiplicative inverses modulo powers of any base. *ACM Transactions on Mathematical Software*, 45(1):9:1–9:7, March 2019. CODEN ACMSCU. ISSN 0098-3500 (print), 1557-7295 (electronic). URL `https://dl.acm.org/citation.cfm?id=3301317`. See [Dum14].

**Wang:2019:CAR**

[WBGI19] Shibo Wang, Mahdi Nazm Bojnordi, Xiaochen Guo, and Engin Ipek. Content aware refresh: Exploiting the asymmetry of DRAM retention errors to reduce the refresh frequency of less vulnerable data. *IEEE Transactions on Computers*, 68(3):362–374, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8456584/`.

**Wang:2015:FFT**

[WBZ+15] Ji Wang, Weidong Bao, Xiaomin Zhu, L. T. Yang, and Yang Xiang. FESTAL: Fault-tolerant elastic scheduling algorithm for real-time tasks in virtualized clouds. *IEEE Transactions on Computers*, 64(9):2545–2558, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:VAO**

[WCH+15] J. Wang, X. Chen, X. Huang, I. You, and Y. Xiang. Verifiable auditing for outsourced database in cloud computing. *IEEE Transactions on Computers*, 64(11): 3293–3303, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wei:2018:GCQ**

[WCL+18] Chun-Yan Wei, Xiao-Qiu Cai, Bin Liu, Tian-Yin Wang, and Fei Gao. A generic construction of quantum-oblivious-key-transfer-based private query with ideal database security and zero failure. *IEEE Transactions on Computers*, 67(1): 2–8, January 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7962191/.

**Wang:2016:MCP**

[WCLY16] B. Wang, J. Chen, W. Liu, and L. T. Yang. Minimum cost placement of bistatic radar sensors for belt barrier coverage. *IEEE Transactions on Computers*, 65 (2):577–588, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:TEE**

[WCM+16] J. Wang, Z. Cao, X. Mao, X. Li, and Y. Liu. Towards energy efficient duty-cycled networks: Analysis, implications and improvement. *IEEE Transactions on Computers*, 65 (1):270–280, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2013:PPP**

[WCW+13] Cong Wang, Sherman S. M. Chow, Qian Wang, Kui Ren, and Wenjing Lou. Privacy-preserving public auditing for secure cloud storage. *IEEE Transactions on Computers*, 62(2):362–375, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2012:HPM**

[WDSP12] Guiming Wu, Yong Dou, Junqing Sun, and Gregory D. Peterson. A high performance and memory efficient LU decomposer on FPGAs. *IEEE Transactions on Computers*, 61(3):366–378, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2012:RCC**

[WE12] Dong Wang and Milos D. Ercegovac. A radix-16 combined complex division/square root unit with operand prescaling. *IEEE Transactions on Computers*, 61(9):1243–1255, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2019:EDT**

[WEH+19] Junshi Wang, Masoumeh Ebrahimi, Letian Huang, Xuan Xie, Qiang Li, Guangjun Li, and Axel Jantsch. Efficient design-for-test approach for networks-on-chip. *IEEE Transactions on Computers*, 68(2):198–213, February 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8440731/`.

**Wang:2014:CFA**

[WEX14] Dong Wang, Miloš D. Ercegovac, and Yang Xiao. Complex function approximation using two-dimensional interpolation. *IEEE Transactions on Computers*, 63(12):2948–2960, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2012:EMB**

[WF12] Zhen Wang and Shuqin Fan. Efficient Montgomery-based semi-systolic multiplier for even-type GNB of GF($2^m$). *IEEE Transactions on Computers*, 61(3):415–419, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wei:2014:GTA**

[WF14] Hongchuan Wei and S. Ferrari. A geometric transversals approach to analyzing the probability of track detection for maneuvering targets. *IEEE Transactions on Computers*, 63(11):2633–2646, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wahba:2017:AEF**

[WF17] Ahmed A. Wahba and Hossam A. H. Fahmy. Area efficient and fast combined binary/decimal floating point fused multiply add unit. *IEEE Transactions on Computers*, 66(2):226–239, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wei:2019:NRS**

[WFT+19] X. Wei, D. Feng, W. Tong, J. LIU, and L. Ye. NICO: Reducing software-transparent crash consistency cost for persistent memory. *IEEE Transactions on Computers*, 68(9):1313–1324, September 2019. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Wu:2017:PEH**

[WFY+17] Fenggang Wu, Ziqi Fan, Ming-Chang Yang, Baoquan Zhang, Xiongzi Ge, and David H. C. Du. Performance evaluation of host aware shingled magnetic recording (HA–SMR) drives. *IEEE Transactions on Computers*, 66(11): 1932–1945, November 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7942044/.

**Weng:2013:HCM**

[WGLL13] Chuliang Weng, Minyi Guo, Yuan Luo, and Minglu Li. Hybrid CPU management for adapting to the diversity of virtual machines. *IEEE Transactions on Computers*, 62(7):1332–1344, July 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2014:NSP**

[WGR+14] Yongwei Wu, Weichao Guo, Jinglei Ren, Xun Zhao, and Weimin Zheng. NO2: Speeding up parallel processing of massive compute-intensive tasks. *IEEE Transactions on Computers*, 63(10):2487–2499, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:SSE**

[WGW+15] A. Wang, M. Gaudet, Peng Wu, M. Ohmacht, J. N. Amaral, C. Barton, R. Silvera, and M. M. Michael. Software support and evaluation of hardware transactional memory on Blue Gene/Q. *IEEE Transactions on Computers*, 64 (1):233–246, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wei:2015:ESW**

[WGZ+15] Kaimin Wei, Song Guo, Deze Zeng, Ke Xu, and Keqiu Li. Exploiting small world properties for message forwarding in delay tolerant networks. *IEEE Transactions on Computers*, 64(10): 2809–2818, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:CPS**

[WHBR16] L. Wang, S. Hu, G. Betis, and R. Ranjan. A computing perspective on Smart City [guest editorial]. *IEEE Transactions on Computers*, 65(5):1337–1338, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:EFF**

[WHC+15a] Wei Wang, Yin Hu, Lianmu Chen, Xinming Huang, and B. Sunar. Exploring the feasibility of fully homomorphic encryption. *IEEE Transactions on Computers*, 64(3):698–706, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wen:2015:STE**

[WHC+15b] Sheng Wen, M. S. Haghighi, Chao Chen, Yang Xiang, Wanlei Zhou, and Weijia Jia. A sword with two edges: Propagation studies on both positive and negative information in online social networks. *IEEE Transactions on Computers*, 64(3):640–653, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wen:2012:NNU**

[WhCCC12] Chi-Neng Wen, Shu hsuan Chou, Chien-Chih Chen, and Tien-Fu Chen. NUDA: a non-uniform debugging architecture and nonintrusive race detection for many-core systems. *IEEE Transactions on Computers*, 61 (2):199–212, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2012:NSI**

[WHL+12] Yue-Li Wang, Cheng-Ju Hsu, Jia-Jie Liu, Ming-Tat Ko, and Fu-Hsing Wang. A new subclass of integer linear programming problems and its applications. *IEEE Transactions on Computers*, 61(12):1813–1822, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wei:2017:PAA**

[WHL17] Hengfeng Wei, Yu Huang, and Jian Lu. Probabilistically-atomic 2-atomicity: Enabling almost strong consistency in distributed storage systems. *IEEE Transactions on Computers*, 66 (3):502–514, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:UMC**

[WHYS16] Rui Wang, William N. N. Hung, Guowu Yang, and Xiaoyu Song. Uncertainty model for configurable hardware/software and resource partitioning. *IEEE Transactions on Computers*, 65(10): 3217–3223, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:EAB**

[WHZ+15] Z. Wang, D. Huang, Y. Zhu, B. Li, and C. Chung. Efficient attribute-based comparable data access control. *IEEE Transactions on Computers*, 64(12):3430–3443, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2011:IAR**

[WJF+11] Suzhen Wu, Hong Jiang, Dan Feng, Lei Tian, and Bo Mao. Improving availability of RAID-structured storage systems by workload outsourcing. *IEEE Transactions on Computers*, 60(1):64–79, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2012:SLM**

[WJL+12] Cheng Wang, Changjun Jiang, Xiang-Yang Li, Shaojie Tang, Yuan He, Xufei Mao, and Yunhao Liu. Scaling laws of multicast capacity for power-constrained wireless networks under Gaussian channel model. *IEEE Transactions on Computers*, 61(5):713–725, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2014:ACW**

[WJL+14] Cheng Wang, Changjun Jiang, Yunhao Liu, Xiang-Yang Li, and Shaojie Tang. Aggregation capacity of wireless sensor networks: Extended network case. *IEEE Transactions on Computers*, 63(6):1351–1364, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2015:PDM**

[WJM15] Suzhen Wu, Hong Jiang, and Bo Mao. Proactive data migration for improved storage availability in large-scale data centers. *IEEE Transactions on Computers*, 64(9):2637–2651, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Woodruff:2019:CCP**

[WJX+19] J. Woodruff, A. Joannou, H. Xia, A. Fox, R. M. Norton, D. Chisnall, B. Davis, K. Gudka, N. W. Filardo, A. T. Markettos, M. Roe, P. G. Neumann, R. N. M. Watson, and S. W. Moore. CHERI Concentrate: Practical compressed capabilities. *IEEE Transactions on Computers*, 68(10):1455–1469, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2017:HNA**

[WJY+17] Xiaohang Wang, Yingtao Jiang, Mei Yang, Hong Li, and Terrence Mak. HRC: A 3D NoC architecture with genuine support for runtime thermal-aware task management. *IEEE Transactions on Computers*, 66(10): 1676–1688, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7914637/`.

**Wang:2016:DRS**

[WKB16] Zhen Wang, Mark Karpovsky, and Lake Bu. Design of reliable and secure devices realizing Shamir's secret sharing. *IEEE Transactions on Computers*, 65 (8):2443–2455, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2013:MAS**

[WL13] Yang Wang and Paul Lu. Maximizing active storage resources with deadlock avoidance in workflow-based computations. *IEEE Transactions on Computers*, 62(11):2210–2223, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:BBM**

[WLC+15] Jiantao Wang, Kam-Yiu Lam, Yuan-Hao Chang, Jen-Wei Hsieh, and Po-Chun Huang. Block-based multi-version B-tree for flash-based embedded database systems. *IEEE Transactions on Computers*, 64(4):925–940, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2019:EMC**

[WLG+19] Yi Wu, You Li, Xiangxuan Ge, Yuan Gao, and Weikang Qian. An efficient method for calculating the error statistics of block-based approximate adders. *IEEE Transactions on Computers*, 68 (1):21–38, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8419308/`.

**Wang:2016:SEP**

[WLJ+16] H. Wang, Y. Li, D. Jin, P. Hui, and J. Wu. Saving energy in partially deployed software defined networks. *IEEE Transactions on Computers*, 65(5):1578–1592, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:WWA**

[WLK15] Yang Wang, P. Lu, and K. B. Kent. WaFS: A workflow-aware file system for effective storage utilization in the cloud. *IEEE Transactions on Computers*, 64(9): 2716–2729, September 2015. CODEN ITCOB4. ISSN

0018-9340 (print), 1557-9956 (electronic).

**Weng:2016:CMV**

[WLLZ16] Chuliang Weng, Qian Liu, Kenli Li, and Deqing Zou. CloudMon: Monitoring virtual machines in clouds. *IEEE Transactions on Computers*, 65(12):3787–3793, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Won:2015:MMC**

[WLM15] Youjip Won, Kyeongyeol Lim, and Jaehong Min. MUCH: Multithreaded content-based file chunking. *IEEE Transactions on Computers*, 64(5):1375–1388, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2013:ORI**

[WLQS13] Yi Wang, Duo Liu, Zhiwei Qin, and Zili Shao. Optimally removing intercore communication overhead for streaming applications on MPSoCs. *IEEE Transactions on Computers*, 62 (2):336–350, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2018:HMR**

[WLS18] Chun-Wei Wu, Kuen-Jong Lee, and Alan P. Su. A hybrid multicast routing approach with enhanced methods for mesh-based networks-on-chip. *IEEE Transactions on Computers*, 67(9):1231–1245, September 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8309347/`.

**Wang:2016:DEO**

[WLT+16] Y. Wang, Z. Li, G. Tyson, S. Uhlig, and G. Xie. Design and evaluation of the optimal cache allocation for content-centric networking. *IEEE Transactions on Computers*, 65(1):95–107, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2014:DPA**

[WLV+14] Danyao Wang, Charles Lo, Jasmina Vasiljevic, Natalie Enright Jerger, and J. Gregory Steffan. DART: A programmable architecture for NoC simulation on FPGAs. *IEEE Transactions on Computers*, 63 (3):664–678, March 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**We:2014:HPN**

[WLY+14] Kyoung-Soo We, Chang-Gun Lee, Kyongsu Yi, Yun Sang Lee, and Kwei-Jay Lin. HRT-PLRU: A new paging scheme for executing hard real-time programs on NAND flash memory. *IEEE Transactions on Computers*, 63(4):927–940, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:MDG**

[WLYY16] Cong Wang, Ji Li, Fan Ye, and Yuanyuan Yang. A mobile data gathering framework for wireless rechargeable sensor networks with vehicle movement costs and capacity constraints. *IEEE Transactions on Computers*, 65(8):2411–2427, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2010:GFP**

[WLZ10] Jianjia Wu, Jyh-Charn Liu, and Wei Zhao. A general framework for parameterized schedulability bound analysis of real-time systems. *IEEE Transactions on Computers*, 59(6):776–783, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5406502.

**Wang:2015:AST**

[WLZ+15] Chao Wang, Xi Li, Junneng Zhang, Peng Chen, Yunji Chen, Xuehai Zhou, and R. C. C. Cheung. Architecture support for task out-of-order execution in MPSoCs. *IEEE Transactions on Computers*, 64(5):1296–1310, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wild:2018:GGF**

[WMG18] Alexander Wild, Amir Moradi, and Tim Güneysu. GliFreD: Glitch-free duplication towards power-equalized circuits on FPGAs. *IEEE Transactions on Computers*, 67(3):375–387, March 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7827086/.

**Wang:2012:CLC**

[WMW12] Xiaorui Wang, Kai Ma, and Yefu Wang. Cache latency control for application fairness or differentiation in power-constrained chip multiprocessors. *IEEE Transactions on Computers*, 61(10):1371–1385, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2017:CBS**

[WNCH17] Yi Wu, Ge Nong, Wai Hong Chan, and Ling Bo Han. Checking big suffix and LCP arrays by probabilistic methods. *IEEE Transac-*

tions on Computers, 66(10): 1667–1675, October 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7922513/.

**Wei:2016:NST**

[WNKL16] W. Wei, K. Namba, Y. Kim, and F. Lombardi. A novel scheme for tolerating single event/multiple bit upsets (SEU/MBU) in non-volatile memories. *IEEE Transactions on Computers*, 65(3):781–790, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:MFR**

[WP16] Yanzhi Wang and Massoud Pedram. Model-free reinforcement learning and Bayesian classification in system-level power management. *IEEE Transactions on Computers*, 65(12): 3713–3726, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2016:CBE**

[WQZ+16] Q. Wu, B. Qin, L. Zhang, J. Domingo-Ferrer, O. Farras, and J. A. Manjon. Contributory broadcast encryption with efficient encryption and short ciphertexts. *IEEE Transactions on Computers*, 65(2):466–479, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:SOC**

[WRW16] C. Wang, K. Ren, and J. Wang. Secure optimization computation outsourcing in cloud computing: A case study of linear programming. *IEEE Transactions on Computers*, 65 (1):216–229, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Waters:2010:RCW**

[WS10] Ron S. Waters and Earl E. Swartzlander. A reduced complexity Wallace multiplier reduction. *IEEE Transactions on Computers*, 59 (8):1134–1137, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467045.

**Wang:2014:ANM**

[WS14] Dawei Wang and Xian-He Sun. APC: A novel memory metric and measurement methodology for modern memory systems. *IEEE Transactions on Computers*, 63(7):1626–1639, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Won:2015:HMR**

[WS15] M. Won and R. Stoleru. A hybrid multicast routing for large scale sensor networks with holes. *IEEE Transactions on Computers*, 64(12): 3362–3375, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2018:BBT**

[WSL+18] Xiaohang Wang, Amit Kumar Singh, Bing Li, Yang Yang, Hong Li, and Terrence Mak. Bubble budgeting: Throughput optimization for dynamic workloads by exploiting dark cores in many core systems. *IEEE Transactions on Computers*, 67 (2):178–192, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8006237/.

**Wu:2013:UMP**

[WSXZ13] Qi Wu, Fei Sun, Wei Xu, and Tong Zhang. Using multilevel phase change memory to build data storage: A time-aware system design perspective. *IEEE Transactions on Computers*, 62(10): 2083–2095, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:MNP**

[WSZ+16] Ruijun Wang, Pengju Shang, Junyao Zhang, Qingdong Wang, Ting Liu, and Jun Wang. MAR: A novel power management for CMP systems in data-intensive environment. *IEEE Transactions on Computers*, 65 (6):1816–1830, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Walker:2013:PRA**

[WTBT13] James Alfred Walker, Martin A. Trefzer, Simon J. Bale, and Andy M. Tyrrell. PAnDA: A reconfigurable architecture that adapts to physical substrate variations. *IEEE Transactions on Computers*, 62(8):1584–1596, August 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2014:MLL**

[WTY+14] Jianxin Wang, Peiqiang Tan, Jinyi Yao, Qilong Feng, and Jianer Chen. On the minimum link-length rectilinear spanning path problem: Complexity and algorithms. *IEEE Transactions on Computers*, 63(12): 3092–3100, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2019:GGB**

[WTZ+19] Hai Wang, Diya Tang, Ming Zhang, Sheldon X. D. Tan, Chi Zhang, He Tang, and Yuan Yuan. GDP: A greedy based dynamic power budgeting method for multi/ many-core systems in dark silicon. *IEEE Transactions on Computers*, 68 (4):526–541, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8493277/`.

**Wu:2014:HBM**

[WW14] Jie Wu and Yunsheng Wang. Hypercube-based multipath social feature routing in human contact networks. *IEEE Transactions on Computers*, 63(2):383–396, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:TEW**

[WW16] Chundong Wang and Weng-Fai Wong. TreeFTL: An efficient workload-adaptive algorithm for RAM buffer management of NAND flash-based devices. *IEEE Transactions on Computers*, 65 (8):2618–2630, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:ARA**

[WWM16] Liang Wang, Xiaohang Wang, and Terrence Mak. Adaptive routing algorithms for lifetime reliability optimization in network-on-chip. *IEEE Transactions on Computers*, 65(9):2896–2902, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2018:FLA**

[WWT+18] Hai Wang, Jiachun Wan, Sheldon X.-D. Tan, Chi Zhang, He Tang, Yuan Yuan, Keheng Huang, and Zhenghong Zhang. A fast leakage-aware full-chip transient thermal estimation method. *IEEE Transactions on Computers*, 67(5): 617–630, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8125149/`.

**Wang:2016:CSC**

[WWY+16] J. Wang, L. Wang, H. Yin, Z. Wei, Z. Yang, and N. Gong. cNV SRAM: CMOS technology compatible non-volatile SRAM based ultra-low leakage energy hybrid memory system. *IEEE Transactions on Computers*, 65(4):1055–1067, April 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2018:NDH**

[WWY+18] Chundong Wang, Qingsong Wei, Jun Yang, Cheng Chen, Yechao Yang, and Mingdi Xue. NV-Dedup: High-performance inline deduplication for non-volatile memory. *IEEE Transactions on Computers*, 67(5): 658–671, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8115169/`.

**Wang:2013:NNP**

[WXLL13] Bang Wang, Han Xu, Wenyu Liu, and Hui Liang. A novel node placement for long belt coverage in wireless networks. *IEEE Transactions on Computers*, 62(12): 2341–2353, 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:ONP**

[WXLY15] Bang Wang, Han Xu, Wenyu Liu, and L. T. Yang. The optimal node placement for long belt coverage in wireless networks. *IEEE Transactions on Computers*, 64 (2):587–592, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2012:SPC**

[WXS12] Hongya Wang, Yingyuan Xiao, and LihChyun Shu. Scheduling periodic continuous queries in real-time data broadcast environments. *IEEE Transactions on Computers*, 61(9): 1325–1340, September 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2014:FOF**

[WXW+14] Zhehui Wang, Jiang Xu, Xiaowen Wu, Yaoyao Ye, Wei Zhang, Mahdi Nikdast, Xuan Wang, and Zhe Wang. Floorplan optimization of fat-tree-based networks-on-chip for chip multiprocessors. *IEEE Transactions on Computers*, 63(6):1446–1459, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2015:AMI**

[WYL+15] Yan Wu, Chun Gang Yan, Lu Liu, Zhi Jun Ding, and Chang Jun Jiang. An adaptive multilevel indexing method for disaster service discovery. *IEEE Transactions on Computers*, 64(9): 2447–2459, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2014:ORS**

[WZ14] Qi Wu and Tong Zhang. OFWAR: Reducing SSD response time using on-demand fast-write-and-rewrite.

*IEEE Transactions on Computers*, 63(10):2500–2512, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2015:III**

[WZBB15] Xinmu Wang, Yu Zheng, A. Basak, and S. Bhunia. IIPS: Infrastructure IP for secure SoC design. *IEEE Transactions on Computers*, 64(8):2226–2238, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2016:MGN**

[WZCG16] Yangbing Wu, Jianfeng Zhao, Deming Chen, and Donghui Guo. Modeling of Gaussian network-based reconfigurable network-on-chip designs. *IEEE Transactions on Computers*, 65 (7):2134–2142, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2019:ICC**

[WZK+19] L. Wang, X. Zhao, D. Kaeli, Z. Wang, and L. Eeckhout. Intra-cluster coalescing and distributed-block scheduling to reduce GPU NoC pressure. *IEEE Transactions on Computers*, 68 (7):1064–1076, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Weng:2015:TEI**

[WZL15] Chuliang Weng, Jianfeng Zhan, and Yuan Luo. TSAC: Enforcing isolation of virtual machines in clouds. *IEEE Transactions on Computers*, 64(5):1470–1482, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2017:IDF**

[WZL+17] H. Wang, K. Zhao, M. Lv, X. Zhang, H. Sun, and T. Zhang. Improving 3D DRAM fault tolerance through weak cell aware error correction. *IEEE Transactions on Computers*, 66(5): 820–833, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2016:WNS**

[WZLS16] J. Wu, X. Zhu, T. Li, and X. Sui. WBSP: A novel synchronization mechanism for architecture parallel simulation. *IEEE Transactions on Computers*, 65 (3):992–1005, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2012:RPM**

[WZLX12] Hao Wang, Haiquan (Chuck) Zhao, Bill Lin, and Jun (Jim)

Xu. Robust pipelined memory system with worst case performance guarantee for network processing. *IEEE Transactions on Computers*, 61(10):1386–1400, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2016:FGR**

[WZM+16] Xiaohang Wang, Baoxin Zhao, Terrence Mak, Mei Yang, Yingtao Jiang, and Masoud Daneshtalab. On fine-grained runtime power budgeting for networks-on-chip systems. *IEEE Transactions on Computers*, 65(9): 2780–2793, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wu:2019:RAD**

[WZW+19] Chengwen Wu, Guangyan Zhang, Yang Wang, Xinyang Jiang, and Weimin Zheng. Redio: Accelerating disk-based graph processing by reducing disk I/Os. *IEEE Transactions on Computers*, 68(3):414–425, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8489961/.

**Wei:2016:PDP**

[WZY16] T. Wei, Q. Zhu, and N. Yu. Proactive demand participation of smart buildings in smart grid. *IEEE Transactions on Computers*, 65 (5):1392–1406, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Wang:2010:EDC**

[WZZ10] Chen Wang, Bing Bing Zhou, and A. Y. Zomaya. EvolvingSpace: a data centric framework for integrating bioinformatics applications. *IEEE Transactions on Computers*, 59 (6):721–734, June 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416682.

**Xia:2015:CBE**

[XAYL15] Feng Xia, A. M. Ahmed, L. Tianruo Yang, and Zhongxuan Luo. Community-based event dissemination with optimal load balancing. *IEEE Transactions on Computers*, 64(7):1857–1869, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiang:2016:MBT**

[XCF16] Dong Xiang, Krishnendu Chakrabarty, and Hideo Fujiwara. Multicast-based testing and thermal-aware test scheduling for 3D ICs with a stacked network-on-chip. *IEEE Transactions on Computers*, 65(9): 2767–2779, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiao:2010:CDR**

[XCW+10] Yang Xiao, Hui Chen, Kui Wu, Bo Sun, Ying Zhang, Xinyu Sun, and Chong Liu. Coverage and detection of a randomized scheduling algorithm in wireless sensor networks. *IEEE Transactions on Computers*, 59 (4):507–521, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5313803`.

**Xie:2011:ULD**

[XDZ11] Ningde Xie, Guiqiang Dong, and Tong Zhang. Using lossless data compression in data storage systems: Not for saving space. *IEEE Transactions on Computers*, 60(3):335–345, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2016:SER**

[XH16] Xin Xu and H. Howie Huang. On soft error reliability of virtualization infrastructure. *IEEE Transactions on Computers*, 65(12): 3727–3739, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xue:2019:DAM**

[XHLW19] D. Xue, L. Huang, C. Li, and C. Wu. Dapper: An adaptive manager for large-capacity persistent memory. *IEEE Transactions on Computers*, 68(7):1019–1034, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xue:2017:CNC**

[XHX+17] Kaiping Xue, Jianan Hong, Yingjie Xue, David S. L. Wei, Nenghai Yu, and Peilin Hong. CABE: A new comparable attribute-based encryption construction with 0-encoding and 1-encoding. *IEEE Transactions on Computers*, 66(9): 1491–1503, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7896558/`.

**Xie:2014:ESP**

[XHZ14] Jin Xie, Wei Hu, and Zhenghao Zhang. Efficient software partial packet recovery in 802.11 wireless LANs. *IEEE Transactions on Computers*, 63(10):2402–2415, October 2014. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Xiao:2016:SSD**

[XHZC16] K. Xiao, X. S. Hu, B. Zhou, and D. Z. Chen. Shell: A spatial decomposition data structure for ray traversal on GPU. *IEEE Transactions on Computers*, 65 (1):230–243, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xia:2015:SLB**

[XJFH15] Wen Xia, Hong Jiang, Dan Feng, and Yu Hua. Similarity and locality based indexing for high performance data deduplication. *IEEE Transactions on Computers*, 64(4):1162–1176, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xia:2016:DDA**

[XJFT16] Wen Xia, Hong Jiang, Dan Feng, and Lei Tian. DARE: A deduplication-aware resemblance detection and elimination scheme for data reduction with low overheads. *IEEE Transactions on Computers*, 65(6):1692–1705, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2016:CIB**

[XJW+16] P. Xu, T. Jiao, Q. Wu, W. Wang, and H. Jin. Conditional identity-based broadcast proxy re-encryption and its application to cloud email. *IEEE Transactions on Computers*, 65(1):66–79, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2013:PKE**

[XJWW13] Peng Xu, Hai Jin, Qianhong Wu, and Wei Wang. Public-key encryption with fuzzy keyword search: A provably secure scheme under keyword guessing attack. *IEEE Transactions on Computers*, 62(11):2266–2277, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2015:AMB**

[XKT+15] Dianxiang Xu, M. Kent, L. Thomas, T. Mouelhi, and Y. Le Traon. Automated model-based testing of role-based access control using predicate/transition nets. *IEEE Transactions on Computers*, 64(9):2490–2505, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xhafa:2016:GES**

[XL16] Fatos Xhafa and Vincenzo Loia. Guest editorial for special section on advanced techniques for efficient and reliable cloud storage. *IEEE Transactions on Computers*, 65(8):2346–2347, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiao:2014:IMP**

[XLC14] Zhen Xiao, Cheng Liu, and Qi Chen. Improving MapReduce performance using smart speculative execution strategy. *IEEE Transactions on Computers*, 63(4):954–967, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2015:ESP**

[XLF15] Dan Xu, Xin Liu, and Bin Fan. Efficient server provisioning and offloading policies for Internet data centers with dynamic load-demand. *IEEE Transactions on Computers*, 64 (3):682–697, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2016:HIA**

[XLJ16] Fei Xu, Fangming Liu, and Hai Jin. Heterogeneity and interference-aware virtual machine provisioning for predictable performance in the cloud. *IEEE Transactions on Computers*, 65 (8):2470–2483, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2014:IML**

[XLL+14] Fei Xu, Fangming Liu, Linghui Liu, Hai Jin, Bo Li, and Baochun Li. iAware: Making live migration of virtual machines interference-aware in the cloud. *IEEE Transactions on Computers*, 63(12):3012–3025, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2015:AAM**

[XLL15] Wenzheng Xu, Weifa Liang, and Xiaola Lin. Approximation algorithms for min-max cycle cover problems. *IEEE Transactions on Computers*, 64(3):600–613, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xie:2018:CEC**

[XLL+18] Xiaolong Xie, Yun Liang, Xiuhong Li, Yudong Wu, Guangyu Sun, Tao Wang, and Dongrui Fan. CRAT: Enabling coordinated register allocation and thread-level parallelism optimization for GPUs. *IEEE Transactions on Computers*, 67(6):890–897, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL https://ieeexplore.ieee.org/document/8119900/.

**Xiao:2012:PMW**

[XLS+12] Yu Xiao, Wei Li, Matti Siekkinen, Petri Savolainen, Antti Yla-Jaaski, and Pan Hui. Power management for wireless data transmission using complex event processing. *IEEE Transactions on Computers*, 61(12): 1765–1777, December 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2011:ESS**

[XLTZ11] Ping Xu, Xiang-Yang Li, Shaojie Tang, and JiZhong Zhao. Efficient and strategyproof spectrum allocations in multichannel wireless networks. *IEEE Transactions on Computers*, 60(4):580–593, April 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2014:SLP**

[XLW14] Han Xu, Wenyu Liu, and Bang Wang. Subarea localization performance of the divide-and-cover node deployment in a long-bounded belt scenario. *IEEE Transactions on Computers*, 63(11): 2842–2850, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2014:SDF**

[XLX+14] Yinlong Xu, John C. S. Lui, Liping Xiang, Yunfeng Zhu, Runhui Li, Patrick P. C. Lee, and Silei Xu. Single disk failure recovery for X-code-based parallel storage systems. *IEEE Transactions on Computers*, 63(4):995–1007, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xie:2013:HER**

[XMH13] Jiafeng Xie, Pramod Kumar Meher, and Jianjun He. Hardware-efficient realization of prime-length DCT based on distributed arithmetic. *IEEE Transactions on Computers*, 62 (6):1170–1178, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xia:2010:SNL**

[XP10] Yinglong Xia and V. K. Prasanna. Scalable node-level computation kernels for parallel exact inference. *IEEE Transactions on Computers*, 59(1):103–115, January 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.

org/stamp/stamp.jsp?tp=&arnumber=5184808.

**Xue:2015:OLI**

[XSR15] Bin Xue, S. K. Shukla, and S. S. Ravi. Optimization of latency insensitive systems through back pressure minimization. *IEEE Transactions on Computers*, 64 (2):464–476, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiao:2012:CMP**

[XTF+12] Ying Xiao, Krishnaiyan Thulasiraman, Xi Fang, Dejun Yang, and Guoliang Xue. Computing a most probable delay constrained path: NP-hardness and approximation schemes. *IEEE Transactions on Computers*, 61(5):738–744, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2015:OEE**

[XTW15] Z. Xu, Y. Tu, and X. Wang. Online energy estimation of relational operations in database systems. *IEEE Transactions on Computers*, 64(11):3223–3236, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiao:2014:CAO**

[XWH14] Mingjun Xiao, Jie Wu, and Liusheng Huang. Community-aware opportunistic routing in mobile social networks. *IEEE Transactions on Computers*, 63(7):1682–1695, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2010:SSO**

[XWL10] Ping Xu, ShiGuang Wang, and Xiang-Yang Li. SALSA: Strategyproof online spectrum admissions for wireless networks. *IEEE Transactions on Computers*, 59(12): 1691–1702, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453346.

**Xie:2016:IAC**

[XWL+16a] K. Xie, X. Wang, X. Liu, J. Wen, and J. Cao. Interference-aware cooperative communication in multi-radio multi-channel wireless networks. *IEEE Transactions on Computers*, 65 (5):1528–1542, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xu:2016:HSA**

[XWL+16b] Chang Xu, Gang Wang, Xiaoguang Liu, Dongdong Guo, and Tie-Yan Liu. Health status assessment and failure prediction for hard drives with recurrent neural networks. *IEEE Transactions on Computers*, 65(11):3502–3508, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiong:2017:ERT**

[XWLX17] Qin Xiong, Fei Wu, Zhonghai Lu, and Changsheng Xie. Extending real-time analysis for wormhole NoCs. *IEEE Transactions on Computers*, 66(9):1532–1546, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7884964/`.

**Xiang:2010:SMM**

[XWY10] Xiaojing Xiang, Xin Wang, and Yuanyuan Yang. Stateless multicasting in mobile ad hoc networks. *IEEE Transactions on Computers*, 59(8):1076–1090, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467044`.

**Xie:2017:DUR**

[XXBL17] Dingbao Xie, Wen Xiong, Lei Bu, and Xuandong Li. Deriving unbounded reachability proof of linear hybrid automata during bounded checking procedure. *IEEE Transactions on Computers*, 66(3):416–430, March 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xue:2015:USC**

[XYF+15] Wei Xue, Chao Yang, Haohuan Fu, Xinliang Wang, Yangtong Xu, Junfeng Liao, Lin Gan, Y. Lu, R. Ranjan, and L. Wang. Ultra-scalable CPU-MIC acceleration of mesoscale atmospheric modeling on Tianhe-2. *IEEE Transactions on Computers*, 64(8):2382–2393, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xue:2017:PCG**

[XYHD17] J. Xue, Z. Yang, S. Hou, and Y. Dai. Processing concurrent graph analytics with decoupled computation model. *IEEE Transactions on Computers*, 66(5):876–890, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Xiao:2019:NEA**

[XZL+19] C. Xiao, L. Zhang, W. Liu, L. Cheng, P. Li, Y. Pan, and N. Bergmann. NV-eCryptfs: Accelerating enterprise-level cryptographic file system with non-volatile memory. *IEEE Transactions on Com-*

*puters*, 68(9):1338–1352, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yazdani:2019:LPH**

[YAG19] R. Yazdani, J. Arnau, and A. González. A low-power, high-performance speech recognition accelerator. *IEEE Transactions on Computers*, 68(12): 1817–1831, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yun:2017:BDM**

[YAGB17] Heechul Yun, Waqar Ali, Santosh Gondi, and Siddhartha Biswas. BWLOCK: A dynamic memory access control framework for soft real-time applications on multicore platforms. *IEEE Transactions on Computers*, 66(7):1247–1252, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07784697-abs.html`.

**Yamashita:2010:SOT**

[Yam10] T. Yamashita. Stability-optimized time adjustment for a networked computer clock. *IEEE Transactions on Computers*, 59(10): 1320–1336, October 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467048`.

**Yang:2014:PDB**

[Yan14] Xu Yang. Path-dividing based scheduling algorithm for reducing energy consumption of clustered VLIW architectures. *IEEE Transactions on Computers*, 63 (10):2526–2539, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yildiz:2014:OCP**

[YASS14] Enes Yildiz, Kemal Akkaya, Esra Sisikoglu, and Mustafa Y. Sir. Optimal camera placement for providing angular coverage in wireless video sensor networks. *IEEE Transactions on Computers*, 63(7):1812–1825, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2015:MUC**

[YCCJ15] Lei Yang, Jiannong Cao, Hui Cheng, and Yusheng Ji. Multi-user computation partitioning for latency sensitive mobile cloud applications. *IEEE Transactions on Computers*, 64 (8):2253–2266, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu-Chun:2015:CTD**

[YCCWC15] Cheng Yu-Chun, Cheng-Fu Chou, E. H. Wu, and Gen-Huey Chen. A cognitive TCP design for a cognitive radio network with an unstable-bandwidth link. *IEEE Transactions on Computers*, 64(10):2730–3740, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yun:2010:HSN**

[YCK10] A. Yun, Jung Hee Cheon, and Yongdae Kim. On homomorphic signatures for network coding. *IEEE Transactions on Computers*, 59(9): 1295–1296, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5441287`.

**Yang:2016:VFC**

[YCK16] Ming-Chang Yang, Yuan-Hao Chang, and Tei-Wei Kuo. Virtual flash chips: Reinforcing the hardware abstraction layer to improve data recoverability of flash devices. *IEEE Transactions on Computers*, 65(9): 2872–2883, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2016:CIA**

[YCKH16] M. Yang, Y. Chang, T. Kuo, and P. Huang. Capacity-independent address mapping for flash storage devices with explosively growing capacity. *IEEE Transactions on Computers*, 65 (2):448–465, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2012:SCS**

[YCL+12] Jing Yang, Qiang Cao, Xu Li, Changsheng Xie, and Qing Yang. ST-CDP: Snapshots in TRAP for continuous data protection. *IEEE Transactions on Computers*, 61(6):753–766, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2016:CAS**

[YCLH16] L. Yang, J. Cao, G. Liang, and X. Han. Cost aware service placement and load dispatching in mobile cloud systems. *IEEE Transactions on Computers*, 65 (5):1440–1452, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2011:EED**

[YCW11] Ching-Nung Yang, Chih-Yang Chiu, and Gen-Chin Wu. Efficient encoding/ decoding for second-order spectral-null codes by reducing random walks. *IEEE Transactions on Computers*, 60(10):1503–1510, October 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5669269`.

**Yang:2019:IWR**

[YCW+19] Ming-Chang Yang, Yuan-Hao Chang, Fenggang Wu, Tei-Wei Kuo, and David H. C. Du. On improving the write responsiveness for host-aware SMR drives. *IEEE Transactions on Computers*, 68(1):111–124, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8387485/`.

**Yoon:2010:MMF**

[YCZ10] MyungKeun Yoon, Shigang Chen, and Zhan Zhang. Minimizing the maximum firewall rule set in a network with multiple firewalls. *IEEE Transactions on Computers*, 59(2):218–230, February 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5332225`.

**Yu:2015:GMA**

[YEG+15] Z. Yu, L. Eeckhout, N. Goswami, T. Li, L. K. John, H. Jin, C. Xu, and J. Wu. GPGPU-MiniBench: Accelerating GPGPU micro-architecture simulation. *IEEE Transactions on Computers*, 64(11):3153–3166, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yaghini:2016:CIT**

[YEY+16] P. M. Yaghini, A. Eghbal, S. S. Yazdi, N. Bagherzadeh, and M. M. Green. Capacitive and inductive TSV-to-TSV resilient approaches for 3D ICs. *IEEE Transactions on Computers*, 65(3):693–705, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yao:2014:NRP**

[YFCV14] Gavin Xiaoxu Yao, Junfeng Fan, Ray C. C. Cheung, and Ingrid Verbauwhede. Novel RNS parameter selection for fast modular multiplication. *IEEE Transactions on Computers*, 63(8):2099–2105, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2014:PMP**

[YFJ+14] Baohua Yang, Jeffrey Fong, Weirong Jiang, Yibo Xue, and Jun Li. Practical multituple packet classification using dynamic discrete bit selection. *IEEE Transactions on Computers*, 63 (2):424–434, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yi:2010:NAS**

[YG10] Kyueun Yi and J.-L. Gaudiot. Network applications on simultaneous multithreading processors. *IEEE Transactions on Computers*, 59(9):1200–1209, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374374`.

**Yu:2015:FMI**

[YGS15] Shui Yu, Song Guo, and I. Stojmenovic. Fool me if you can: Mimicking attacks and anti-attacks in cyberspace. *IEEE Transactions on Computers*, 64 (1):139–151, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2019:ROP**

[YHC19] M. Yang, W. Huang, and J. Chen. Resource-oriented partitioning for multiprocessor systems with shared resources. *IEEE Transactions on Computers*, 68(6):882–898, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yin:2012:MCS**

[YHH+12] Liangze Yin, Fei He, William N. N. Hung, Xiaoyu Song, and Ming Gu. Maxterm covering for satisfiability. *IEEE Transactions on Computers*, 61(3):420–426, March 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2016:ECA**

[YHML16] Y. Yang, Y. Huang, X. Ma, and J. Lu. Enabling context-awareness by predicate detection in asynchronous environments. *IEEE Transactions on Computers*, 65 (2):522–534, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yue:2016:RLS**

[YHT+16] Y. Yue, B. He, L. Tian, H. Jiang, F. Wang, and D. Feng. Rotated logging storage architectures for data centers: Models and optimizations. *IEEE Transactions on Computers*, 65 (1):203–215, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu:2013:QDD**

[YHV13] Heng Yu, Yajun Ha, and Bharadwaj Veeravalli. Quality-driven dynamic scheduling for real-time adaptive applications on multiprocessor systems. *IEEE Transactions on Computers*, 62(10): 2026–2040, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu:2019:RYR**

[YKDEV19] J. Yu, D. Kozhaya, J. Decouchant, and P. Esteves-Verissimo. RepuCoin: Your reputation is your power. *IEEE Transactions on Computers*, 68(8):1225–1237, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yaakob:2015:PIA**

[YKK+15] N. Yaakob, I. Khalil, H. Kumarage, M. Atiquzzaman, and Z. Tari. By-passing infected areas in wireless sensor networks using BPR. *IEEE Transactions on Computers*, 64 (6):1594–1606, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2014:REB**

[YL14] Weihua Yang and Huiqiu Lin. Reliability evaluation of BC networks in terms of the extra vertex- and edge-connectivity. *IEEE Transactions on Computers*, 63(10): 2540–2548, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Younis:2010:LAR**

[YLA10] M. F. Younis, Sookyoung Lee, and A. A. Abbasi. A localized algorithm for restoring internode connectivity in networks of moveable sensors. *IEEE Transactions on Computers*, 59(12): 1669–1682, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5551121`.

**Yuen:2015:TAB**

[YLA+15] Tsz Hon Yuen, J. K. Liu, Man Ho Au, Xinyi Huang, W. Susilo, and Jianying Zhou. $k$-times attribute-based anonymous access control for cloud computing. *IEEE Transactions on Computers*, 64(9):2595–2608, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2019:OAM**

[YLGD19] L. Yang, W. Liu, N. Guan, and N. Dutt. Optimal application mapping and scheduling for network-on-chips with computation in STT–RAM based router. *IEEE Transactions on Computers*, 68(8):1174–1189, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yaguez:2014:SAS**

[YLGE14] Alvaro Garcia Yaguez, Diego R. Llanos, and Arturo Gonzalez-Escribano. Squashing alternatives for software-based speculative parallelization. *IEEE Transactions on Computers*, 63(7):1826–1839, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yoon:2010:MPJ**

[YLH10] Man-Ki Yoon, Chang-Gun Lee, and Junghee Han. Migrating from per-job analysis to per-resource analysis for tighter bounds of end-to-end response times. *IEEE Transactions on Computers*, 59(7):933–942, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5342409`.

**Yoo:2013:PNB**

[YLH13] Jonghun Yoo, Jaesoo Lee, and Seongsoo Hong. Petri net-based FTL architecture for parametric WCET estimation via FTL operation sequence derivation. *IEEE Transactions on Computers*, 62(11):2238–2251, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ye:2016:FPD**

[YLL16] Liang-Cheng Ye, Jia-Rong Liang, and Hai-Xiang Lin. A fast pessimistic diagnosis algorithm for hypercube-like networks under the comparison model. *IEEE Transactions on Computers*, 65(9):2884–2888, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2015:POU**

[YLML15] Wangdong Yang, Kenli Li, Zeyao Mo, and Keqin Li. Performance optimization using partitioned SpMV on GPUs and multicore CPUs. *IEEE Transactions on Computers*, 64(9):2623–2636, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2015:RCL**

[YLP15] Ching-Nung Yang, Zih-Yang Lin, and Sheng-Lung Peng. Reducing code length of second-order spectral-null code. *IEEE Transactions on Computers*, 64(2):492–503, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2015:NRF**

[YLY+15a] Xinyu Yang, Jie Lin, Wei Yu, P.-M. Moulema, Xinwen Fu, and Wei Zhao. A novel en-route filtering scheme against false data injection attacks in cyber-physical networked systems. *IEEE Transactions on Computers*, 64(1):4–18, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yuan:2015:DFM**

[YLY15b] Dong Yuan, Xiao Liu, and Yun Yang. Dynamic on-the-fly minimum cost benchmarking for storing generated scientific datasets in the cloud. *IEEE Transactions on Computers*, 64(10): 2781–2795, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu:2011:PTE**

[YM11] Heeyeol Yu and Rabi N. Mahapatra. A power and throughput-efficient packet classifier with $n$ Bloom filters. *IEEE Transactions on Computers*, 60(8):1182–1193, August 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2017:ECB**

[YMAG17] Bo Yang, Kalikinkar Mandal, Mark D. Aagaard, and Guang Gong. Efficient composited de Bruijn sequence generators. *IEEE Transactions on Computers*, 66 (8):1354–1368, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/7869363/.

**Yavits:2015:CAA**

[YMG15] L. Yavits, A. Morad, and R. Ginosar. Computer architecture with associative processor replacing last-level cache and SIMD accelerator. *IEEE Transactions on Computers*, 64(2):368–381, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yavits:2016:ETA**

[YMG16] L. Yavits, A. Morad, and R. Ginosar. The effect of temperature on Amdahl Law in 3D multicore era. *IEEE Transactions on Computers*, 65(6):2010–2013, June 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yasudo:2017:SNC**

[YMK+17] Ryota Yasudo, Hiroki Matsutani, Michihiro Koibuchi, Hideharu Amano, and Tadao Nakamura. Scalable networks-on-chip with elastic links demarcated by decentralized routers. *IEEE Transactions on Computers*, 66(4):702–716, April 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yin:2013:ISM**

[YMT13] Xiaoyan Yin, Xiaomin Ma, and Kishor S. Trivedi. An interacting stochastic models approach for the performance evaluation of DSRC vehicular safety communication. *IEEE Transactions on Computers*, 62(5):873–885, May 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yin:2014:PRE**

[YMTV14] Xiaoyan Yin, Xiaomin Ma, K. S. Trivedi, and A. Vinel. Performance and reliability evaluation of BSM broadcasting in DSRC with multichannel schemes. *IEEE Transactions on Computers*, 63(12):3101–3113, December 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2012:HPC**

[YP12] Yi-Hua Edward Yang and Viktor K. Prasanna. High-performance and compact architecture for regular expression matching on FPGA. *IEEE Transactions on Computers*, 61(7):1013–1025, July 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yao:2016:GRT**

[YPB+16] Gang Yao, Rodolfo Pellizzoni, Stanley Bak, Heechul Yun, and Marco Caccamo. Global real-time memory-centric scheduling for multicore systems. *IEEE Transactions on Computers*, 65(9):2739–2751, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yi:2013:ICP**

[YRG13] Kyueun Yi, Won W. Ro, and Jean-Luc Gaudiot. Importance of coherence protocols with network applications on multicore processors. *IEEE Transactions on Computers*, 62(1):6–15, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yi:2016:IPA**

[YRT+16] Xun Yi, Fang-Yu Rao, Zahir Tari, Feng Hao, Elisa Bertino, Ibrahim Khalil, and Albert Y. Zomaya. ID2S password-authenticated key exchange protocols. *IEEE Transactions on Computers*, 65(12):3687–3701, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yan:2016:CRS**

[YSLL16] G. Yan, F. Sun, H. Li, and X. Li. CoreRank: Redeeming "Sick Silicon" by dynamically quantifying core-level healthy condition. *IEEE Transactions on Computers*, 65(3):716–729, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yi:2014:DIA**

[YSZ+14] Letian Yi, Jiwu Shu, Ying Zhao, Yingjin Qian, Youyou Lu, and Weimin Zheng. Design and implementation of an asymmetric block-based parallel file system. *IEEE Transactions on Computers*, 63(7):1723–1735, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yin:2017:AER**

[YTD+17] Jianwei Yin, Yan Tang, Shuiguang Deng, Ying Li, Wei Lo, Kexiong Dong, Albert Y. Zomaya, and Calton Pu. ASSER: An efficient, reliable, and cost-effective storage scheme for object-based cloud storage systems. *IEEE Transactions on Computers*, 66(8):1326–1340, August 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7859394/`.

**Yin:2018:DDD**

[YTD+18] Jianwei Yin, Yan Tang, Shuiguang Deng, Ying Li, and Albert Y. Zomaya. $D^3$: A dynamic dual-phase deduplication framework for distributed primary storage. *IEEE Transactions on Computers*, 67(2):193–207, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8015182/`.

**Yu:2016:NSA**

[YTM16] Lei Yu, Fei Teng, and Frédéric Magoulès. Node scaling analysis for power-aware real-time tasks scheduling. *IEEE Transactions on Computers*, 65(8):2510–2521, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2012:TPI**

[YTND12] Joon-Sung Yang, Nur A. Touba, and Benoit Nadeau-Dostie. Test point insertion with control points driven by existing functional flip-flops. *IEEE Transactions on Computers*, 61(10):1473–1483, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yuce:2014:FEC**

[YUGD14] Bilgiday Yuce, H. Fatih Ugurdag, Sezer Gören, and Günhan Dündar. Fast and efficient circuit topologies for finding the maximum of $n$ $k$-bit numbers. *IEEE Transactions on Computers*, 63 (8):1868–1881, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yun:2012:ETB**

[Yun12] SangKyun Yun. An efficient TCAM-based implementation of multipattern matching using covered state encoding. *IEEE Transactions on Computers*, 61 (2):213–221, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2012:UEP**

[YW12] Kai-Chao Yang and Jia-Shung Wang. Unequal error protection for streaming media based on rateless codes. *IEEE Transactions on Computers*, 61(5): 666–675, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ye:2019:NCA**

[YWM19] T. Ye, Y. Wei, and W. Meier. A new cube attack on MORUS by using division property. *IEEE Transactions on Computers*, 68(12): 1731–1740, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu:2015:VSE**

[YWQX15] Weikuan Yu, Yandong Wang, Xinyu Que, and Cong Xu. Virtual shuffling for efficient data movement in MapReduce. *IEEE Transactions on Computers*, 64 (2):556–568, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2016:NTC**

[YWW+16] Jun Yang, Qingsong Wei, Chundong Wang, Cheng Chen, Khai Leong Yong, and Bingsheng He. NV-tree: A consistent and workload-adaptive tree structure for non-volatile memory. *IEEE Transactions on Computers*, 65(7):2169–2183, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2012:RWE**

[YWXZ12] Xuejun Yang, Zhiyuan Wang, Jingling Xue, and Yun Zhou. The reliability wall for exascale supercomputing. *IEEE Transactions on Computers*, 61(6):767–779, June 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yin:2016:FSE**

[YXWL16] Y. Yin, L. Xie, J. Wu, and S. Lu. Focus and shoot: Exploring auto-focus in RFID tag identification towards a specified area. *IEEE Transactions on Computers*, 65 (3):888–901, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu:2014:BBM**

[YXZZ14] Hongliang Yu, Xiaojia Xiang, Ying Zhao, and Weimin Zheng. BIRDS: A bare-metal recovery system for instant restoration of data services. *IEEE Transactions on Computers*, 63 (6):1392–1407, June 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2010:EHP**

[YY10] Min Yang and Yuanyuan Yang. An efficient hybrid peer-to-peer system for distributed data sharing. *IEEE Transactions on Computers*, 59(9):1158–1171, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5342410`.

**Yang:2014:ANC**

[YY14] Min Yang and Yuanyuan Yang. Applying network coding to peer-to-peer file sharing. *IEEE Transactions on Computers*, 63(8): 1938–1950, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yoo:2012:AMP**

[YYC12] Junhee Yoo, Sungjoo Yoo, and Kiyoung Choi. Active memory processor for network-on-chip-based architecture. *IEEE Transactions on Computers*, 61(5):622–635, May 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yan:2011:RSA**

[YyHL11] Guihai Yan, yinhe Han, and Xiaowei Li. ReviveNet: a self-adaptive architecture for improving lifetime reliability via localized timing adaptation. *IEEE Transactions on Computers*, 60(9): 1219–1232, September 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5710881`.

**Yun:2016:MBM**

[YYP+16] H. Yun, G. Yao, R. Pellizzoni, M. Caccamo, and L. Sha. Memory bandwidth management for efficient performance isolation in multi-core platforms. *IEEE Transactions on Computers*, 65 (2):562–576, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yao:2016:SAM**

[YYW+16] G. Yao, H. Yun, Z. P. Wu, R. Pellizzoni, M. Caccamo, and L. Sha. Schedulability analysis for memory bandwidth regulated multicore real-time systems. *IEEE Transactions on Computers*, 65(2):601–614, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Ye:2015:SAS**

[YZ15] Dayong Ye and Minjie Zhang. A self-adaptive strategy for evolution of cooperation in distributed networks. *IEEE Transactions on Computers*, 64(4):899–911, April 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yu:2010:LAI**

[YZF+10] Wei Yu, Nan Zhang, Xinwen Fu, R. Bettati, and Wei Zhao. Localization attacks to Internet threat monitors: Modeling and countermeasures. *IEEE Transactions on Computers*, 59(12):1655–1668, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5453347`.

**Yu:2016:FIT**

[YZGG16] S. Yu, W. Zhou, S. Guo, and M. Guo. A feasible IP traceback framework through dynamic deterministic packet marking. *IEEE Transactions on Computers*, 65(5):1418–1427, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yao:2015:OOD**

[YZH+15] H. Yao, D. Zeng, H. Huang, S. Guo, A. Barnawi, and I. Stojmenovic. Opportunistic offloading of deadline-constrained bulk cellular traffic in vehicular DTNs. *IEEE Transactions on Computers*, 64(12):3515–3527, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Yang:2012:DCR**

[YZHX12] Jian Yang, Ke Zeng, Han Hu, and Hongsheng Xi. Dynamic cluster reconfiguration for energy conservation in computation intensive service. *IEEE Transactions on Computers*, 61(10):1401–1416, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zidaric:2019:HOA**

[ZAG19] Nusa Zidaric, Mark Aagaard, and Guang Gong.

Hardware optimizations and analysis for the WG-16 cipher with tower field arithmetic. *IEEE Transactions on Computers*, 68 (1):67–82, January 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8409309/`.

**Zhang:2017:ARF**

[ZBK+17] Hongyan Zhang, Lars Bauer, Michael Andreas Kochte, Eric Schneider, Hans-Joachim Wunderlich, and Jörg Henkel. Aging resilience and fault tolerance in runtime reconfigurable architectures. *IEEE Transactions on Computers*, 66(6):957–970, June 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7588134/`.

**Zhao:2017:GFT**

[ZBW17] Hongzhi Zhao, Nader Bagherzadeh, and Jie Wu. A general fault-tolerant minimal routing for mesh architectures. *IEEE Transactions on Computers*, 66(7):1240–1246, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07814237-abs.html`.

**Zheng:2013:MPC**

[ZC13] Qiang Zheng and Guohong Cao. Minimizing probing cost and achieving identifiability in probe-based network link monitoring. *IEEE Transactions on Computers*, 62(3):510–523, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2014:MRH**

[ZCC+14] Weiping Zhu, Jiannong Cao, Henry C. B. Chan, Xuefeng Liu, and Vaskar Raychoudhury. Mobile RFID with a high identification rate. *IEEE Transactions on Computers*, 63(7):1778–1792, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2019:EMP**

[ZCK19] H. Zhang, D. Chen, and S. Ko. Efficient multiple-precision floating-point fused multiply-add with mixed-precision support. *IEEE Transactions on Computers*, 68(7):1035–1048, July 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:SMS**

[ZCL+16] Detian Zhang, Chi-Yin Chow, Qing Li, Xinming Zhang, and Yinlong Xu. A spatial mashup service for efficient evaluation of concurrent $k$-NN queries. *IEEE Transactions on Comput-*

*ers*, 65(8):2428–2442, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2016:PDA**

[ZCR16] W. Zhu, J. Cao, and M. Raynal. Predicate detection in asynchronous distributed systems: A probabilistic approach. *IEEE Transactions on Computers*, 65 (1):173–186, January 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:DPP**

[ZCS16] W. Zhang, A. M. K. Cheng, and J. Subhlok. DwarfCode: A performance prediction tool for parallel applications. *IEEE Transactions on Computers*, 65 (2):495–507, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2018:ACT**

[ZCW18] Jian Zhang, Shuming Chen, and Yaohua Wang. Advancing CMOS-type Ising arithmetic unit into the domain of real-world applications. *IEEE Transactions on Computers*, 67(5): 604–616, May 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8118124/`.

**Zhu:2016:PBM**

[ZCY+16] Di Zhu, Lizhong Chen, Siyu Yue, Timothy M. Pinkston, and Massoud Pedram. Providing balanced mapping for multiple applications in many-core chip multiprocessors. *IEEE Transactions on Computers*, 65(10): 3122–3135, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2015:AAB**

[ZCYX15] X. Zhu, C. Chen, L. T. Yang, and Y. Xiang. ANGEL: Agent-based scheduling for real-time tasks in virtualized clouds. *IEEE Transactions on Computers*, 64(12): 3389–3403, December 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zabihi:2019:MPS**

[ZCZ+19] M. Zabihi, Z. I. Chowdhury, Z. Zhao, U. R. Karpuzcu, J. Wang, and S. S. Sapatnekar. In-memory processing on the spintronic CRAM: From hardware design to application mapping. *IEEE Transactions on Computers*, 68(8):1159–1173, August 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhai:2016:PPL**

[ZCZL16] Jidong Zhai, Wenguang Chen, Weimin Zheng, and Keqin Li. Performance prediction for large-scale parallel applications using representative replay. *IEEE Transactions on Computers*, 65(7):2184–2198, July 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zeng:2013:EFR**

[ZD13] Haibo Zeng and Marco Di Natale. An efficient formulation of the real-time feasibility region for design optimization. *IEEE Transactions on Computers*, 62(4):644–661, April 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2015:PAL**

[ZDP+15] Xuyun Zhang, Wanchun Dou, Jian Pei, S. Nepal, Chi Yang, Chang Liu, and Jinjun Chen. Proximity-aware local-recoding anonymization with MapReduce for scalable big data privacy preservation in cloud. *IEEE Transactions on Computers*, 64(8):2293–2307, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhao:2013:PVA**

[ZDYZ13] Bo Zhao, Yu Du, Jun Yang, and Youtao Zhang. Process variation-aware nonuniform cache management in a 3D die-stacked multicore processor. *IEEE Transactions on Computers*, 62(11):2252–2265, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See errata [ZDYZ14].

**Zhao:2014:EPV**

[ZDYZ14] Bo Zhao, Yu Du, Jun Yang, and Youtao Zhang. Errata to "Process Variation-Aware Nonuniform Cache Management in a 3D Die–Stacked Multicore Processor. *IEEE Transactions on Computers*, 63(2):525–526, February 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). See [ZDYZ13].

**Zhang:2019:OFS**

[ZFHC19] Xiaoyi Zhang, Dan Feng, Yu Hua, and Jianxi Chen. Optimizing file systems with a write-efficient journaling scheme on non-volatile memory. *IEEE Transactions on Computers*, 68 (3):402–413, March 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8466031/`.

**Zhang:2017:FAE**

[ZFJ+17] Yucheng Zhang, Dan Feng, Hong Jiang, Wen Xia, Min Fu, Fangting Huang, and Yukun Zhou. A fast asymmetric extremum content defined chunking algorithm for data deduplication in backup storage systems. *IEEE Transactions on Computers*, 66(2):199–211, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zeng:2015:ISM**

[ZGB+15] Deze Zeng, Song Guo, A. Barnawi, Shui Yu, and I. Stojmenovic. An improved stochastic modeling of opportunistic routing in vehicular CPS. *IEEE Transactions on Computers*, 64 (7):1819–1829, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zeng:2016:JOT**

[ZGG+16] Deze Zeng, Lin Gu, Song Guo, Zixue Cheng, and Shui Yu. Joint optimization of task scheduling and image placement in fog computing supported software-defined embedded system. *IEEE Transactions on Computers*, 65(12):3702–3712, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zang:2013:STL**

[ZGR13] Wei Zang and Ann Gordon-Ross. T-SPaCS — a two-level single-pass cache simulation methodology. *IEEE Transactions on Computers*, 62(2):390–403, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2014:RDS**

[ZGW14] Qiang Zhu, Guodong Guo, and Dajin Wang. Relating diagnosability, strong diagnosability and conditional diagnosability of strong networks. *IEEE Transactions on Computers*, 63(7):1847–1851, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2015:SSP**

[ZGWC15] Zhenzhe Zheng, Yang Gui, Fan Wu, and Guihai Chen. STAR: Strategy-proof double auctions for multi-cloud, multi-tenant bandwidth reservation. *IEEE Transactions on Computers*, 64(7):2071–2083, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2013:EAA**

[ZGY13] Zhemin Zhang, Zhiyang Guo, and Yuanyuan Yang. Efficient all-to-all broadcast in Gaussian on-chip networks. *IEEE Transactions on Computers*, 62(10): 1959–1971, October 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2014:BRO**

[ZGY14] Zhemin Zhang, Zhiyang Guo, and Yuanyuan Yang. Bufferless routing in optical Gaussian macrochip interconnect. *IEEE Transactions on Computers*, 63(11): 2685–2700, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2010:OWS**

[Zhe10] S. Q. Zheng. An optimal wide-sense nonblocking distributor. *IEEE Transactions on Computers*, 59(12): 1709–1714, December 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5483291`.

**Zhang:2014:BMC**

[ZHM14] Baoxian Zhang, Jie Hao, and Hussein T. Mouftah. Bidirectional multi-constrained routing algorithms. *IEEE Transactions on Computers*, 63(9):2174–2186, September 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2019:IAM**

[ZHM+19] J. Zhou, X. S. Hu, Y. Ma, J. Sun, T. Wei, and S. Hu. Improving availability of multicore real-time systems suffering both permanent and transient faults. *IEEE Transactions on Computers*, 68(12):1785–1801, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2019:PPA**

[ZHQX19] Panping Zhou, Jianzhong Huang, Xiao Qin, and Changsheng Xie. PaRS: A popularity-aware redundancy scheme for in-memory stores. *IEEE Transactions on Computers*, 68 (4):556–569, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8502118/`.

**Zhou:2015:EPP**

[ZHW15] Zhibin Zhou, Dijiang Huang, and Zhijie Wang. Efficient privacy-preserving ciphertext-policy attribute based-encryption and broadcast encryption. *IEEE Transactions on Computers*, 64(1):126–138, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:PPV**

[ZHW+16] Lei Zhang, Chuanyan Hu, Qianhong Wu, Josep Domingo-Ferrer, and Bo Qin. Privacy-preserving vehicular communication authentication with hierarchical aggregation and fast response. *IEEE Transactions on Computers*, 65 (8):2562–2574, August 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zadegan:2012:ATA**

[ZICL12] Farrokh Ghani Zadegan, Urban Ingelsson, Gunnar Carlsson, and Erik Larsson. Access time analysis for IEEE P1687. *IEEE Transactions on Computers*, 61(10): 1459–1472, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2014:SAP**

[ZJH+14] Hehua Zhang, Yu Jiang, W. N. N. Hung, Xiaoyu Song, Ming Gu, and Jiaguang Sun. Symbolic analysis of programmable logic controllers. *IEEE Transactions on Computers*, 63(10): 2563–2575, October 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:FTO**

[ZJL+16] H. Zhang, H. Jiang, B. Li, F. Liu, A. V. Vasilakos, and J. Liu. A framework for truthful online auctions in cloud computing with heterogeneous user demands. *IEEE Transactions on Computers*, 65(3):805–818, March 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhan:2014:CCO**

[ZJS14] Dongyuan Zhan, Hong Jiang, and Sharad C. Seth. CLU: Co-optimizing locality and utility in thread-aware capacity management for shared last level caches. *IEEE Transactions on Computers*, 63(7):1656–1667, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2011:OPV**

[ZJXL11] Jun Zhu, Zhefu Jiang, Zhen Xiao, and Xiaoming Li. Optimizing the performance of virtual machine synchronization for fault tolerance. *IEEE Transactions on Computers*, 60(12): 1718–1729, December 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5629326`.

**Zhong:2011:SSS**

[ZL11] Zhenyu Zhong and Kang Li. Speed up statistical spam filter by approximation. *IEEE Transactions on Computers*, 60(1):120–134, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2015:SOF**

[ZL15] Zibin Zheng and M. R. Lyu. Selecting an optimal fault tolerance strategy for reliable service-oriented systems with local and global constraints. *IEEE Transactions on Computers*, 64 (1):219–232, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:WAS**

[ZL16] Qi Zhang and Ling Liu. Workload adaptive shared memory management for high performance network I/O in virtualized cloud. *IEEE Transactions on Computers*, 65(11):3480–3494, November 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2018:DIV**

[ZL18] Tianwei Zhang and Ruby B. Lee. Design, implementation and verification of cloud architecture for monitoring a virtual machine's security health. *IEEE Transactions on Computers*, 67(6):799–815, June 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8169039/`.

**Zhang:2019:CCF**

[ZL19] X. Zhang and Y. Lao. On the construction of composite finite fields for hardware obfuscation. *IEEE Transactions on Computers*, 68(9): 1353–1364, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zahaf:2019:PMM**

[ZLBB19] H. Zahaf, G. Lipari, M. Bertogna, and P. Boulet. The parallel multi-mode digraph task model for energy-aware real-time heterogeneous multi-core systems. *IEEE Transactions on Computers*, 68(10): 1511–1524, October 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zeng:2015:EMM**

[ZLG+15] D. Zeng, P. Li, S. Guo, T. Miyazaki, J. Hu, and Y. Xiang. Energy minimization in multi-task software-defined sensor networks. *IEEE Transactions on Computers*, 64(11):3128–3139, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2015:QCC**

[ZLH+15] Jiantao Zhou, Caihe Lan, W. N. N. Hung, Xinrui Guo, and Xiaoyu Song. A quantitative characterization of cross coverage. *IEEE Transactions on Computers*, 64 (8):2408–2414, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2017:HEA**

[ZLJ+17] Y. Zhang, X. Liao, H. Jin, L. Gu, G. Tan, and B. B. Zhou. HotGraph: Efficient asynchronous processing for real-world graphs. *IEEE Transactions on Computers*, 66(5):799–809, May 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2015:BDR**

[ZLLX15] Yunfeng Zhu, Jian Lin, P. P. C. Lee, and Yinlong Xu. Boosting degraded reads in heterogeneous erasure-coded storage systems. *IEEE Transactions on Computers*, 64 (8):2145–2157, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2011:SPM**

[ZLN11] Kai Zheng, Hongbin Lu, and Erich Nahum. Scalable pattern matching on multicore platform via dynamic differentiated distributed detection ($D^4$). *IEEE Transactions on Computers*, 60 (3):346–359, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2017:MSM**

[ZLSI17] Qi Zhang, Ling Liu, Gong Su, and Arun Iyengar. MemFlex: A shared memory swapper for high performance VM execution. *IEEE Transactions on Computers*, 66(9):1645–1652, September 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7885535/`.

**Zhang:2017:RTO**

[ZLW+17] Xuebin Zhang, Jiangpeng Li, Hao Wang, Danni Xiong, Jerry Qu, Hyunsuk Shin, Jung Pill Kim, and Tong Zhang. Realizing transparent OS/apps compression in mobile devices at zero latency overhead. *IEEE Transactions on Computers*, 66 (7):1188–1199, July 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://www.computer.org/csdl/trans/tc/2017/07/07843601-abs.html`.

**Zhang:2015:ARR**

[ZLWZ15] Guangyan Zhang, Keqin Li, Jingzhe Wang, and Weimin

Zheng. Accelerate RDP RAID-6 scaling by reducing disk I/Os and XOR operations. *IEEE Transactions on Computers*, 64 (1):32–44, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:PPR**

[ZLX+16] W. Zhang, Y. Lin, S. Xiao, J. Wu, and S. Zhou. Privacy preserving ranked multi-keyword search for multiple data owners in cloud computing. *IEEE Transactions on Computers*, 65 (5):1566–1577, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2015:DSG**

[ZLXW15] Shuming Zhou, Limei Lin, Li Xu, and Dajin Wang. The $t/k$-diagnosability of star graph networks. *IEEE Transactions on Computers*, 64(2):547–555, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2015:TSP**

[ZLY15] Gaofeng Zhang, Xiao Liu, and Yun Yang. Time-series pattern based effective noise generation for privacy protection on cloud. *IEEE Transactions on Computers*, 64(5):1456–1469, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2015:CLB**

[ZLYS15] Chunsheng Zhu, V. C. M. Leung, L. T. Yang, and Lei Shu. Collaborative location-based sleep scheduling for wireless sensor networks integrated with mobile cloud computing. *IEEE Transactions on Computers*, 64 (7):1844–1856, July 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2010:CNA**

[ZM10] Gang Zhou and H. Michalik. Comments on "A New Architecture for a Parallel Finite Field Multiplier with Low Complexity Based on Composite Field". *IEEE Transactions on Computers*, 59(7):1007–1008, July 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5416689`.

**Zheng:2017:HFA**

[ZM17] Nan Zheng and Pinaki Mazumder. Hardware-friendly actor-critic reinforcement learning through modulation of spike-timing-dependent plasticity. *IEEE Transactions on Computers*, 66(2):299–311, February 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zaccaria:2018:SFH**

[ZMB18] V. Zaccaria, F. Melzani, and G. Bertoni. Spectral features of higher-order side-channel countermeasures. *IEEE Transactions on Computers*, 67(4):596–603, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL http://ieeexplore.ieee.org/document/8103813/.

**Zhang:2013:BAF**

[ZMR+13] Yiwei Zhang, Joseph P. Mcgeehan, Edward M. Regan, Stephen Kelly, and Jose Luis Nunez-Yanez. Biophysically accurate floating point neuroprocessors for reconfigurable logic. *IEEE Transactions on Computers*, 62(3):599–608, March 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zong:2011:EPT**

[ZMRQ11] Ziliang Zong, Adam Manzanares, Xiaojun Ruan, and Xiao Qin. EAD and PEBD: Two energy-aware duplication scheduling algorithms for parallel tasks on homogeneous clusters. *IEEE Transactions on Computers*, 60(3):360–374, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zilic:2013:GEI**

[ZMS13] Zeljko Zilic, Prabhat Mishra, and Sandeep K. Shukla. Guest Editors' introduction: Special section on system-level design and validation of heterogeneous chip multiprocessors. *IEEE Transactions on Computers*, 62 (2):209–210, February 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2015:TSR**

[ZMW15] W. Zheng, K. Ma, and X. Wang. TE-Shave: Reducing data center capital and operating expenses with thermal energy storage. *IEEE Transactions on Computers*, 64(11):3278–3292, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhao:2019:CXC**

[ZMW+19] X. Zhao, S. Ma, Z. Wang, N. E. Jerger, and L. Eeckhout. CD-Xbar: A converge-diverge crossbar network for high-performance GPUs. *IEEE Transactions on Computers*, 68(9):1283–1296, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhao:2011:EDG**

[ZMY11] Miao Zhao, Ming Ma, and Yuanyuan Yang. Efficient data gathering with mobile collectors and space-division multiple access technique in wireless sensor networks. *IEEE Transactions on Computers*, 60(3):400–417, March 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zadegan:2018:CFM**

[ZNL18] Farrokh Ghani Zadegan, Dimitar Nikolov, and Erik Larsson. On-chip fault monitoring using self-reconfiguring IEEE 1687 networks. *IEEE Transactions on Computers*, 67(2):237–251, February 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/7990262/`.

**Zhou:2013:EPR**

[ZOD13] Bo Zhou, Hiroyuki Okamura, and Tadashi Dohi. Enhancing performance of random testing through Markov Chain Monte Carlo methods. *IEEE Transactions on Computers*, 62(1):186–192, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhao:2019:SMB**

[ZOK+19] Yinglin Zhao, Peng Ouyang, Wang Kang, Shouyi Yin, Youguang Zhang, Shaojun Wei, and Weisheng Zhao. An STT-MRAM based in memory architecture for low power integral computing. *IEEE Transactions on Computers*, 68(4):617–623, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8523808/`.

**Zomaya:2011:SJb**

[Zom11a] Albert Zomaya. State of the journal. *IEEE Transactions on Computers*, 60(5):609–613, May 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zomaya:2011:SJa**

[Zom11b] Albert Y. Zomaya. State of the journal. *IEEE Transactions on Computers*, 60(1):2, January 2011. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zomaya:2012:MEB**

[Zom12a] Albert Y. Zomaya. A message to the Editorial Board. *IEEE Transactions on Computers*, 61(12):1665, December 2012. CODEN ITCOB4. ISSN 0018-9340

(print), 1557-9956 (electronic).

**Zomaya:2012:SJa**

[Zom12b] Albert Y. Zomaya. State of the journal. *IEEE Transactions on Computers*, 61(1): 1–2, January 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zomaya:2012:SJb**

[Zom12c] Albert Y. Zomaya. State of the journal. *IEEE Transactions on Computers*, 61 (2):145–150, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zomaya:2013:SJ**

[Zom13] Albert Y. Zomaya. State of the journal. *IEEE Transactions on Computers*, 62(1): 1–5, January 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zomaya:2015:FSJ**

[Zom15a] A. Y. Zomaya. Farewell state of the journal editorial. *IEEE Transactions on Computers*, 64(1):1, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zomaya:2015:MIS**

[Zom15b] A. Y. Zomaya. In memoriam [Ivan Stojmenovic]. *IEEE Transactions on Computers*, 64(1):3, January 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zotov:2010:DVB**

[Zot10] I. V. Zotov. Distributed virtual bit-slice synchronizer: a scalable hardware barrier mechanism for $n$-dimensional meshes. *IEEE Transactions on Computers*, 59(9):1187–1199, September 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5396324`.

**Zavattoni:2015:SIA**

[ZPM+15] E. Zavattoni, L. J. Dominguez Perez, S. Mitsunari, A. H. Sanchez-Ramirez, T. Teruya, and F. Rodriguez-Henriquez. Software implementation of an attribute-based encryption scheme. *IEEE Transactions on Computers*, 64 (5):1429–1441, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2011:QAF**

[ZQQ11] Xiaomin Zhu, Xiao Qin, and Meikang Qiu. QoS-aware fault-tolerant scheduling for real-time tasks on heterogeneous clusters. *IEEE Transactions on Computers*, 60(6):800–812, June 2011.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2019:SCS**

[ZQZ+19] X. Zhang, Z. Qian, S. Zhang, X. Li, X. Wang, and S. Lu. Semi-clairvoyant scheduling in data analytics systems. *IEEE Transactions on Computers*, 68(9): 1376–1389, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhan:2015:PBT**

[ZR15a] Xin Zhan and S. Reda. Power budgeting techniques for data centers. *IEEE Transactions on Computers*, 64(8):2267–2278, August 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2015:SDL**

[ZR15b] Z. Zhang and T. G. Robertazzi. Scheduling divisible loads in Gaussian, mesh and torus network of processors. *IEEE Transactions on Computers*, 64(11): 3249–3264, November 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhong:2015:DPM**

[ZRL15] Ziming Zhong, V. Rychkov, and A. Lastovetsky. Data partitioning on multicore and multi-GPU platforms using functional performance models. *IEEE Transactions on Computers*, 64(9): 2506–2518, September 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:GDL**

[ZRS+16] Jiao Zhang, Fengyuan Ren, Ran Shu, Tao Huang, and Yunjie Liu. Guaranteeing delay of live virtual machine migration by determining and provisioning appropriate bandwidth. *IEEE Transactions on Computers*, 65(9): 2910–2917, September 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zha:2013:GGH**

[ZS13] Xinyan Zha and Sartaj Sahni. GPU-to-GPU and host-to-host multipattern string matching on a GPU. *IEEE Transactions on Computers*, 62(6):1156–1169, June 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zanon:2019:FKC**

[ZSP+19] G. H. M. Zanon, M. A. Simplicio, G. C. C. F. Pereira, J. Doliskani, and P. S. L. M. Barreto. Faster key compression for isogeny-based cryptosystems. *IEEE Transactions on Computers*, 68(5):688–701, May 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2015:ASP**

[ZT15] Hanying Zheng and Xueyan Tang. Analysis of server provisioning for distributed interactive applications. *IEEE Transactions on Computers*, 64(10):2752–2766, October 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2014:DMA**

[ZV14] Qin Zheng and Bharadwaj Veeravalli. On the design of mutually aware optimal pricing and load balancing strategies for grid computing systems. *IEEE Transactions on Computers*, 63 (7):1802–1811, July 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2013:REB**

[ZWC13] Qiang Zhu, Xin-Ke Wang, and Guanglan Cheng. Reliability evaluation of BC networks. *IEEE Transactions on Computers*, 62(11): 2337–2340, November 2013. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2018:DSS**

[ZWC+18] Yu Zhang, Qingsong Wei, Cheng Chen, Mingdi Xue, Xinkun Yuan, and Chundong Wang. Dynamic scheduling with service curve for QoS guarantee of large-scale cloud storage. *IEEE Transactions on Computers*, 67(4):457–468, April 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/document/8107532/`.

**Zheng:2016:BDD**

[ZWD+16] X. Zheng, J. Wang, W. Dong, Y. He, and Y. Liu. Bulk data dissemination in wireless sensor networks: Analysis, implications and improvement. *IEEE Transactions on Computers*, 65 (5):1428–1439, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2015:AAD**

[ZWH+15] Tao Zhang, Jianxin Wang, Jiawei Huang, Yi Huang, Jianer Chen, and Yi Pan. Adaptive-acceleration data center TCP. *IEEE Transactions on Computers*, 64 (6):1522–1533, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2015:CMC**

[ZWL15] Sheng Zhang, Jie Wu, and Sanglu Lu. Collaborative mobile charging. *IEEE Transactions on Computers*, 64(3):654–667, March 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2019:FCS**

[ZWL+19] Q. Zhou, K. Wang, P. Li, D. Zeng, S. Guo, B. Ye, and M. Guo. Fast coflow scheduling via traffic compression and stage pipelining in datacenter networks. *IEEE Transactions on Computers*, 68(12):1755–1771, December 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2015:NOP**

[ZWLS15] Baoxian Zhang, Xili Wan, Junzhou Luo, and Xiaojun Shen. A nearly optimal packet scheduling algorithm for input queued switches with deadline guarantees. *IEEE Transactions on Computers*, 64(6):1548–1563, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:CEC**

[ZWW+16] G. Zhang, G. Wu, S. Wang, J. Shu, W. Zheng, and K. Li. CaCo: An efficient Cauchy coding approach for cloud storage systems. *IEEE Transactions on Computers*, 65(2):435–447, February 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2019:ADL**

[ZWW19] Jian Zhou, Huafeng Wu, and Jun Wang. ApproxSSD: Data layout aware sampling on an array of SSDs. *IEEE Transactions on Computers*, 68(4):471–483, April 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8467988/`.

**Zhang:2012:NPM**

[ZWX12] Ziyi Zhang, Xin Wang, and Qin Xin. A new performance metric for construction of robust and efficient wireless backbone network. *IEEE Transactions on Computers*, 61(10):1495–1506, October 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2015:SPR**

[ZWYY15] Jin-Xin Zhou, Zhen-Lin Wu, Shi-Chen Yang, and Kui-Wu Yuan. Symmetric property and reliability of balanced hypercube. *IEEE Transactions on Computers*, 64(3):876–881, March 2015.

CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhu:2014:SDF**

[ZXX+14] Yunfeng Zhu, Liping Xiang, Yinlong Xu, John C. S. Lui, Patrick P. C. Lee, Runhui Li, and Silei Xu. Single disk failure recovery for X-code-based parallel storage systems. *IEEE Transactions on Computers*, 63(4):995–1007, April 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhao:2012:BRH**

[ZY12] Miao Zhao and Yuanyuan Yang. Bounded relay hop mobile data gathering in wireless sensor networks. *IEEE Transactions on Computers*, 61(2):265–277, February 2012. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:PPD**

[ZYC16] Q. Zhang, L. T. Yang, and Z. Chen. Privacy preserving deep computation model on cloud for big data feature learning. *IEEE Transactions on Computers*, 65 (5):1351–1362, May 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:CAE**

[ZYHZ16] Yan Zhang, Guowu Yang, William N. N. Hung, and Juling Zhang. Computing affine equivalence classes of Boolean functions by group isomorphism. *IEEE Transactions on Computers*, 65 (12):3606–3616, December 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2015:RAS**

[ZYL15] Ziming Zheng, Li Yu, and Zhiling Lan. Reliability-aware speedup models for parallel applications with coordinated checkpointing/ restart. *IEEE Transactions on Computers*, 64(5):1402–1415, May 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2016:HSC**

[ZYW+16] Weihua Zhang, Shiqiang Yu, Haojun Wang, Zhuofang Dai, and Haibo Chen. Hardware support for concurrent detection of multiple concurrency bugs on fused CPU–GPU architectures. *IEEE Transactions on Computers*, 65(10):3083–3095, October 2016. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2010:MMC**

[ZYY10] Hao Zheng, Haiqiong Yao, and T. Yoneda. Modular model checking of large asynchronous designs with efficient abstraction refinement. *IEEE Transactions on Computers*, 59(4):561–573, April 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5374376`.

**Zhou:2018:RIB**

[ZYY18] Quan Zhou, Liang Yang, and Xin Yan. Reconfigurable instruction-based multicore parallel convolution and its application in real-time template matching. *IEEE Transactions on Computers*, 67(12):1780–1793, December 2018. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `https://ieeexplore.ieee.org/document/8375740/`.

**Zheng:2010:PPT**

[ZZ10] Hongzhong Zheng and Zhichun Zhu. Power and performance trade-offs in contemporary DRAM system designs for multicore processors. *IEEE Transactions on Computers*, 59(8):1033–1046, August 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5467050`.

**Zarrinchian:2017:LBS**

[ZZ17] Ghobad Zarrinchian and Morteza Saheb Zamani. Latch-based structure: A high resolution and self-reference technique for hardware Trojan detection. *IEEE Transactions on Computers*, 66(1):100–113, January 2017. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhao:2019:CUC**

[ZZ19] Y. Zhao and H. Zeng. The concept of unschedulability core for optimizing real-time systems with fixed-priority scheduling. *IEEE Transactions on Computers*, 68(6):926–938, June 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2019:QDA**

[ZZJ+19] M. Zhang, L. Zhang, L. Jiang, F. T. Chong, and Z. Liu. Quick-and-Dirty: An architecture for high-performance temporary short writes in MLC PCM. *IEEE Transactions on Computers*, 68(9):1365–1375, September 2019. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2014:RRD**

[ZZL14] Guangyan Zhang, Weimin Zheng, and Keqin Li. Rethinking RAID-5 data layout for better scalability. *IEEE Transactions on Computers*, 63(11):2816–2828, November 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zheng:2015:CMC**

[ZZM+15] Hao Zheng, Zhen Zhang, C. J. Myers, E. Rodriguez, and Yingying Zhang. Compositional model checking of concurrent systems. *IEEE Transactions on Computers*, 64(6):1607–1621, June 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhang:2010:AND**

[ZZS10] Guangyan Zhang, Weiman Zheng, and Jiwu Shu. ALV: a new data redistribution approach to RAID-5 scaling. *IEEE Transactions on Computers*, 59(3):345–357, March 2010. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic). URL `http://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=5276795`.

**Zhang:2015:NPL**

[ZZX+15] Daqiang Zhang, Daqing Zhang, Haoyi Xiong, L. T. Yang, and V. Gauthier. NextCell: Predicting location using social interplay from cell phone traces. *IEEE Transactions on Computers*, 64(2):452–463, February 2015. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).

**Zhou:2014:TEP**

[ZZYZ14] Ping Zhou, Bo Zhao, Jun Yang, and Youtao Zhang. Throughput enhancement for phase change memories. *IEEE Transactions on Computers*, 63(8):2080–2093, August 2014. CODEN ITCOB4. ISSN 0018-9340 (print), 1557-9956 (electronic).